

Vishay Semiconductors

IR Receiver Modules for Remote Control Systems



LINKS TO ADDITIONAL RESOURCES



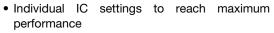


DESCRIPTION

This IR receiver series is optimized for short burst remote control systems in different environments. The customer can chose between different IC settings (AGC variants), to find the optimum solution for his application. The higher the AGC, the better noise is suppressed, but the lower the code compatibility.

The devices contain a PIN diode and a preamplifier assembled on a lead frame. The epoxy package contains an IR filter. The demodulated output signal can be directly connected to a microprocessor for decoding. These components have not been qualified to automotive specifications.

FEATURES





- Immunity against noise (lamps, LCD TV, Wi-Fi)
- · Low supply current
- · Photo detector and preamplifier in one package
- Supply voltage: 2.0 V to 5.5 V
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



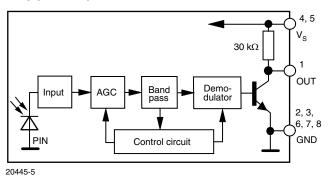
GREEN

(5-2008)

DESIGN SUPPORT TOOLS

- 3D models
- Window size calculator

BLOCK DIAGRAM



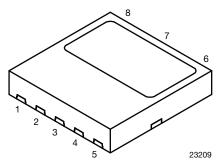


Vishay Semiconductors

MECHANICAL DATA

Pinning:

 $6, 8 = GND, 5 = V_S, 1 = OUT$

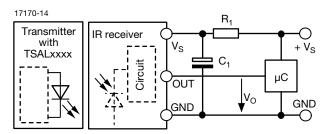


ORDERING CODE

Taping:

TSOP37...TT1 - top view taped, 1800 pcs/reel

APPLICATION CIRCUIT



 ${\rm R}_{\rm 1}$ and ${\rm C}_{\rm 1}$ recommended in case there are strong ripple or spikes on the supply line.

PARTS TA	BLE			
AGC		NOISY ENVIRONMENTS AND SHORT BURSTS (AGC3)	VERY NOISY ENVIRONMENTS AND SHORT BURSTS (AGC5)	
	33 kHz	TSOP37333	TSOP37533	
Carrier frequency	36 kHz	TSOP37336 (1)(2)	TSOP37536	
	38 kHz	TSOP37338 (3)(4)(5)	TSOP37538	
	40 kHz	TSOP37340	TSOP37540	
	56 kHz	TSOP37356	TSOP37556	
Package		Belobog		
Pinning		1 = OUT, 2, 3, 6, 7, 8 = GND, 4, 5 = V _S		
Dimensions (mm)		3.95 W x 3.95 H x 0.8 D		
Mounting		SMD		
Application		Remote control		
Best choice for		(1) MCIR (2) RCMM (3) RECS-80 Code (4) r-map (5) XMP		
Special options • Extended temperature range: www.vishay.com/doc?82738		loc?82738		

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		V _S	-0.3 to +6	V
Supply current		I _S	3	mA
Output voltage		Vo	-0.3 to (V _S + 0.3)	V
Output current		I _O	5	mA
Junction temperature		Tj	100	°C
Storage temperature range		T _{stg}	-25 to +85	°C
Operating temperature range		T _{amb}	-25 to +85	°C
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW

Note

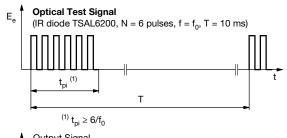
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.



Vishay	Semiconductors
--------	----------------

ELECTRICAL AND OPTI	L AND OPTICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		Vs	2.0	-	5.5	V
Supply current	$V_S = 3.3 \text{ V}, E_V = 0$	I _{SD}	0.25	0.35	0.45	0.45 mA
Supply current	$E_v = 40$ klx, sunlight	I _{SH}	-	0.45	-	mA
Transmission distance	$E_V = 0$, IR diode TSAL6200, $I_F = 50$ mA, test signal see Fig. 1	d	-	15	-	m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see Fig. 1	V _{OSL}	-	-	100	mV
Minimum irradiance	Test signal: RC5 code	E _{e min.}	-	0.25	0.5	mW/m ²
Minimum irradiance	Test signal: XMP code	E _{e min.}	-	0.3	0.6	mW/m ²
Maximum irradiance	t_{pi} - $3/f_o$ < t_{po} < t_{pi} + $3.5/f_o$, test signal see Fig. 1	E _{e max.}	30	-	-	W/m ²
Directivity	Angle of half transmission distance	Ψ1/2	-	± 75	-	٥

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)



www.vishay.com

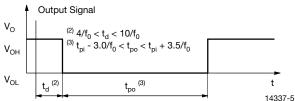


Fig. 1 - Output Function

0.4

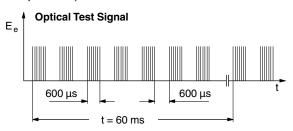
0.3

0.2

0.1

0.0

t_{po} - Output Pulse Width (ms)



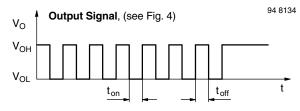


Fig. 3 - Output Function

Output pulse width

Input burst length $\lambda = 950 \text{ nm}$,
optical test signal, Fig. 1

0.1 10 1000 100 000 E_e - Irradiance (mW/m²)

Fig. 2 - Output Pulse Width vs. Irradiance

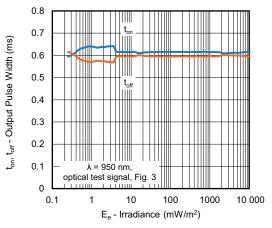


Fig. 4 - Output Pulse Diagram

Vishay Semiconductors

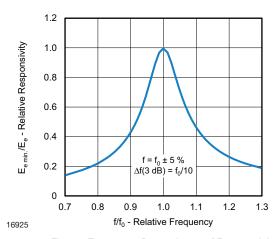


Fig. 5 - Frequency Dependance of Responsivity

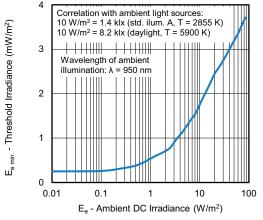


Fig. 6 - Sensitivity in Bright Ambient

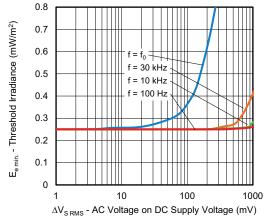


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

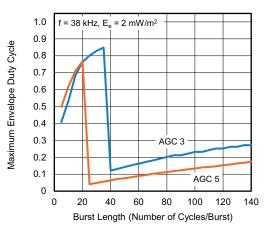


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

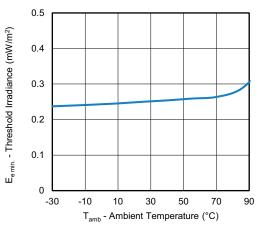


Fig. 9 - Sensitivity vs. Ambient Temperature

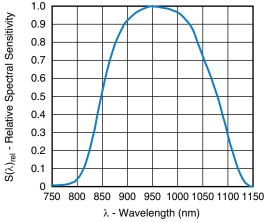


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength



Vishay Semiconductors

www.vishay.com

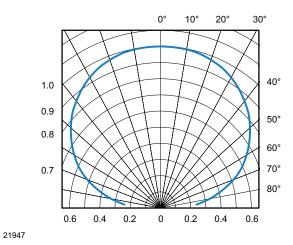


Fig. 11 - Directivity

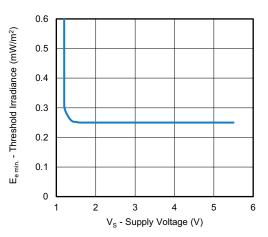


Fig. 12 - Sensitivity vs. Supply Voltage



Vishay Semiconductors

SUITABLE DATA FORMAT

The TSOP373.., TSOP375.. series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP373.., TSOP375.. in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output. Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see Fig. 13 or Fig. 14)

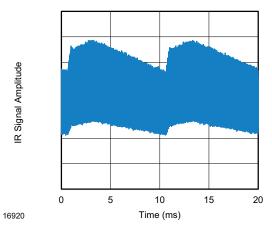


Fig. 13 - IR Signal from Fluorescent Lamp With Low Modulation

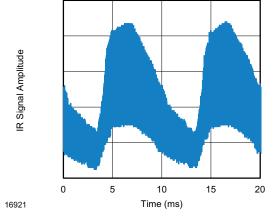


Fig. 14 - IR Signal from Fluorescent Lamp With High Modulation

	TSOP373	TSOP375
Minimum burst length	6 cycles/burst	6 cycles/burst
After each burst of length a minimum gap time is required of	6 to 35 cycles ≥ 10 cycles	6 to 20 cycles ≥ 10 cycles
For bursts greater than	35 cycles	20 cycles
a minimum gap time in the data stream is needed of	> 9 x burst length	> 25 x burst length
Maximum number of continuous short bursts/second	2000	2000
MCIR code	Preferred	No
XMP code	Preferred	Yes
RECS-80 code	Preferred	Yes
RCMM code	Preferred	Yes
r-map code	Preferred	Yes
Suppression of interference from fluorescent lamps	Fig. 13	Fig. 13 and Fig. 14

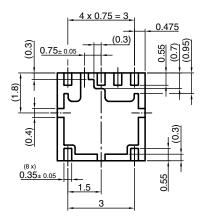
Note

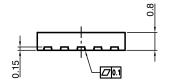
• For data formats with long bursts (more than 10 carrier cycles) please see the datasheet for TSOP372.., TSOP374..

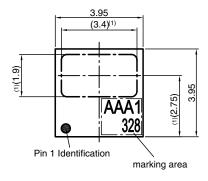


Vishay Semiconductors

PACKAGE DIMENSIONS in millimeters



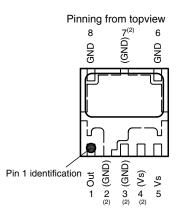




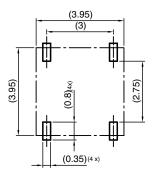
Drawing-No.: 6.550-5315.01-4 Issue: 2; 12.02.14

Not indicated tolerances ± 0.1 technical drawings according to DIN

specifications



Proposed pad layout from component side (dim. for reference only)



Notes

(1) Optically effective area

(2) Pins connected internally. It is not necessary to connect externally



Vishay Semiconductors

ASSEMBLY INSTRUCTIONS

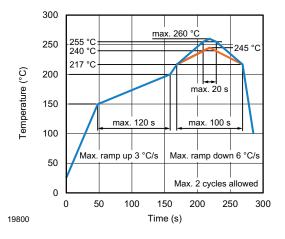
Reflow Soldering

- Reflow soldering must be done within 168 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

Manual Soldering

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off

VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE

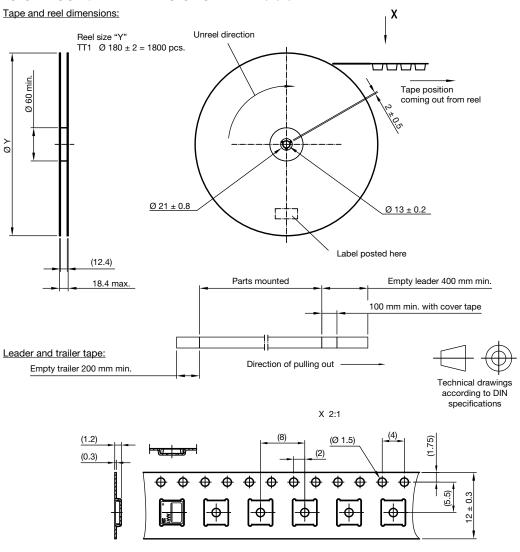




Vishay Semiconductors

TAPING VERSION TSOP37... DIMENSIONS in millimeters

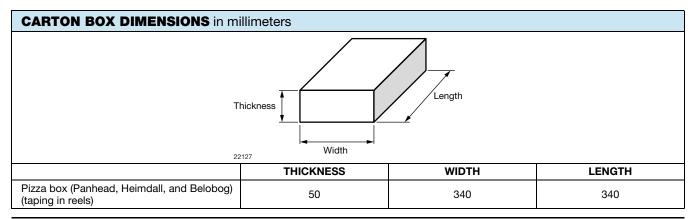
www.vishay.com



Drawing-No.: 9.700-5347.01-4 Issue: 2; 07.03.18 Not indicated tolerances ± 0.1

OUTER PACKAGING

The sealed reel is packed into a pizza box.





Vishay Semiconductors

LABEL

Standard bar code labels for finished goods

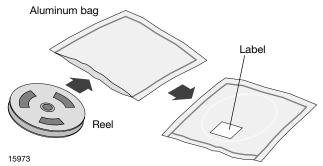
www.vishay.com

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

PLAIN WRITING	ABBREVIATION	LENGTH	
Item-description	-	18	
Item-number	INO	8	
Selection-code	SEL	3	
LOT-/serial-number	BATCH	10	
Data-code	COD	3 (YWW)	
Plant-code	PTC	2	
Quantity	QTY	8	
Accepted by	ACC	-	
Packed by	PCK	-	
Mixed code indicator	MIXED CODE	-	
Origin	xxxxxx+	Company logo	
Long bar code top	Туре	Length	
Item-number	N	8	
Plant-code	N	2	
Sequence-number	X	3	
Quantity	N	8	
Total length	-	21	
Short bar code bottom	Туре	Length	
Selection-code	X	3	
Data-code	N	3	
Batch-number	X	10	
Filter	-	1	
Total length	-	17	

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 168 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 $^{\circ}$ C + 5 $^{\circ}$ C / - 0 $^{\circ}$ C and < 5 $^{\circ}$ RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC® standard J-STD-020 level 3 label is included on all dry bags.



22650

TSOP373.., TSOP375..

Vishay Semiconductors

www.vishay.com

Caution This bag contains MOISTURE-SENSITIVE DEVICES 1. Calculated shelf life in sealed bag: 12 months at $<\!40^{\circ}\text{C}$ and $<\!90\%$ relative humidity (RH) 3. After bag is opened, devices that will be subjected to reflow solder or other high temperature process must be 168 a) Mounted within: $\frac{168}{\text{If blank, see adjacent bar code label}}$ hours of factory conditions ≤30°C/60% RH, or b) Stored per J-STD-033 4. Devices require bake, before mounting, if: a) Humidity Indicator Card reads > 10% for level 2a - 5a devices or $>\!60\%$ for level 2 devices when read at $23{\pm}5^{\circ}{\rm C}$ b) 3a or 3b are not met 5. If baking is required, refer to IPC/JEDEC J-STD-033 for bake procedure If blank, see adjacent bar code label Note: Level and body temperature defined by IPC/JEDEC J-STD-020 $\,$

EIA JEDC standard J-STD-020 level 3 label is included on all dry bags

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

BAR CODE PRODUCT LABEL (example)



22178



Legal Disclaimer Notice

Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2024 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED