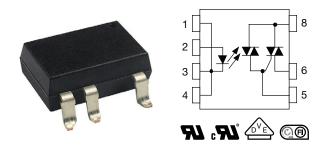


# **Optocoupler, Power Phototriac**



### **LINKS TO ADDITIONAL RESOURCES**





### **DESCRIPTION**

The VO2223 is an optically coupled phototriac driving an integrated power TRIAC in a DIP-8 package. Featuring galvanic and electrical noise isolation, the VO2223 is able to directly drive medium AC loads with a low voltage input signal. The high blocking voltage of 600 V permits control of off-line voltages up to 230  $V_{\rm AC}$  and is sufficient for as much as 380  $V_{\rm AC}$ .

### **FEATURES**

- Fully integrated power TRIAC
- Peak off-state voltage 600 V
- Load current 0.9 A<sub>RMS</sub>
- dV/dt of 210 V/μs
- DIP-8 package
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

# Pb-free





#### **APPLICATIONS**

- Air conditioners
- · Microwave ovens
- · Washing machines
- Refrigerators
- · Fan heaters
- Inductive heating cooker
- Water heaters
- · Industrial equipments

## **AGENCY APPROVALS**

- UL 1577
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- FIMKO

ORDERING INFORMATION					
V O 2	2 2 3 - X 0 0 #  MBER PACKAGE OPTION				
AGENCY CERTIFIED / PACKAGE	TRIGGER, CURRENT I <sub>FT</sub> (mA)				
UL, cUL	10				
DIP-8	VO2223				
UL, cUL, VDE (option 1)	10				
DIP-8	VO2223-X001				



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# Vishay Semiconductors

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Forward current		I <sub>F</sub>	50	mA		
Reverse voltage		$V_{R}$	5	V		
Input power dissipation		P <sub>diss</sub>	70	mW		
OUTPUT						
Output power dissipation		P <sub>diss</sub>	1130	mW		
Repetitive peak off-state voltage	Sine wave, 50 Hz to 60 Hz, gate open	$V_{DRM}$	600	V		
RMS on-state current		I <sub>T(RMS)</sub>	0.9	Α		
Non repetitive surge peak on-state current	50 Hz, peak	I <sub>TSM</sub>	10	Α		
COUPLER						
Total power dissipation (1)		P <sub>diss</sub>	1200	mW		
Ambient temperature range		T <sub>amb</sub>	-40 to +85	°C		
Storage temperature range		T <sub>stg</sub>	-40 to +150	°C		
Soldering temperature	t ≤ 10 s max.	T <sub>sld</sub>	260	°C		

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Total power dissipation value is based on 2S2P PCB.

## **ABSOLUTE MAXIMUM RATING CURVES**

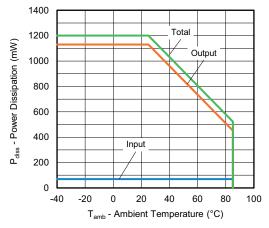


Fig. 1 - Power Dissipation vs. Ambient Temperature

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# Vishay Semiconductors

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Trigger input current	V <sub>T</sub> = 6 V	I <sub>FT</sub>	ı	3.5	10	mA
Input reverse current	V <sub>R</sub> = 5 V I <sub>R</sub>		ı	1	10	μΑ
Forward voltage	$I_F = 10 \text{ mA}$	$V_{F}$	0.9	-	1.5	V
OUTPUT						
Peak on-state voltage	I <sub>TM</sub> = 1 A	$V_{TM}$	ı	1	1.7	V
Peak off-state current	V <sub>DRM</sub> = 600 V	I <sub>DRM</sub>	ı	1	100	μΑ
Holding current	$R_L = 100 \Omega$	I <sub>H</sub>	ı	-	25	mA
Critical rate of rise of off-state voltage	$V_{IN} = 400 V_{RMS}$ (Fig. 3)	dV/dt <sub>cr</sub>	ı	210	-	V/µs
Critical rate of rise of commutating voltage	$V_{IN} = 240 V_{RMS}, I_T = 1 A_{RMS}$ (Fig. 3)	dV/dt <sub>crq</sub>	ı	0.7	1	V/µs

#### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.

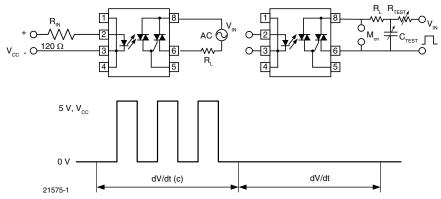
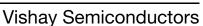


Fig. 2 - dV/dt Test Circuit

SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Climatic classification	According to IEC 68 part 1		40 / 85 / 21			
Pollution degree	According to DIN VDE 0109	to DIN VDE 0109				
Comparative tracking index	Insulation group Illa	CTI	175			
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V <sub>ISO</sub>	5300	V <sub>RMS</sub>		
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	8000	V <sub>peak</sub>		
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V <sub>IORM</sub>	890	V <sub>peak</sub>		
Isolation resistance	T <sub>amb</sub> = 25 °C, V <sub>IO</sub> = 500 V	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω		
	$T_{amb} = 100  ^{\circ}\text{C},  V_{IO} = 500  \text{V}$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω		
Output safety power		P <sub>SO</sub>	2000	mW		
Input safety current		I <sub>SI</sub>	150	mA		
Input safety temperature		T <sub>SI</sub>	175	°C		
Creepage distance	DID 0		≥ 7	mm		
Clearance distance	DIP-8		≥ 7	mm		
Creepage distance	SMD 9 option 7		≥ 8	mm		
Clearance distance	SMD-8, option 7		≥ 8	mm		

## Note

• This phototriac coupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with safety ratings shall be ensured by means of protective circuits.





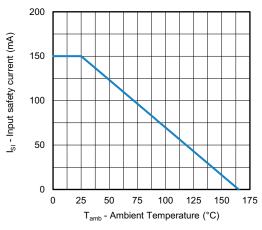


Fig. 3 - Input Safety Current vs. Ambient Temperature

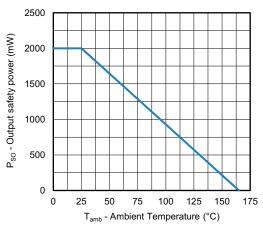


Fig. 4 - Output Safety Power vs. Ambient Temperature

# TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

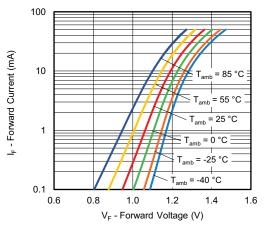


Fig. 5 - Forward Current vs. Forward Voltage

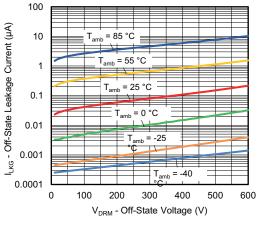


Fig. 7 - Off-State Leakage Current vs. Off-State Voltage

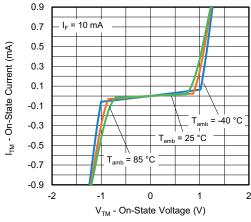


Fig. 6 - On-State Current vs. On-State Voltage

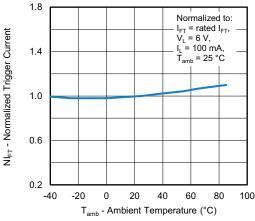


Fig. 8 - Normalized Trigger Input Current vs. Ambient Temperature



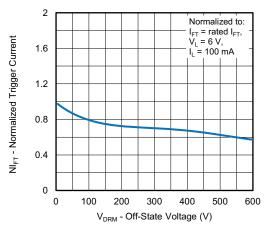


Fig. 9 - Normalized Trigger Current vs. Off-State Voltage

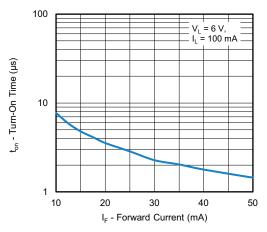


Fig. 10 - Turn-On Time vs. Forward Current

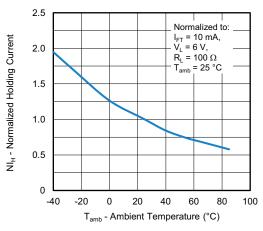


Fig. 11 - Normalized Holding Current vs. Ambient Temperature

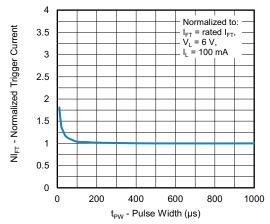
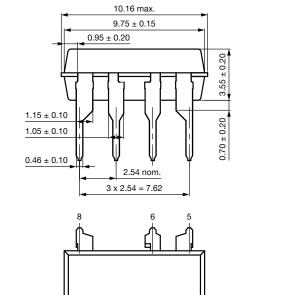
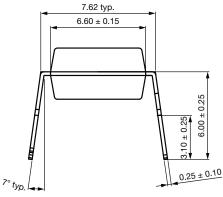


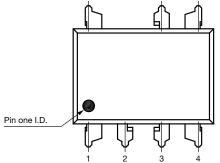
Fig. 12 - Normalized Trigger Current vs. Pulse Width

## **PACKAGE DIMENSIONS** in millimeters

## DIP-8

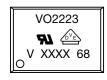






23212

# PACKAGE MARKING (Example of VO2223-X001)



## Notes

- XXXX = LMC (lot marking code)
- The VDE logo is only marked on option 1 parts. Option information is not marked on the part
- Tape and reel suffix (T) is not part of the package marking

# **PACKING INFORMATION**

DEVICE PER TUBE					
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX		
DIP-8	50	40	2000		

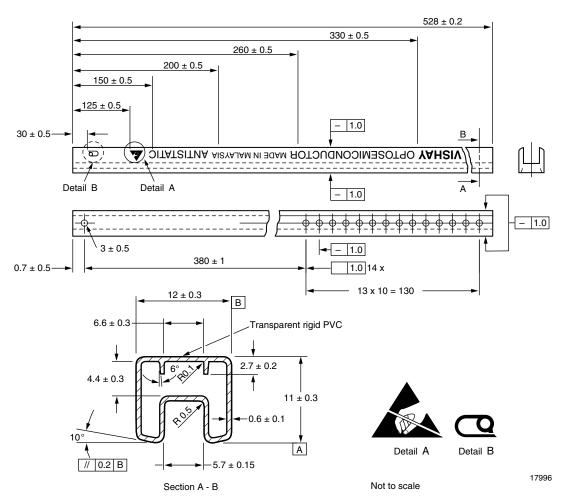


Fig. 13 - Shipping Tube Specifications for DIP Packages

## **SOLDER PROFILES**

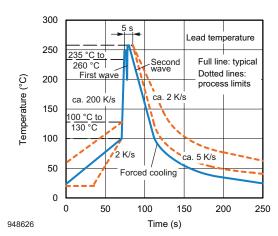


Fig. 14 - Recommended Wave Soldering Double Wave Profile for DIP Devices

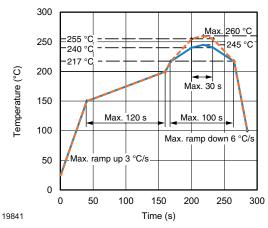


Fig. 15 - Recommended Lead (Pb)-free Reflow Solder Profile for SMD Devices



# HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions:  $T_{amb}$  < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



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