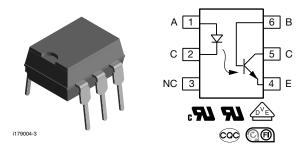
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Optocoupler, Phototransistor Output, With Base Connection



LINKS TO ADDITIONAL RESOURCES



DESCRIPTION

The H11A1 is an industry standard single channel phototransistor coupler.

Each optocoupler consists of gallium arsenide infrared LED and a silicon NPN phototransistor.

The isolation performance is accomplished through Vishay double molding isolation manufacturing process. Compliance to DIN EN 60747-5-5 partial discharge isolation specification is available is by ordering option 1.

These isolation processes and the Vishay ISO9001 quality program results in the highest isolation performance available for a commercial plastic phototransistor optocoupler.

The devices are available in lead formed configuration suitable for surface mounting and are available either on tape and reel, or in standard tube shipping containers.

Note

• Designing with datasheet is covered in Application Note 45

FEATURES

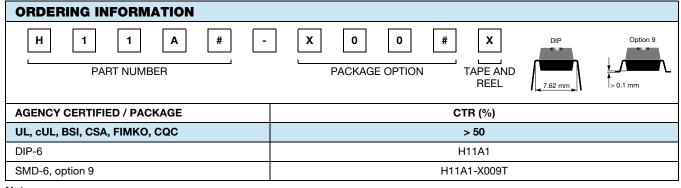
- Interfaces with common logic families
- Input-output coupling capacitance < 0.5 pF
- Industry standard dual-in line 6-pin package
- Isolation rated voltage 4420 V_{RMS}
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- AC mains detection
- · Reed relay driving
- · Switch mode power supply feedback
- Telephone ring detection
- Logic ground isolation
- · Logic coupling with high frequency noise rejection

AGENCY APPROVALS

- <u>UL 1577</u>
- <u>cUL</u>
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- <u>BSI EN 62368-1</u>
- <u>CQC GB 4943.1</u>
- <u>CQC GB 8898</u>
- <u>FIMKO</u>



Note

· Additional options may be possible, please contact sales office

1



RoHS

COMPLIAN[®]



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
Reverse voltage		V _R	6	V				
Forward current		I _F	60	mA				
Surge current	t ≤ 10 µs	I _{FSM}	2.5	A				
Power dissipation		P _{diss}	100	mW				
OUTPUT								
Collector emitter breakdown voltage		V _{CEO}	70	V				
Emitter base breakdown voltage		V _{EBO}	7	V				
Collector current		Ι _C	50	mA				
	t < 1 ms	Ι _C	100	mA				
Power dissipation		P _{diss}	150	mW				
COUPLER								
Storage temperature range		T _{stg}	-55 to +150	°C				
Operating temperature range		T _{amb}	-55 to +100	°C				
Junction temperature		Tj	100	°C				
Soldering temperature	Max. 10 s, dip soldering: distance to seating plane ≥ 1.5 mm	T _{sld}	260	°C				

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	I _F = 10 mA	V _F	-	1.1	1.5	V	
Reverse current	V _R = 3 V	I _R	-	-	10	μA	
Capacitance	$V_R = 0 V$, f = 1 MHz	Co	-	50	-	pF	
OUTPUT							
Collector emitter breakdown voltage	$I_{\rm C} = 1 {\rm mA}, I_{\rm F} = 0 {\rm mA}$	BV _{CEO}	30	-	-	V	
Emitter collector breakdown voltage	I _E = 100 μA, I _F = 0 mA	BV _{ECO}	7	-	-	V	
Collector base breakdown voltage	I _C = 10 μA, I _F = 0 mA	BV _{CBO}	70	-	-	V	
Collector emitter leakage current	V _{CE} = 10 V, I _F = 0 mA	I _{CEO}	-	5	50	nA	
Emitter collector capacitance	V _{CE} = 0 V	C _{CE}	-	6	-	pF	
COUPLER							
Collector emitter, saturation voltage	$I_{CE} = 0.5 \text{ mA}, I_F = 10 \text{ mA}$	V _{CEsat}	-	-	0.4	V	
Capacitance (input-output)		C _{IO}	-	0.5	-	pF	

Note

 Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
I _C /I _F	$V_{CE} = 10 \text{ V}, \text{ I}_{F} = 10 \text{ mA}$	CTR _{DC}	50	-	-	%

SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	I_{C} = 2 mA, R_{L} = 100 Ω,V_{CE} = 10 V	t _{on}	-	3	-	μs
Turn-off time		t _{off}	-	3	-	μs

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SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Climatic classification	According to IEC 68 part 1		55 / 100 / 21			
Comparative tracking index		CTI	175			
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	4420	V _{RMS}		
Maximum transient isolation voltage		VIOTM	8000	V _{peak}		
Maximum repetitive peak isolation voltage		V _{IORM}	890	V _{peak}		
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹²	Ω		
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹¹	Ω		
Output safety power		P _{SO}	700	mW		
Input safety current		I _{SI}	400	mA		
Safety temperature		Ts	175	°C		
Creepage distance			≥7	mm		
Clearance distance			≥7	mm		
Insulation thickness		DTI	≥ 0.4	mm		

Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

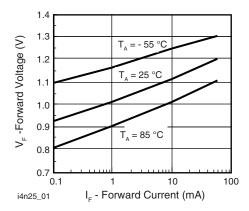


Fig. 1 - Forward Voltage vs. Forward Current

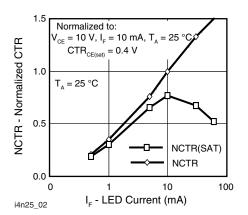


Fig. 2 - Normalized Non-Saturated and Saturated CTR vs. LED Current

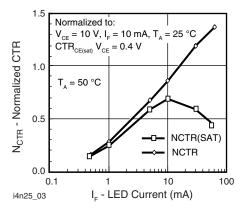


Fig. 3 - Normalized Non-Saturated and Saturated CTR vs. LED Current

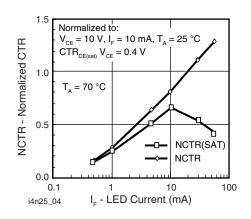
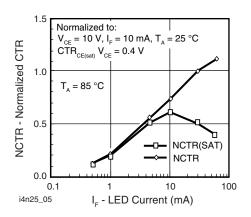


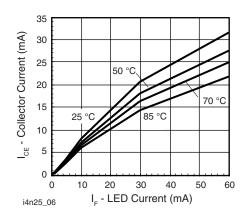
Fig. 4 - Normalized Non-Saturated and Saturated CTR vs. LED Current

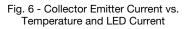
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Fig. 5 - Normalized Non-Saturated and Saturated CTR vs. LED Current





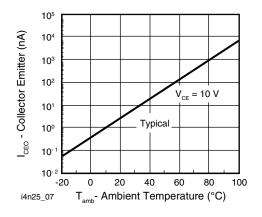


Fig. 7 - Collector Emitter Leakage Current vs. Temperature

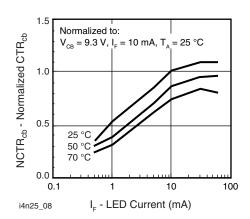


Fig. 8 - Normalized CTR_{cb} vs. LED Current and Temperature

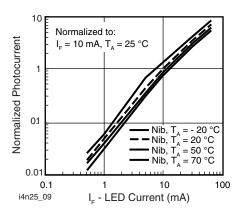


Fig. 9 - Normalized Photocurrent vs. ${\rm I}_{\rm F}$ and Temperature

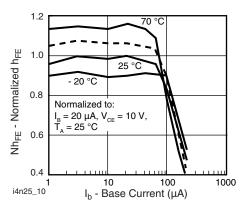


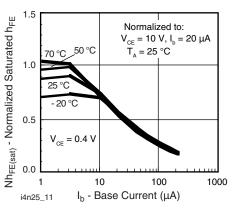
Fig. 10 - Normalized Non-Saturated h_{FE} vs. Base Current and Temperature

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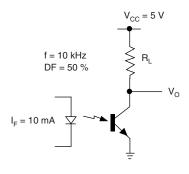
Fig. 13 - Switching Timing



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Fig. 11 - Normalized HFE vs. Base Current and Temperature



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Fig. 14 - Switching Schematic

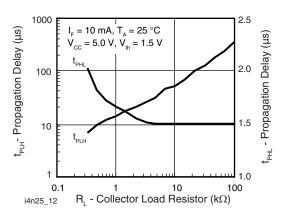
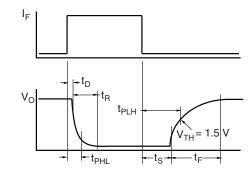


Fig. 12 - Propagation Delay vs. Collector Load Resistor



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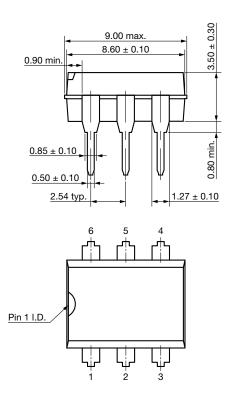
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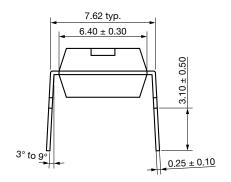


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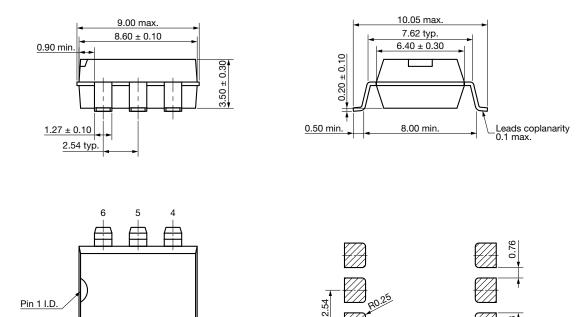
PACKAGE DIMENSIONS in millimeters

DIP-6





Option 9



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6 For technical questions, contact: <u>optocoupleranswers@vishay.com</u> Document Number: 83730

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PACKAGE MARKING



Notes

- XXXX = LMC (lot marking code)
- Only options 1 and 7 are reflected in the package marking
- The VDE Logo is only marked on option1 parts
- Tape and reel suffix (T) is not part of the package marking



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