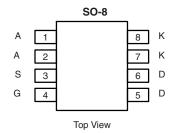




P-Channel 20-V (D-S) MOSFET with Schottky Diode

PRODUCT SUMMARY							
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)				
- 20	$0.054 \text{ at V}_{GS} = -10 \text{ V}$	6.2	4.5 nC				
	0.094 at V _{GS} = - 4.5 V	4.7	4.5 110				

SCHOTTKY PRODUCT SUMMARY					
V _{KA} (V)	V _f (V) Diode Forward Voltage	I _F (A) ^a			
20	0.45 at 1 A	2			



Ordering Information: Si4621DY-T1-E3 (Lead (Pb)-free)

Si4621DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

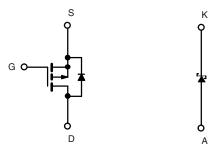
FEATURES

- Halogen-free According to IEC 61249-2-21 **Definition**
- LITTLE FOOT® Plus Schottky
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

- Portable Devices
 - Ideal for Boost Circuits
 - Ideal for Buck Circuits



P-Channel MOSFET

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage (MOSFET)		V_{DS}	- 20		
Reverse Voltage (Schottky)		V_{KA}	20	V	
Gate-Source Voltage (MOSFET)	V_{GS}	± 20			
	T _C = 25 °C		- 6.2		
Continuous Drain Current (T _{.I} = 150 °C) (MOSFET)	T _C = 70 °C	I _D	- 5 ^a		
communication (1) = 100 °C) (meet 21)	T _A = 25 °C	υ.υ	- 5 ^{b, c}	7	
	T _A = 70 °C		- 4 ^{b, c}	1	
Pulsed Drain Current (MOSFET)		I _{DM}	- 25	А	
Continuous Source-Drain Diode Current	T _C = 25 °C	Is	- 2.6		
(MOSFET Diode Conduction)	T _A = 25 °C	'S	1.7 ^{b, c}		
Average Forward Current (Schottky)	I _F	2 ^b			
Pulsed Forward Current (Schottky)		I _{FM}	5		
	T _C = 25 °C		3.1		
Maximum Power Dissipation (MOSFET)	T _C = 70 °C		2	1	
Maximum Tower Dissipation (MOSI ET)	T _A = 25 °C		2 ^{b, c}	7	
	T _A = 70 °C	P _D	1.3 ^{b, c}	w	
	T _C = 25 °C	' ' '	2.7	7 "	
Maximum Power Dissipation (Schottky)	T _C = 70 °C		1.7		
Maximum Fower Dissipation (Schottky)	T _A = 25 °C		1.6 ^{b, c}		
	T _A = 70 °C		1 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	



THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient (MOSFET) ^{b, f}	R _{thJA}	55	62.5				
Maximum Junction-to-Foot (Drain) (MOSFET)	R _{thJF}	33	40	°C/W			
Maximum Junction-to-Ambient (Schottky) ^{b, g}	R _{thJA}	63	78	C/VV			
Maximum Junction-to-Foot (Drain) (Schottky)	R _{thJF}	39	47				

Notes:

- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- f. Maximum under Steady State conditions is 110 °C/W. g. Maximum under Steady State conditions is 115 °C/W.

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 16		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			3.6		mv/·C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtaga Drain Current	1	V _{DS} = - 20 V, V _{GS} = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 55 °C			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le 5 \text{ V}, V_{GS} = -10 \text{ V}$	- 25			Α	
	D	V _{GS} = - 10 V, I _D = - 5 A		0.042	0.054	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 1.1 A		0.073	0.094		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 5 A		10		S	
Dynamic ^b							
Input Capacitance	C _{iss}			450			
Output Capacitance	C _{oss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		160		pF	
Reverse Transfer Capacitance	C _{rss}			105		1	
	0	$V_{DS} = -10 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -6.2 \text{ A}$		8.7	13		
Total Gate Charge	Q_g	V _{DS} = - 10 V, V _{GS} = - 4.5 V, I _D = - 6.2 A		4.5	6.8	nC	
Gate-Source Charge	Q_{gs}			1.7			
Gate-Drain Charge	Q_{gd}			1.8		1	
Gate Resistance	R_g	f = 1 MHz		9		Ω	
Turn-On Delay Time	t _{d(on)}			15	25		
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_{L} = 2.5 \Omega$		60	90		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ - 4 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		22	35	7	
Fall Time	t _f			15	25]	
Turn-On Delay Time	t _{d(on)}			5	10	ns	
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_{L} = 2.5 \Omega$		60	90	7	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -4 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		20	30	1	
Fall Time	t _f			7	15	1	





SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions		Тур.	Max.	Unit	
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 6.2	Α	
Pulse Diode Forward Current	I _{SM}				- 25	^	
Body Diode Voltage	V _{SD}	I _S = - 1.7 A, V _{GS} = 0 V		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			21	40	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	$I_F = -1.7 \text{ A, dl/dt} = 100 \text{ A/}\mu\text{s, T}_J = 25$		10	20	nC	
Reverse Recovery Fall Time	t _a	°C		7		ns	
Reverse Recovery Rise Time	t _b			16		115	

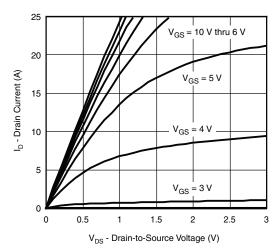
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

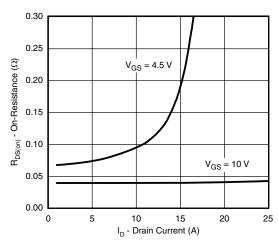
SCHOTTKY SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Forward Voltage Drop	V _F	I _F = 1 A		0.41	0.45	V		
		I _F = 1 A, T _J = 125 °C		0.36	0.41			
	I _{rm}	V _r = 20 V		0.02	0.20	mA		
Maximum Reverse Leakage Current		$V_r = 20 \text{ V}, T_J = 85 ^{\circ}\text{C}$		0.7	7			
		V _r = 20 V, T _J = 125 °C		5	50			
Junction Capacitance	C _T	V _r = 10 V		60		pF		

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

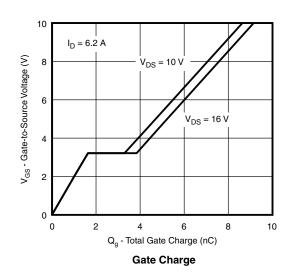
MOSFET TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)

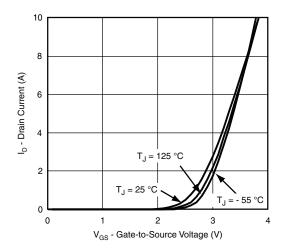


Output Characteristics

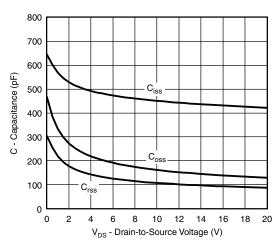


On-Resistance vs. Drain Current and Gate Voltage

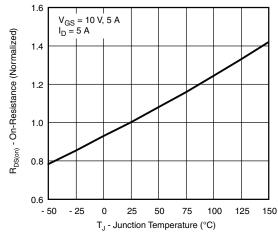




Transfer Characteristics



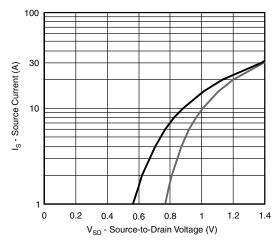
Capacitance



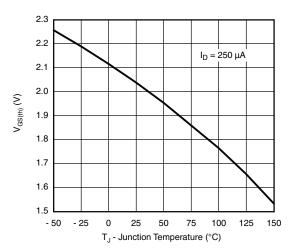
On-Resistance vs. Junction Temperature



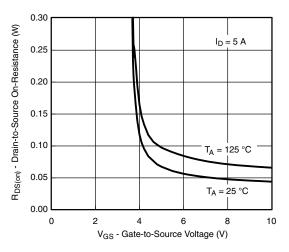
MOSFET TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



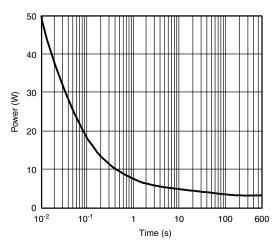
Source-Drain Diode Forward Voltage



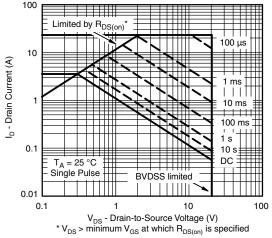
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

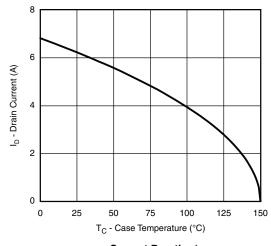


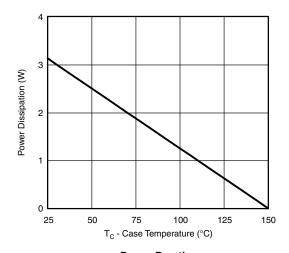
Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Case

MOSFET TYPICAL CHARACTERISTICS ($T_A = 25~^{\circ}C$, unless otherwise noted)





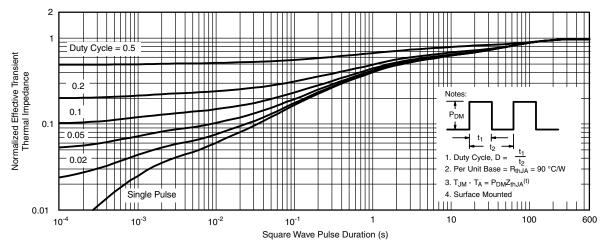
Current Derating*

Power Derating

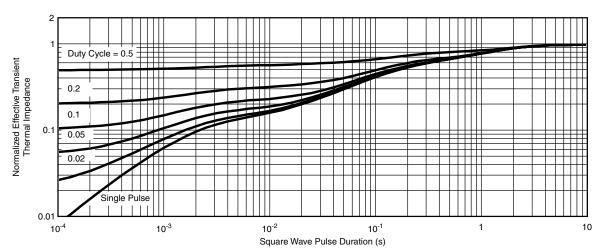
 $^{^{\}star}$ The power dissipation P_D is based on T_{J(max)} = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



MOSFET TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



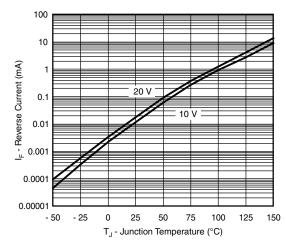
Normalized Thermal Transient Impedance, Junction-to-Ambient

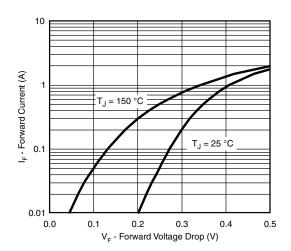


Normalized Thermal Transient Impedance, Junction-to-Foot

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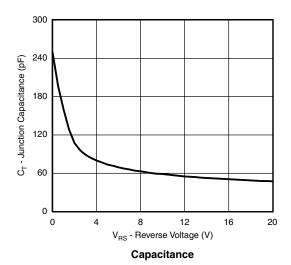
SCHOTTKY TYPICAL CHARACTERISTICS ($T_A = 25~^{\circ}C$, unless otherwise noted)



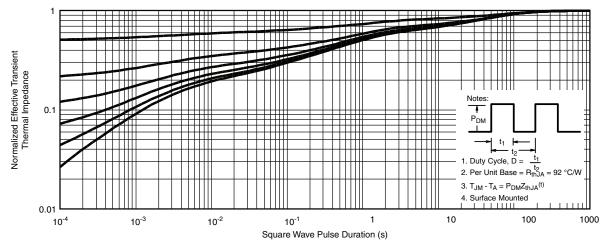


Reverse Current vs. Junction Temperature

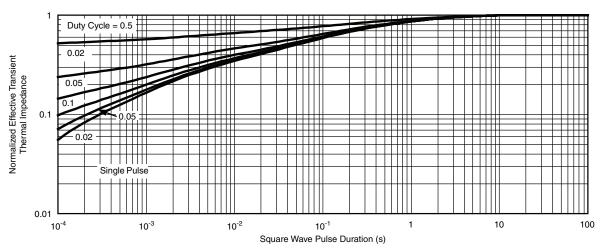
Forward Voltage Drop



SCHOTTKY TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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