



N-Channel 100 V (D-S) MOSFET



PRODUCT SUMMARY			
V _{DS} (V)	100		
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 10 \text{ V}$	0.0082		
Q _g typ. (nC)	97		
I _D (A)	90 q		
Configuration	Single		

FEATURES

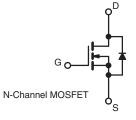
- TrenchFET® power MOSFETs
- 175 °C junction temperature



- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Power supply
 - Secondary synchronous rectification
- Industrial
- · Primary switch



ORDERING INFORMATION		
Package	TO-263	
Lead (Pb)-free	SUM90N10-8m2P-E3	

ABSOLUTE MAXIMUM RATINGS	$(T_C = 25 ^{\circ}C, \text{ unless other})$	wise noted)		
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V _{DS}	100	V
Gate-source voltage		V_{GS}	± 20	7 °
Continuous drain current (T _{.I} = 175 °C)	T _C = 25 °C		90 ^d	
Continuous drain current (1j = 175 C)	T _C = 70 °C	l _D	90 ^d	
Pulsed drain current		I _{DM}	240	_ A
Avalanche current		I _{AS}	60	
Single avalanche energy a	L = 0.1 mH	E _{AS}	180	mJ
lavimum navar discination 3	T _C = 25 °C	В	300 b	W
Maximum power dissipation ^a	T _A = 25 °C °C	P _D	3.75	
Operating junction and storage temperature rai	nge	T _J , T _{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-ambient (PCB mount) ^c	R _{thJA}	40	°C/W
Junction-to-case (drain)	R _{thJC}	0.5	C/VV

Notes

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).
- d. Package limited.



SPECIFICATIONS ($T_J = 25^{\circ}$ PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static	OTWIDOL	TEST CONDITIONS	141114.		IVIAA.	Oltil
Drain-source breakdown voltage	V _{DS}	V _{DS} = 0 V, I _D = 250 μA	100	_	_	
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2.5	_	4.5	V
Gate-body leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	_	± 250	nA
	400	V _{DS} = 100 V, V _{GS} = 0 V	-	-	1	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V, T _J = 125 °C	-	-	50	μA
3	200	V _{DS} = 100 V, V _{GS} = 0 V, T _J = 150 °C	-	-	250	•
On-state drain current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	70	-	-	Α
	_ ` `	V _{GS} = 10 V, I _D = 20 A	-	0.0067	0.0082	
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = 10 V, I _D = 20 A, T _J = 125 °C	-	0.0127	0.0170	Ω
Forward transconductance a	9 _{fs}	$V_{DS} = 15 \text{ V}, I_D = 20 \text{ A}$	-	62	-	S
Dynamic ^b			L	I		
Input capacitance	C _{iss}		-	6290	-	
Output capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 50 \text{ V}, f = 1 \text{ MHz}$	-	535	-	pF
Reverse transfer capacitance	C _{rss}		-	182	-	
Total gate charge ^c	Qg		-	97	150	
Gate-source charge ^c	Q _{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 85 \text{ A}$	-	32	-	nC
Gate-drain charge ^c	Q _{gd}		-	25	-	
Gate resistance	Rg	f = 1 MHz	0.28	1.4	2.8	W
Turn-on delay time ^c	t _{d(on)}		-	23	35	
Rise time ^c	t _r	$V_{DD} = 50 \text{ V}, R_L = 0.588 \Omega$	-	17	26	20
Turn-off delay time ^c	t _{d(off)}	$I_D\cong 85$ A, $V_{GEN}=10$ V, $R_g=1$ Ω	-	34	52	ns
Fall time ^c	t _f		-	9	18	
Source-Drain Diode Ratings and Ch	naracteristics (T _C = 25 °C) ^b				
Continuous current	I _S		-	-	85	А
Pulsed current	I _{SM}		-	-	240	
Forward voltage ^a	V _{SD}	$I_F = 30 \text{ A}, V_{GS} = 0 \text{ V}$	-	0.85	1.5	V
Reverse recovery time	t _{rr}		-	61	100	ns
Peak reverse recovery current	I _{RM(REC)}	$I_F = 75 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$	-	3	4.5	Α
Reverse recovery charge	Q _{rr}		-	91	130	nC

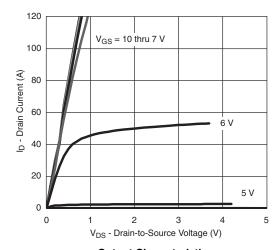
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

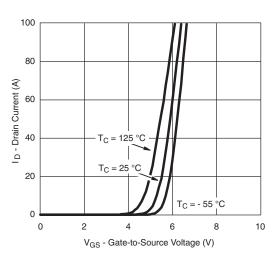
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



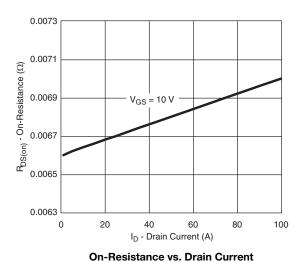
TYPICAL CHARACTERISTICS (25 °C, UNLESS OTHERWISE NOTED)

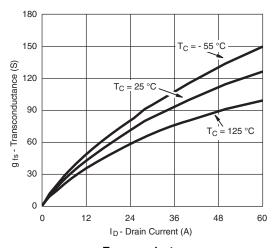


Output Characteristics

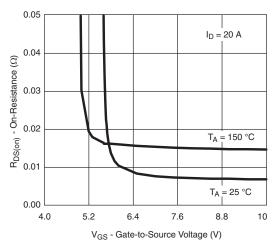


Transfer Characteristics

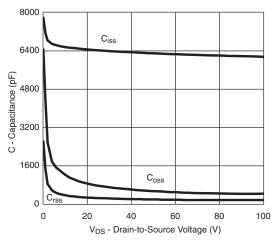




Transconductance



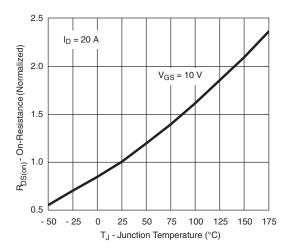
On-resistance vs. Gate-to-Source Voltage



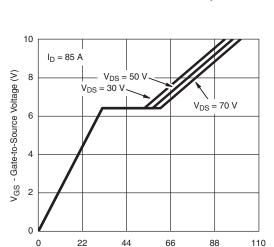
Capacitance



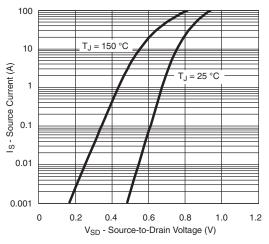
TYPICAL CHARACTERISTICS (25 °C, UNLESS OTHERWISE NOTED)



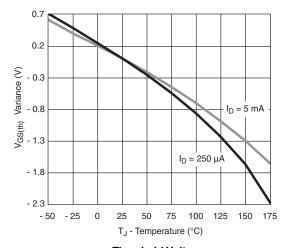
On-Resistance vs. Junction Temperature



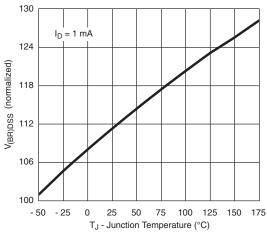
Q_q - Total Gate Charge (nC) **Gate Charge**



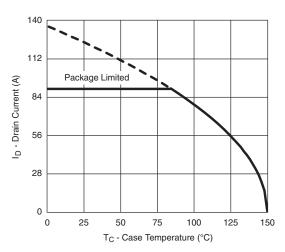
Source-Drain Diode Forward Voltage



Threshold Voltage



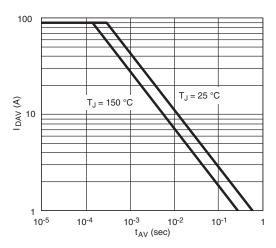
Drain Source Breakdown vs. Junction Temperature



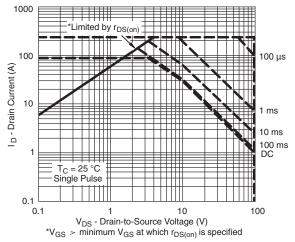
Maximum Drain Current vs. Case Temperature



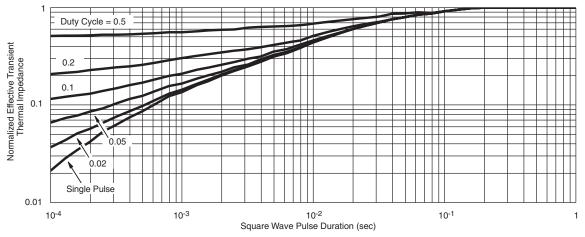
TYPICAL CHARACTERISTICS (25 °C, UNLESS OTHERWISE NOTED)



Single Pulse Avalanche Current Capability vs. Time



Safe Operating Area



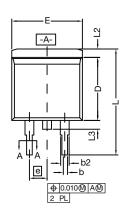
Normalized Thermal Transient Impedance, Junction-to-Case

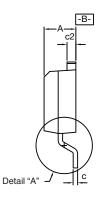
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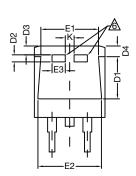
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TO-263 (D²PAK): 3-LEAD

VERSION 1: FACILITY CODE = T

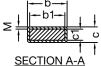








DETAIL A (ROTATED 90°)



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Notes

- 1. Plane B includes maximum features of heat sink tab and plastic.
- 2. No more than 25 % of L1 can fall above seating plane by max. 8 mils.
- 3. Pin-to-pin coplanarity max. 4 mils.
- 4. *: Thin lead is for SUB, SYB. Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

6. This feature is for thick lead.

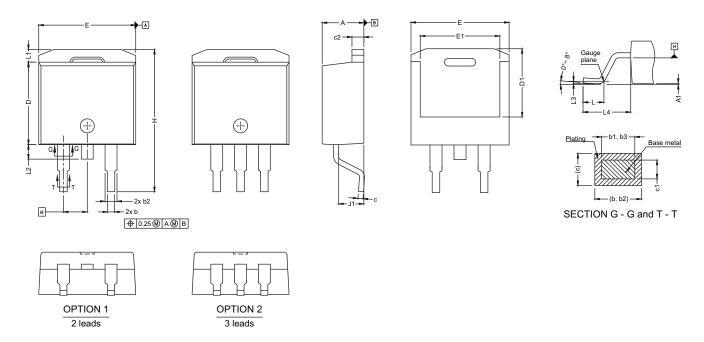
		INCHES		MILLIN	METERS
	DIM.	MIN.	MAX.	MIN.	MAX.
	Α	0.160	0.190	4.064	4.826
b		0.020	0.039	0.508	0.990
	b1	0.020	0.035	0.508	0.889
	b2	0.045	0.055	1.143	1.397
c*	Thin lead	0.013	0.018	0.330	0.457
C	Thick lead	0.023	0.028	0.584	0.711
c1	Thin lead	0.013	0.017	0.330	0.431
Ci	Thick lead	0.023	0.027	0.584	0.685
	c2	0.045	0.055	1.143	1.397
	D	0.340	0.380	8.636	9.652
	D1	0.220	0.240	5.588	6.096
	D2	0.038	0.042	0.965	1.067
	D3	0.045	0.055	1.143	1.397
	D4	0.044	0.052	1.118	1.321
	Е	0.380	0.410	9.652	10.414
	<u>E1</u>	0.245	-	6.223	-
	E2	0.355	0.375	9.017	9.525
	E3	0.072	0.078	1.829	1.981
	е	0.100	BSC	2.54	BSC
K		0.045	0.055	1.143	1.397
	L	0.575	0.625	14.605	15.875
	L1	0.090	0.110	2.286	2.794
	L2	0.040	0.055	1.016	1.397
	L3	0.050	0.070	1.270	1.778
	L4	0.010	BSC	0.254	BSC
	М	-	0.002	-	0.050



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VERSION 2: FACILITY CODE = N



DIM.	MIN.	MAX.
A	4.36	4.56
A1	0	0.25
b	0.70	0.90
b1	0.51	0.89
b2	1.20	1.46
b3	1.17	1.37
С	0.38	0.694
c1	0.38	0.534
c2	1.19	1.34
D	8.60	9.00
D1	6.9	7.5
E	10.15	10.55
E1	8.1	8.7
е	2.54	BSC
Н	15.0	15.6
L	1.9	2.5
L1	-	1.65
L2	-	1.78
L3	0.25 typ.	
L4	4.78	5.28
J1	2.56 2.96	

DWG: 5843





RECOMMENDED MINIMUM PADS FOR D²PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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