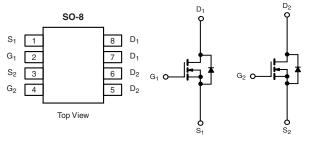


Automotive Dual N-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	60			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.080			
I _D (A)	± 3.7			
Configuration	Dual			



N-Channel MOSFET

N-Channel MOSFET

FEATURES

- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance



RoHS'

AEC-Q101 RELIABILITY

- Passed all AEC-Q101 Reliability Testing
- · Characterization Ongoing

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free	SQ9945AEY-T1-E3
SnPb	SQ9945AEY-T1

ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V_{DS}	60	.,	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current ^a	T _C = 25 °C		- 3.7		
	T _C = 70 °C		- 3.2		
Continuous Source Current (Diode Conduction) ^a		Is	2	A	
Pulsed Drain Current ^b		I _{DM}	25		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	-	mJ	
Single Pulse Avalanche Current	L = 0.1 MH	I _{AS}	-	A	
Maximum Power Dissipation ^b	T _C = 25 °C	- P _D	2.4	W	
	T _A = 70 °C		1.7] ""	
Operating Junction and Storage Temperature F	Range	T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient F	PCB Mount ^c	R _{thJA}	-	°C/W
Junction-to-Case (Drain)		R _{thJC}	-	C/VV

Notes

- a. Package limited.
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- c. When mounted on 1" square PCB (FR-4 material).

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

SQ9945AEY

Vishay Siliconix



PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static	•							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		-	-	-	.,	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		-	3.0	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
Zero Gate Voltage Drain Current		V _{GS} = 0 V	V _{DS} = 60 V	-	-	1.0		
	I _{DSS}	V _{GS} = 0 V	V _{DS} = 60 V	-	-	10	μΑ	
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 55 °C	-	-	-		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	20	-	-	Α	
Drain-Source On-State Resistance ^a		V _{GS} = 10 V	I _D = 3.7 A	-	0.060	0.080	Ω	
	R _{DS(on)}	V _{GS} = 10 V	I _D = 30 A, T _J = 125 °C	-	-	-		
		V _{GS} = 10 V	I _D = 30 A, T _J = 175 °C	-	-	-		
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 3.7 A		-	-	11	S	
Dynamic ^b								
Input Capacitance	C _{iss}		V V _{DS} = 25 V, f = 1 MHz	-	-	-	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	-	-		
Reverse Transfer Capacitance	C _{rss}]		-	-	-		
Total Gate Charge ^c	Qg		V _{DS} = 30 V, I _D = 3.7 A	-	11	20	nC	
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V		-	2	-		
Gate-Drain Charge ^c	Q _{gd}]		-	2	-		
Turn-On Delay Time ^c	t _{d(on)}			-	9	20		
Rise Time ^c	t _r		V_{DD} = 30 V, R_L = 30 Ω $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 6 Ω		10	20	- ns	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 1 A, Y$			21	40		
Fall Time ^c	t _f			-	8	20		
Source-Drain Diode Ratings and Char	acteristics T _C = 2	.5 °Cb						
Pulsed Current ^a	I _{SM}			-	-	-	Α	
Forward Voltage	V _{SD}	I _F = 85 A, V _{GS} = 0 V			-		V	
Reverse Recovery Time	t _{rr}	I _F = 2 A, dI/dt = 100 A/μs		_	45	80	ns	
Peak Reverse Recovery Current	I _{RM(REC)}			-	-	-	Α	
Reverse Recovery Charge	Q _{rr}			-	-	-	μC	

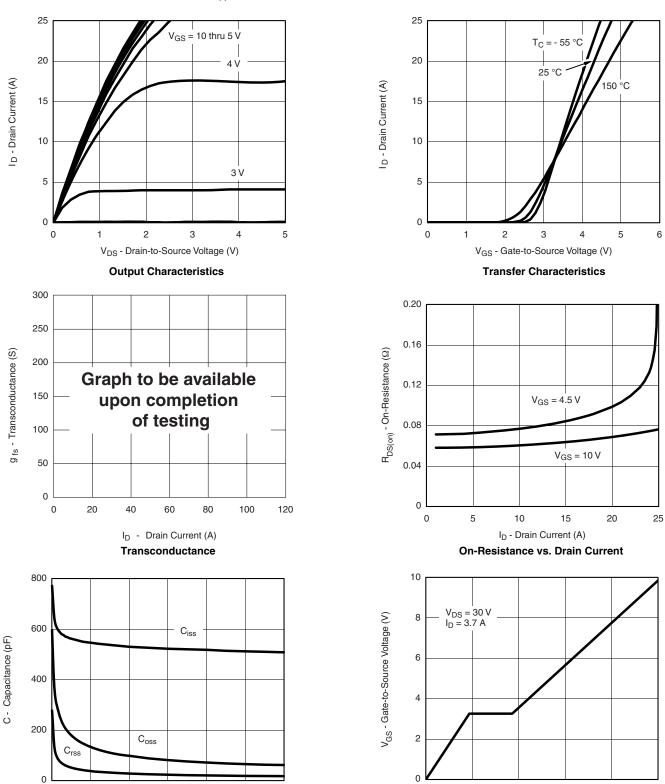
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted



0

2

0

10

50

40

V_{DS} - Drain-to-Source Voltage (V)

Capacitance

60

8

6

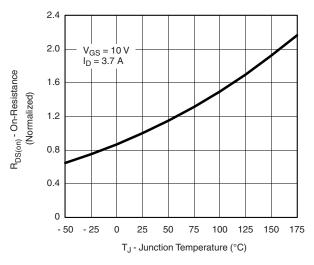
Q_g - Total Gate Charge (nC)

Gate Charge

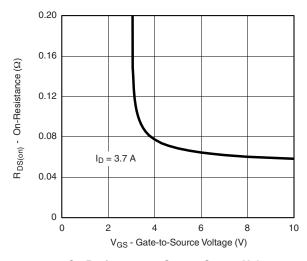
10



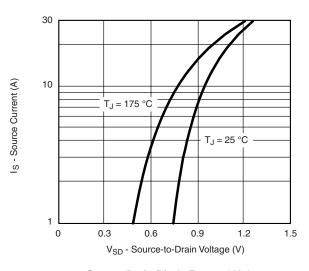
TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted



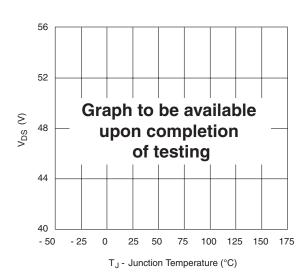
On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage



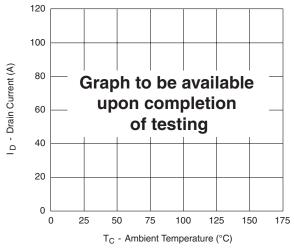
Source Drain Diode Forward Voltage



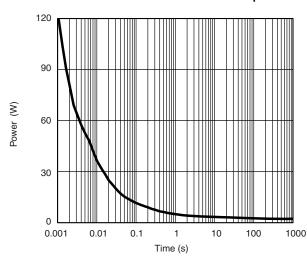
Drain Source Breakdown vs. Junction Temperature



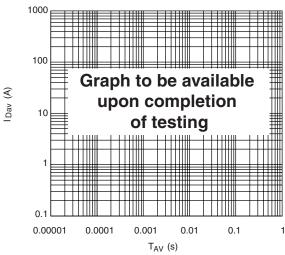
THERMAL RATINGS $T_A = 25$ °C, unless otherwise noted



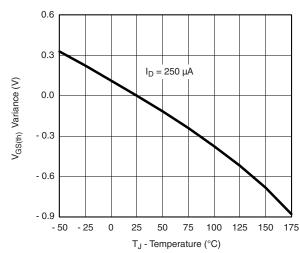
Maximum Drain Current vs. Ambient Temperature



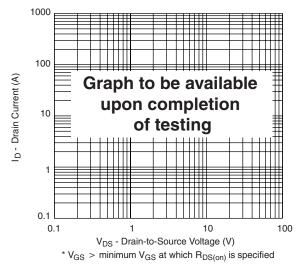
Single Pulse Power, Junction-to-Ambient



Avalanche Current vs. Time



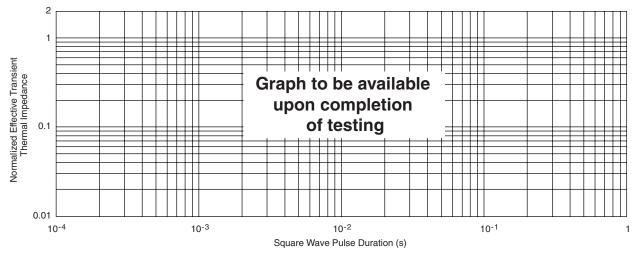
Threshold Voltage



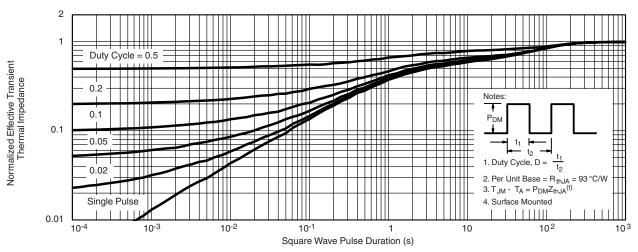
Safe Operating Area



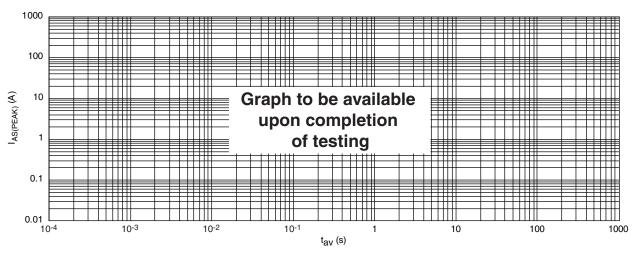
THERMAL RATINGS $T_A = 25$ °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Case



Normalized Thermal Transient Impedance, Junction-to-Ambient

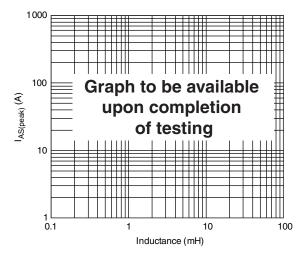


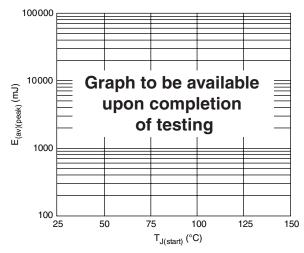
Single Pulse Avalanche Current (Peak) vs. Time in Avalanche





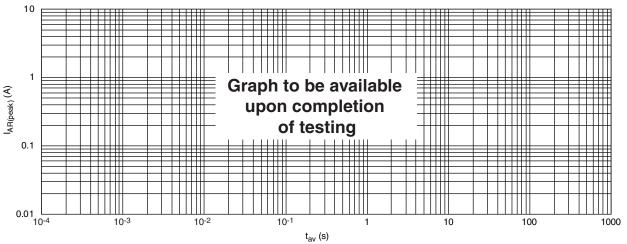
THERMAL RATINGS $T_A = 25$ °C, unless otherwise noted





Single Pulse Avalanche Current (Peak) vs. Inductance

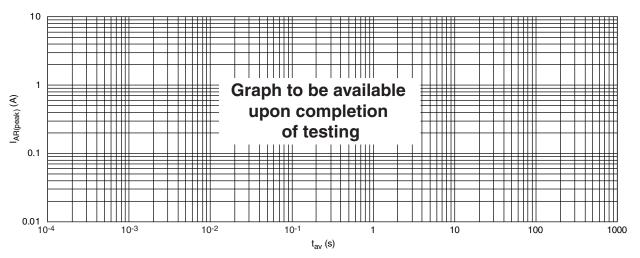
Single Pulse Avalanche Energy (Peak) vs. T_{J(start)}



Repetitive Avalanche Current (Peak) vs. Time in Avalanche at T_A = 25 °C



THERMAL RATINGS $T_A = 25$ °C, unless otherwise noted



Repetitive Avalanche Current (Peak) vs. Time in Avalanche at T_A = 150 °C

Note

The characteristics shown in the six graphs

- Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
- Single Pulse Avalanche Current (Peak) vs. Time in Avalanche
- Single Pulse Avalanche Current (Peak) vs. Inductance
- Single Pulse Avalanche Energy (Peak) vs. T_{J (start)}
- Repetitive Avalanche Current (Peak) vs. Time in Avalanche at T_A = 25 °C
- Repetitive Avalanche Current (Peak) vs. Time in Avalanche at T_A = 150 $^{\circ}$ C

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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