

Complementary Silicon Plastic Power Transistors

2N6487, 2N6488 (NPN), 2N6490, 2N6491 (PNP)

These devices are designed for use in general-purpose amplifier and switching applications.

Features

- High DC Current Gain
- High Current Gain Bandwidth Product
- TO-220 Compact Package
- These Devices are Pb-Free and are RoHS Compliant*

MAXIMUM RATINGS (Note 1)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage 2N6487, 2N6490 2N6488, 2N6491	V _{CEO}	60 80	Vdc
Collector-Base Voltage 2N6487, 2N6490 2N6488, 2N6491	V _{CB}	70 90	Vdc
Emitter-Base Voltage	V_{EB}	5.0	Vdc
Collector Current - Continuous	I _C	15	Adc
Base Current	Ι _Β	5.0	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	75 0.6	W W/°C
Total Power Dissipation @ T _A = 25°C Derate above 25°C	P _D	1.8 0.014	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

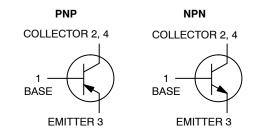
THERMAL CHARACTERISTICS

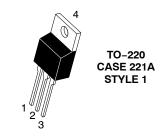
Characteristics	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.67	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	70	°C/W

^{*}For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

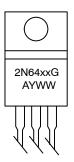
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15 AMPERE COMPLEMENTARY SILICON POWER TRANSISTORS 60-80 VOLTS, 75 WATTS





MARKING DIAGRAM



2N64xx = Specific Device Code = See Table on Page 5 G = Pb-Free Package = Assembly Location Α

= Year W/W = Work Week

ORDERING INFORMATION

See detailed ordering, marking, and shipping information on page 5 of this data sheet.

^{1.} Indicates JEDEC Registered Data.

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted) (Note 2)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Sustaining Voltage (Note 3) (I _C = 200 mAdc, I _B = 0) 2N6487, 2N6490 2N6488, 2N6491	V _{CEO(sus)}	60 80	- -	Vdc
Collector-Emitter Sustaining Voltage (Note 3) (I _C = 200 mAdc, V _{BE} = 1.5 Vdc) 2N6487, 2N6490 2N6488, 2N6491	V _{CEX}	70 90	- -	Vdc
Collector Cutoff Current $(V_{CE} = 30 \text{ Vdc}, I_B = 0)$ $2N6487, 2N6490$ $(V_{CE} = 40 \text{ Vdc}, I_B = 0)$ $2N6488, 2N6491$	I _{CEO}	-	1.0 1.0	mAdc
Collector Cutoff Current	I _{CEX}	- - -	500 500 5.0 5.0	μAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)	I _{EBO}	-	1.0	mAdc
ON CHARACTERISTICS				
DC Current Gain ($I_C = 5.0$ Adc, $V_{CE} = 4.0$ Vdc) ($I_C = 15$ Adc, $V_{CE} = 4.0$ Vdc)	h _{FE}	20 5.0	150 -	-
Collector–Emitter Saturation Voltage ($I_C = 5.0$ Adc, $I_B = 0.5$ Adc) ($I_C = 15$ Adc, $I_B = 5.0$ Adc)	V _{CE(sat)}	- -	1.3 3.5	Vdc
Base-Emitter On Voltage ($I_C = 5.0$ Adc, $V_{CE} = 4.0$ Vdc) ($I_C = 15$ Adc, $V_{CE} = 4.0$ Vdc)	V _{BE(on)}		1.3 3.5	Vdc
DYNAMIC CHARACTERISTICS	<u>.</u>			
Current-Gain - Bandwidth Product (Note 4) ($I_C = 1.0 \text{ Adc}, V_{CE} = 4.0 \text{ Vdc}, f_{test} = 1.0 \text{ MHz}$)	f _T	5.0		MHz
Small–Signal Current Gain ($I_C = 1.0 \text{ Adc}$, $V_{CE} = 4.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	h _{fe}	25	=	_

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Indicates JEDEC Registered Data.

- 3. Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2.0%.
- 4. $f_T = |h_{fe}| \cdot f_{test}$

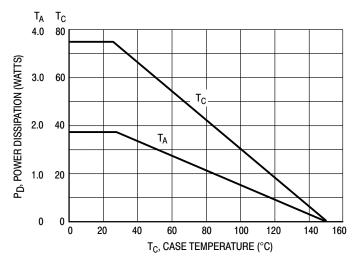
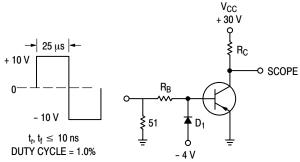


Figure 1. Power Derating



 $\rm R_B$ and $\rm R_C$ varied to obtain desired current levels. For PNP, reverse all polarities.

D₁ MUST BE FAST RECOVERY TYPE, e.g.: 1N5825 USED ABOVE I_B \approx 100 mA MSD6100 USED BELOW I_B \approx 100 mA

1000 500 200 t, TIME (ns) 100 $t_d \ @ \ V_{BE(off)} \approx 5.0 \ V$ 50 $T_C = 25^{\circ}C$ 20 $V_{CC} = 30 \text{ V}$ $I_C/I_B = 10$ 10 ____ 0.5 2.0 20 IC, COLLECTOR CURRENT (AMP)

Figure 2. Switching Time Test Circuit

Figure 3. Turn-On Time

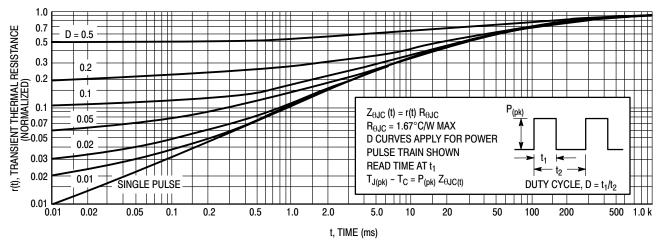


Figure 4. Thermal Response

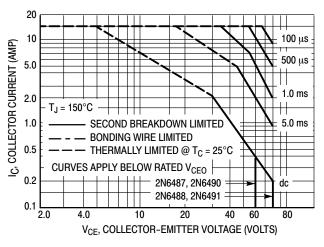


Figure 5. Active-Region Safe Operating Area

There are two limitations on the power handling ability of a transistors average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 5 is based on $T_{J(pk)} = 150^{\circ}C$; T_C is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \le 150^{\circ}C$. $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

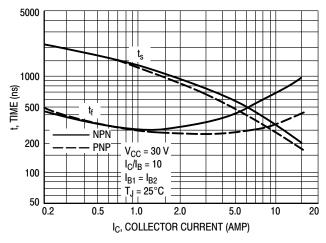


Figure 6. Turn-Off Time

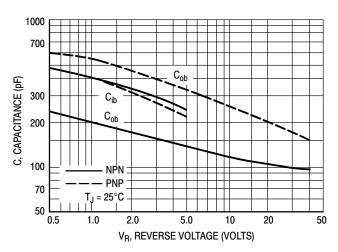
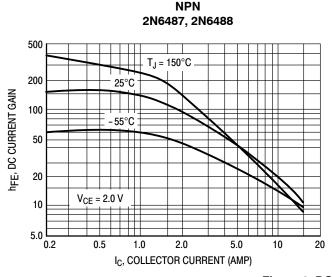


Figure 7. Capacitances

PNP



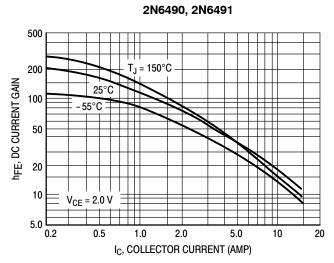


Figure 8. DC Current Gain

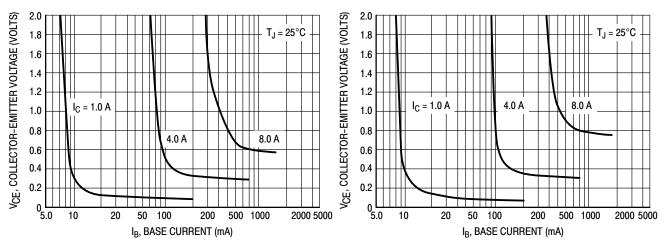


Figure 9. Collector Saturation Region

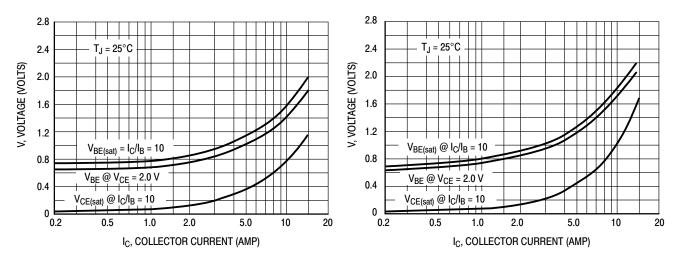


Figure 10. "On" Voltages

ORDERING INFORMATION

Device	Device Marking	Package	Shipping
2N6487G	2N6487	TO-220 (Pb-Free)	50 Units / Rail
2N6488G	2N6488	TO-220 (Pb-Free)	50 Units / Rail
2N6490G	2N6490	TO-220 (Pb-Free)	50 Units / Rail
2N6491G	2N6491	TO-220 (Pb-Free)	50 Units / Rail

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