Onsemi

Complementary Silicon Plastic Power Transistors

MJE15028, MJE15030 (NPN), MJE15029, MJE15031 (PNP)

These devices are designed for use as high-frequency drivers in audio amplifiers.

Features

- High Current Gain Bandwidth Product
- TO-220 Compact Package
- These Devices are Pb-Free and are RoHS Compliant*

MAXIMUM RATINGS

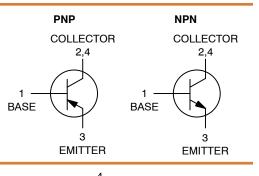
Rating	Symbol	Value	Unit		
Collector-Emitter Voltage MJE15028G, MJE15029G MJE15030G, MJE15031G	V _{CEO}	120 150	Vdc		
Collector-Base Voltage MJE15028G, MJE15029G MJE15030G, MJE15031G	V _{CB}	120 150	Vdc		
Emitter-Base Voltage	V _{EB}	5.0	Vdc		
Collector Current – Continuous	۱ _C	8.0	Adc		
Collector Current – Peak	I _{CM}	16	Adc		
Base Current	Ι _Β	2.0	Adc		
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	50 0.40	W W/°C		
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	2.0 0.016	W W/°C		
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C		

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.5	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	°C/W

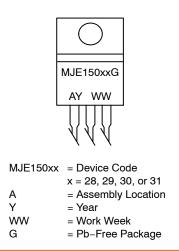
8 AMPERE POWER TRANSISTORS **COMPLEMENTARY SILICON** 120-150 VOLTS, 50 WATTS





MARKING DIAGRAM

TO-220



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

*For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Characteristic Symbol			
OFF CHARACTERISTICS				
Collector-Emitter Sustaining Voltage (Note 1) ($I_C = 10 \text{ mAdc}, I_B = 0$) MJE15028, MJE15029 MJE15030, MJE15031	V _{CEO(sus)}	120 150		Vdc
Collector Cutoff Current $(V_{CE} = 120 \text{ Vdc}, I_B = 0)$ MJE15028, MJE15029 $(V_{CE} = 150 \text{ Vdc}, I_B = 0)$ MJE15030, MJE15031	ICEO	_	0.1	mAdc
Collector Cutoff Current $(V_{CB} = 120 \text{ Vdc}, I_F = 0)$	I _{CBO}	_	0.1	μAdc
MJE15028, MJĒ15029 (V _{CB} = 150 Vdc, I _E = 0) MJE15030, MJE15031		-	10 10	
Emitter Cutoff Current $(V_{BE} = 5.0 \text{ Vdc}, I_C = 0)$	I _{EBO}	_	10	μAdc
ON CHARACTERISTICS (Note 1)				
	h _{FE}	40 40 40 20	- - - -	_
DC Current Gain Linearity (V _{CE} From 2.0 V to 20 V, I _C From 0.1 A to 3 A) (NPN to PNP)	h _{FE}	Т		
Collector-Emitter Saturation Voltage $(I_{C} = 1.0 \text{ Adc}, I_{B} = 0.1 \text{ Adc})$	V _{CE(sat)}	_	0.5	Vdc
Base-Emitter On Voltage (I _C = 1.0 Adc, V _{CE} = 2.0 Vdc)	V _{BE(on)}	_	1.0	Vdc
DYNAMIC CHARACTERISTICS			•	·
	1 1		1	1

Current Gain – Bandwidth Product (Note 2)	f _T			MHz
$(I_C = 500 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f_{test} = 10 \text{ MHz})$		30	-	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 1. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

2. $f_T = |h_{fe}| \cdot f_{test}$.

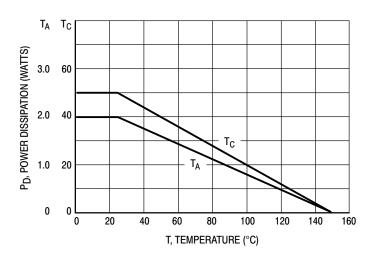


Figure 1. Power Derating

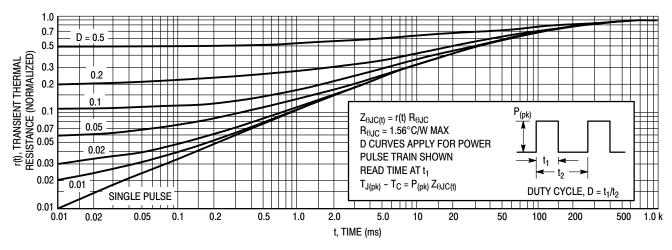


Figure 2. Thermal Response

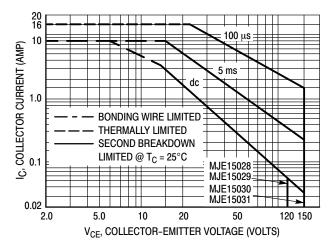
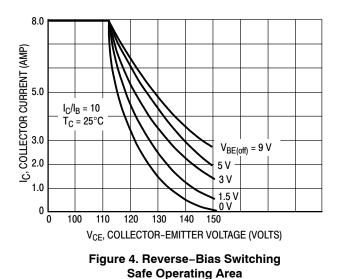


Figure 3. Forward Bias Safe Operating Area



There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation, i.e., the transistor must not be subjected to greater dissipation then the curves indicate.

The data of Figures 3 and 4 is based on $T_{J(pk)} = 150^{\circ}$ C; T_{C} is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)}$ < 150°C. $T_{J(pk)}$ may be calculated from the data in Figure 2. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

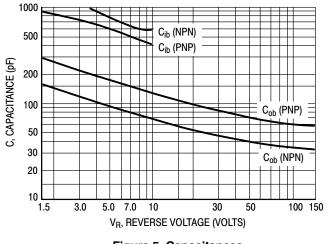
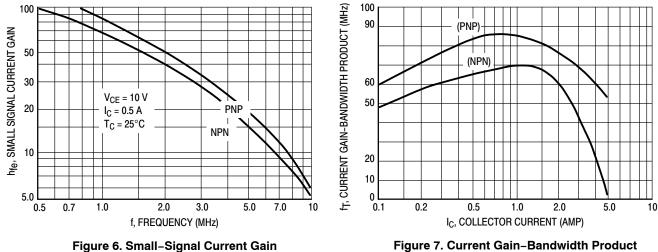


Figure 5. Capacitances



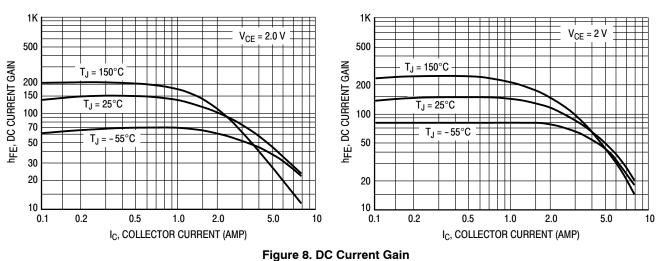


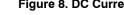
NPN — MJE15028 MJE15030

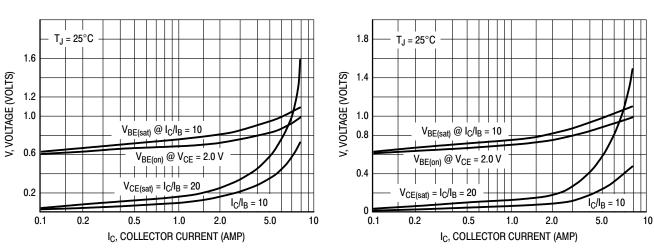
NPN

PNP — MJE15029 MJE15031

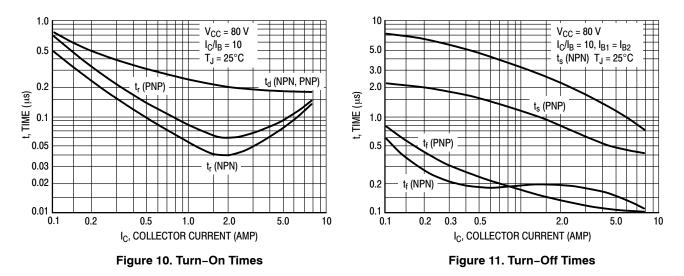
PNP











ORDERING INFORMATION

Device	Package	Shipping
MJE15028G	TO-220 (Pb-Free)	50 Units / Rail
MJE15029G	TO-220 (Pb-Free)	50 Units / Rail
MJE15030G	TO-220 (Pb-Free)	50 Units / Rail
MJE15031G	TO-220 (Pb-Free)	50 Units / Rail

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, <u>BRD8011/D</u>.

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	TO-22 CASE 2 ISSUE	21A					DATE	13 JAN 2022
SCALE 1:1		PLANE 1 2 3.	. CONT . DIMEI LEA	ROLLING D NSION Z DE D IRREGUL/ WIDTH FOR	AND TOLERAI IMENSION: IN FINES A ZONI ARITIES ARE A F102 DEVICE	NCHES E WHERE AL ALLOWED. E = 1.35MM	L BODY AND	
A A				INC	1	MILLIM		
	Ŭ		DIM	MIN.	MAX.	MIN.	MAX.	
1 2 3			A	0.570	0.620	14.48	15.75	
			B	0.380	0.415	9.66	10.53	
<u>╄</u> <u></u>			C D	0.160	0.190	4.07	4.83	
			F	0.025	0.038	0.64 3.60	0.96 4.09	
Z-J K			G	0.095	0.101	2.42	2.66	
			н	0.110	0.161	2.42	4.10	
				0.014	0.024	0.36	0.61	
			ĸ	0.500	0.562	12.70	14.27	
∨4	R —		L	0.045	0.060	1.15	1.52	
G	J → →		N	0.190	0.210	4.83	5.33	
D			Q	0.100	0.120	2.54	3.04	
N			R	0.080	0.110	2.04	2.79	
			s	0.045	0.055	1.15	1.41	
			т	0.235	0.255	5.97	6.47	
			U	0.000	0.050	0.00	1.27	
			V	0.045		1.15		
			Z		0.080		2.04	
STYLE 1: PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR	STYLE 2: PIN 1. BASE 2. EMITTER 3. COLLECTOR 4. EMITTER	3. 0	CATHODI NODE GATE NODE		2. MA 3. GA	in terminal In terminal Te In terminal	.2	
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STYLE 9: PIN 1. GATE 2. COLLECTOR 3. EMITTER 4. COLLECTOR	STYLE 10: PIN 1. GATE 2. SOURCE 3. DRAIN 4. SOURCE	3. 0	OURCE		2. MA 3. GA	NIN TERMINAL NIN TERMINAL TE DT CONNECTI	.2	

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 DESCRIPTION:
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