

MBT3946DW1T1G, SMBT3946DW1T1G

Complementary General Purpose Transistor

The MBT3946DW1T1G device is a spin-off of our popular SOT-23/SOT-323 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-363-6 surface mount package. By putting two discrete devices in one package, this device is ideal for low-power surface mount applications where board space is at a premium.

Features

- h_{FE} , 100–300
- Low $V_{CE(sat)}$, ≤ 0.4 V
- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Table 1. MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|--|-----------|---------------------------|------|
| Collector – Emitter Voltage (NPN) (PNP) | V_{CEO} | 40 –40 | Vdc |
| Collector – Base Voltage (NPN) (PNP) | V_{CBO} | 60 –40 | Vdc |
| Emitter – Base Voltage (NPN) (PNP) | V_{EBO} | 6.0 –5.0 | Vdc |
| Collector Current – Continuous (NPN) (PNP) | I_C | 200 –200 | mAdc |
| Electrostatic Discharge | ESD | HBM Class 2 MM Class B | |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Table 2. THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|--|-----------------|-------------|--------------------|
| Total Package Dissipation (Note 1) $T_A = 25^\circ\text{C}$ | P_D | 150 | mW |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 833 | $^\circ\text{C/W}$ |
| Junction and Storage Temperature Range | T_J, T_{stg} | –55 to +150 | $^\circ\text{C}$ |

1. Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.

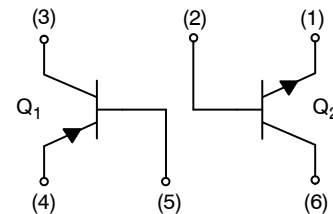


ON Semiconductor®

<http://onsemi.com>

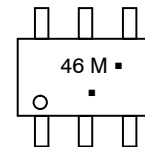


**SOT-363/SC-88
CASE 419B
STYLE 1**



MBT3946DW1T1*
*Q1 PNP
Q2 NPN

MARKING DIAGRAM



46 = Specific Device Code
M = Date Code
▪ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

| Device | Package | Shipping† |
|----------------|--------------------|------------------------|
| MBT3946DW1T1G | SC-88 (Pb-Free) | 3,000 / Tape & Reel |
| SMBT3946DW1T1G | SC-88 (Pb-Free) | 3,000 / Tape & Reel |
| MBT3946DW1T2G | SC-88 (Pb-Free) | 3,000 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MBT3946DW1T1G, SMBT3946DW1T1G

Table 3. ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

| Characteristic | Symbol | Min | Max | Unit |
|---|--|---------------|--|--|
| OFF CHARACTERISTICS | | | | |
| Collector–Emitter Breakdown Voltage (Note 2) ($I_C = 1.0\text{ mAdc}$, $I_B = 0$) ($I_C = -1.0\text{ mAdc}$, $I_B = 0$) | (NPN) (PNP) | $V_{(BR)CEO}$ | 40 –40 | – – Vdc |
| Collector–Base Breakdown Voltage ($I_C = 10\text{ }\mu\text{Adc}$, $I_E = 0$) ($I_C = -10\text{ }\mu\text{Adc}$, $I_E = 0$) | (NPN) (PNP) | $V_{(BR)CBO}$ | 60 –40 | – – Vdc |
| Emitter–Base Breakdown Voltage ($I_E = 10\text{ }\mu\text{Adc}$, $I_C = 0$) ($I_E = -10\text{ }\mu\text{Adc}$, $I_C = 0$) | (NPN) (PNP) | $V_{(BR)EBO}$ | 6.0 –5.0 | – – Vdc |
| Base Cutoff Current ($V_{CE} = 30\text{ Vdc}$, $V_{EB} = 3.0\text{ Vdc}$) ($V_{CE} = -30\text{ Vdc}$, $V_{EB} = -3.0\text{ Vdc}$) | (NPN) (PNP) | I_{BL} | – – | 50 –50 nAdc |
| Collector Cutoff Current ($V_{CE} = 30\text{ Vdc}$, $V_{EB} = 3.0\text{ Vdc}$) ($V_{CE} = -30\text{ Vdc}$, $V_{EB} = -3.0\text{ Vdc}$) | (NPN) (PNP) | I_{CEX} | – – | 50 –50 nAdc |
| ON CHARACTERISTICS (Note 2) | | | | |
| DC Current Gain ($I_C = 0.1\text{ mAdc}$, $V_{CE} = 1.0\text{ Vdc}$) ($I_C = 1.0\text{ mAdc}$, $V_{CE} = 1.0\text{ Vdc}$) ($I_C = 10\text{ mAdc}$, $V_{CE} = 1.0\text{ Vdc}$) ($I_C = 50\text{ mAdc}$, $V_{CE} = 1.0\text{ Vdc}$) ($I_C = 100\text{ mAdc}$, $V_{CE} = 1.0\text{ Vdc}$) ($I_C = -0.1\text{ mAdc}$, $V_{CE} = -1.0\text{ Vdc}$) ($I_C = -1.0\text{ mAdc}$, $V_{CE} = -1.0\text{ Vdc}$) ($I_C = -10\text{ mAdc}$, $V_{CE} = -1.0\text{ Vdc}$) ($I_C = -50\text{ mAdc}$, $V_{CE} = -1.0\text{ Vdc}$) ($I_C = -100\text{ mAdc}$, $V_{CE} = -1.0\text{ Vdc}$) | (NPN) (PNP) | h_{FE} | 40 70 100 60 30 60 80 100 60 30 | – – 300 – – – – 300 – – |
| Collector–Emitter Saturation Voltage ($I_C = 10\text{ mAdc}$, $I_B = 1.0\text{ mAdc}$) ($I_C = 50\text{ mAdc}$, $I_B = 5.0\text{ mAdc}$) ($I_C = -10\text{ mAdc}$, $I_B = -1.0\text{ mAdc}$) ($I_C = -50\text{ mAdc}$, $I_B = -5.0\text{ mAdc}$) | (NPN) (PNP) | $V_{CE(sat)}$ | – – – – | 0.2 0.3 –0.25 –0.4 Vdc |
| Base–Emitter Saturation Voltage ($I_C = 10\text{ mAdc}$, $I_B = 1.0\text{ mAdc}$) ($I_C = 50\text{ mAdc}$, $I_B = 5.0\text{ mAdc}$) ($I_C = -10\text{ mAdc}$, $I_B = -1.0\text{ mAdc}$) ($I_C = -50\text{ mAdc}$, $I_B = -5.0\text{ mAdc}$) | (NPN) (PNP) | $V_{BE(sat)}$ | 0.65 – –0.65 – | 0.85 0.95 –0.85 –0.95 Vdc |
| SMALL-SIGNAL CHARACTERISTICS | | | | |
| Current–Gain – Bandwidth Product ($I_C = 10\text{ mAdc}$, $V_{CE} = 20\text{ Vdc}$, $f = 100\text{ MHz}$) ($I_C = -10\text{ mAdc}$, $V_{CE} = -20\text{ Vdc}$, $f = 100\text{ MHz}$) | (NPN) (PNP) | f_T | 300 250 | – – MHz |
| Output Capacitance ($V_{CB} = 5.0\text{ Vdc}$, $I_E = 0$, $f = 1.0\text{ MHz}$) ($V_{CB} = -5.0\text{ Vdc}$, $I_E = 0$, $f = 1.0\text{ MHz}$) | (NPN) (PNP) | C_{obo} | – – | 4.0 4.5 pF |
| Input Capacitance ($V_{EB} = 0.5\text{ Vdc}$, $I_C = 0$, $f = 1.0\text{ MHz}$) ($V_{EB} = -0.5\text{ Vdc}$, $I_C = 0$, $f = 1.0\text{ MHz}$) | (NPN) (PNP) | C_{ibo} | – – | 8.0 10.0 pF |
| Input Impedance ($V_{CE} = 10\text{ Vdc}$, $I_C = 1.0\text{ mAdc}$, $f = 1.0\text{ kHz}$) ($V_{CE} = -10\text{ Vdc}$, $I_C = -1.0\text{ mAdc}$, $f = 1.0\text{ kHz}$) | (NPN) (PNP) | h_{ie} | 1.0 2.0 | 10 12 k Ω |
| Voltage Feedback Ratio ($V_{CE} = 10\text{ Vdc}$, $I_C = 1.0\text{ mAdc}$, $f = 1.0\text{ kHz}$) ($V_{CE} = -10\text{ Vdc}$, $I_C = -1.0\text{ mAdc}$, $f = 1.0\text{ kHz}$) | (NPN) (PNP) | h_{re} | 0.5 0.1 | 8.0 10 X 10 ^{–4} |
| Small–Signal Current Gain ($V_{CE} = 10\text{ Vdc}$, $I_C = 1.0\text{ mAdc}$, $f = 1.0\text{ kHz}$) ($V_{CE} = -10\text{ Vdc}$, $I_C = -1.0\text{ mAdc}$, $f = 1.0\text{ kHz}$) | (NPN) (PNP) | h_{fe} | 100 100 | 400 400 – |

MBT3946DW1T1G, SMBT3946DW1T1G

Table 4. ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted) (continued)

| Characteristic | Symbol | Min | Max | Unit |
|--|----------------------------|------------|------------|------------------|
| Output Admittance ($V_{CE} = 10\text{ Vdc}$, $I_C = 1.0\text{ mAdc}$, $f = 1.0\text{ kHz}$) ($V_{CE} = -10\text{ Vdc}$, $I_C = -1.0\text{ mAdc}$, $f = 1.0\text{ kHz}$) | h_{oe} (NPN) (PNP) | 1.0 3.0 | 40 60 | μmhos |
| Noise Figure ($V_{CE} = 5.0\text{ Vdc}$, $I_C = 100\ \mu\text{A}$, $R_S = 1.0\text{ k}\Omega$, $f = 1.0\text{ kHz}$) ($V_{CE} = -5.0\text{ Vdc}$, $I_C = -100\ \mu\text{A}$, $R_S = 1.0\text{ k}\Omega$, $f = 1.0\text{ kHz}$) | NF (NPN) (PNP) | - - | 5.0 4.0 | dB |

SWITCHING CHARACTERISTICS

| | | | | | |
|--|----------------|-------|--------|------------|----|
| Delay Time ($V_{CC} = 3.0\text{ Vdc}$, $V_{BE} = -0.5\text{ Vdc}$) ($V_{CC} = -3.0\text{ Vdc}$, $V_{BE} = 0.5\text{ Vdc}$) | (NPN) (PNP) | t_d | - - | 35 35 | ns |
| Rise Time ($I_C = 10\text{ mAdc}$, $I_{B1} = 1.0\text{ mAdc}$) ($I_C = -10\text{ mAdc}$, $I_{B1} = -1.0\text{ mAdc}$) | (NPN) (PNP) | t_r | - - | 35 35 | |
| Storage Time ($V_{CC} = 3.0\text{ Vdc}$, $I_C = 10\text{ mAdc}$) ($V_{CC} = -3.0\text{ Vdc}$, $I_C = -10\text{ mAdc}$) | (NPN) (PNP) | t_s | - - | 200 225 | ns |
| Fall Time ($I_{B1} = I_{B2} = 1.0\text{ mAdc}$) ($I_{B1} = I_{B2} = -1.0\text{ mAdc}$) | (NPN) (PNP) | t_f | - - | 50 75 | |

2. Pulse Test: Pulse Width $\leq 300\ \mu\text{s}$; Duty Cycle $\leq 2.0\%$.

(NPN)

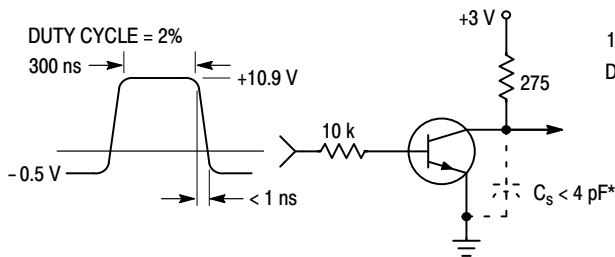


Figure 1. Delay and Rise Time Equivalent Test Circuit

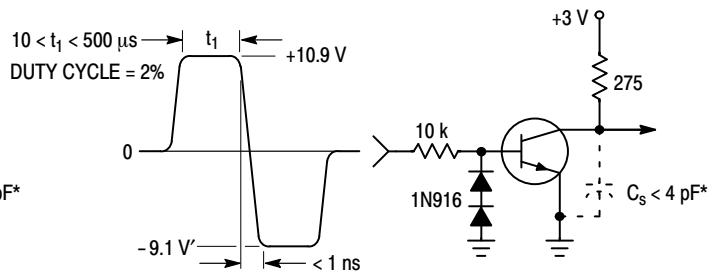


Figure 2. Storage and Fall Time Equivalent Test Circuit

* Total shunt capacitance of test jig and connectors

TYPICAL TRANSIENT CHARACTERISTICS

— $T_J = 25^\circ\text{C}$
- - - $T_J = 125^\circ\text{C}$

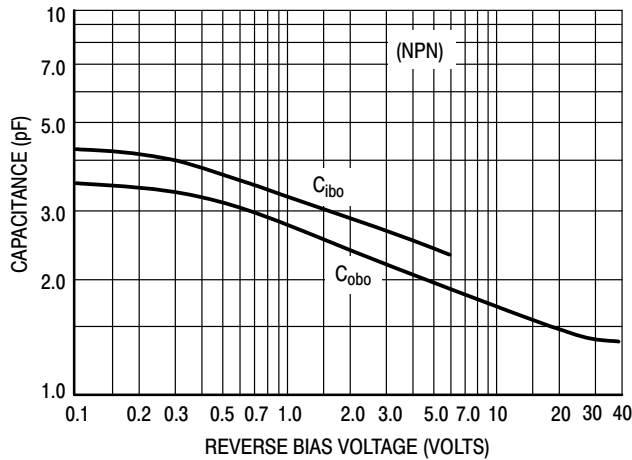


Figure 3. Capacitance

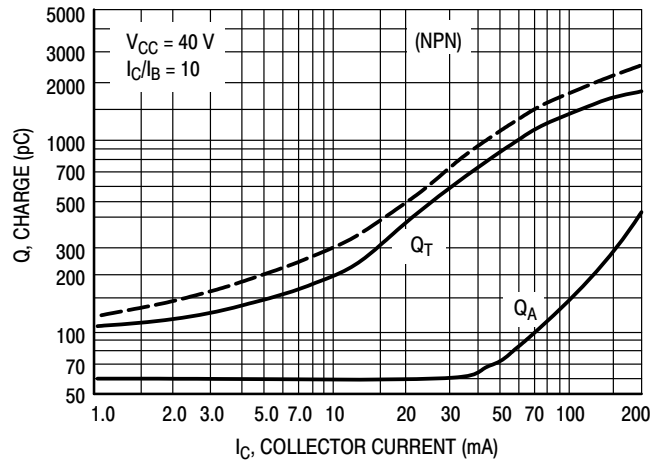


Figure 4. Charge Data

MBT3946DW1T1G, SMBT3946DW1T1G

(NPN)

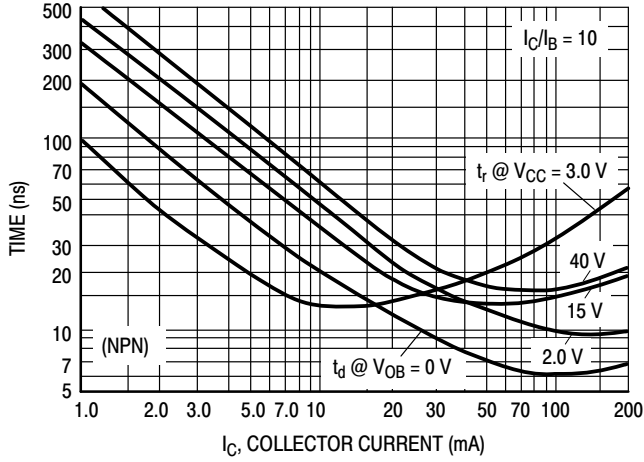


Figure 5. Turn-On Time

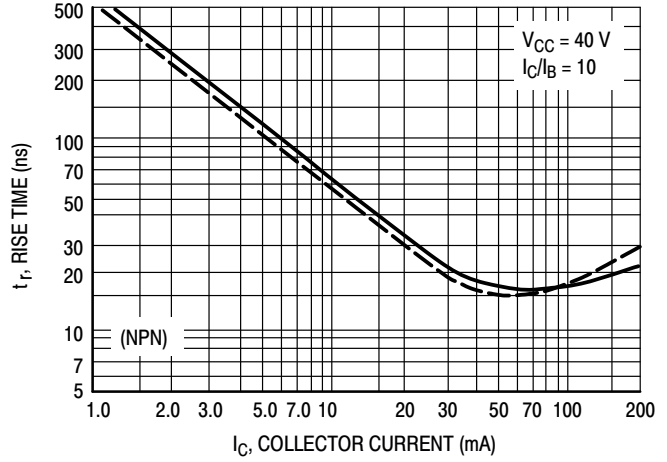


Figure 6. Rise Time

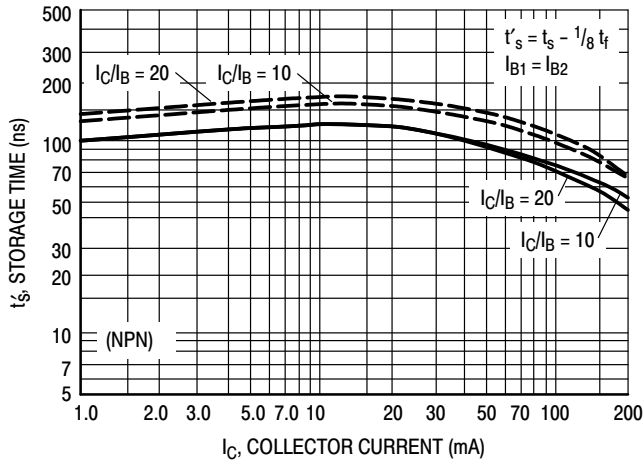


Figure 7. Storage Time

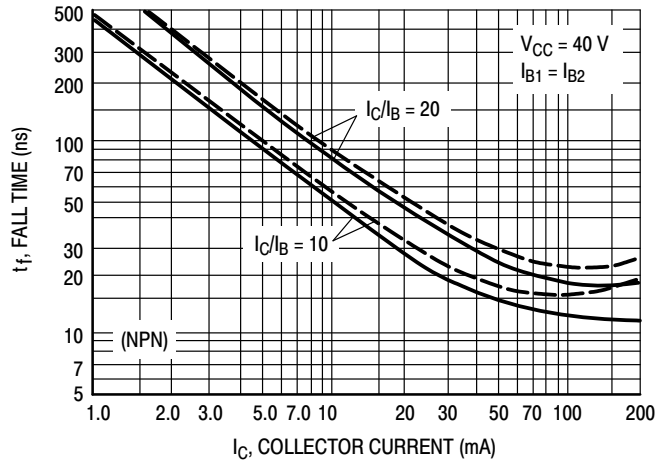


Figure 8. Fall Time

TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

($V_{CE} = 5.0 \text{ Vdc}$, $T_A = 25^\circ\text{C}$, Bandwidth = 1.0 Hz)

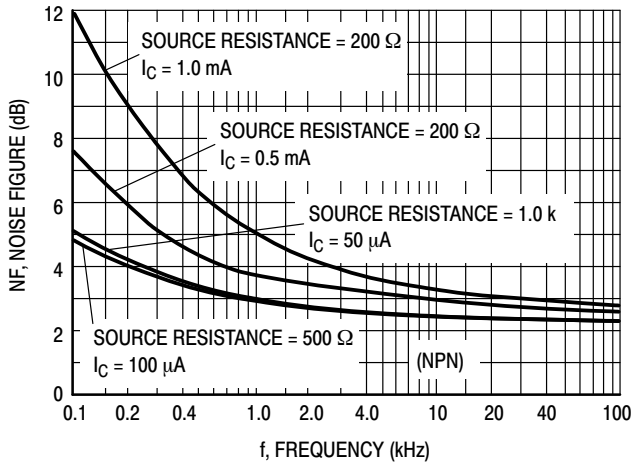


Figure 9. Noise Figure

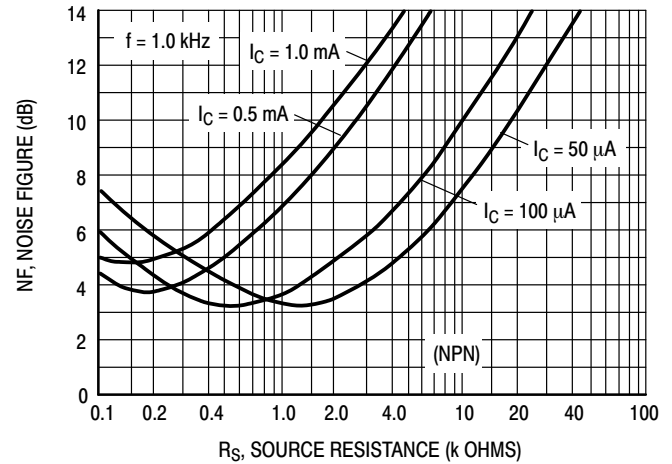


Figure 10. Noise Figure

MBT3946DW1T1G, SMBT3946DW1T1G

(NPN) h PARAMETERS

($V_{CE} = 10 \text{ Vdc}$, $f = 1.0 \text{ kHz}$, $T_A = 25^\circ\text{C}$)

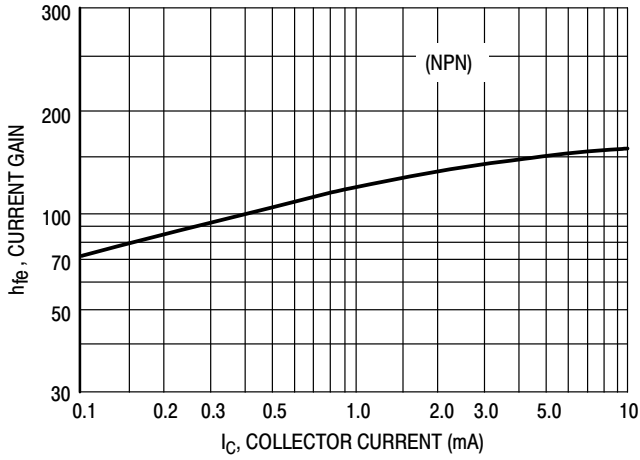


Figure 11. Current Gain

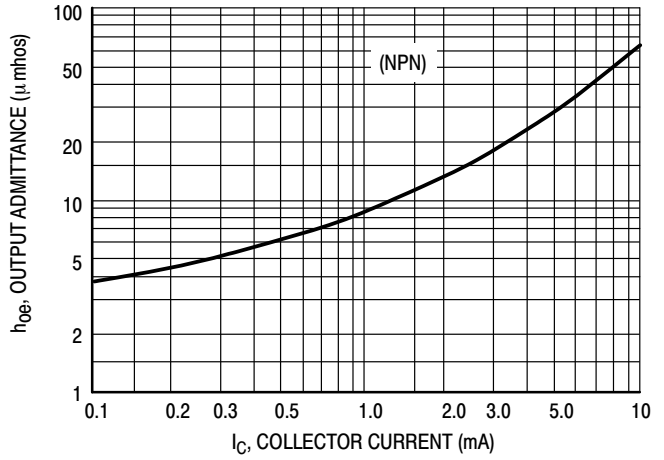


Figure 12. Output Admittance

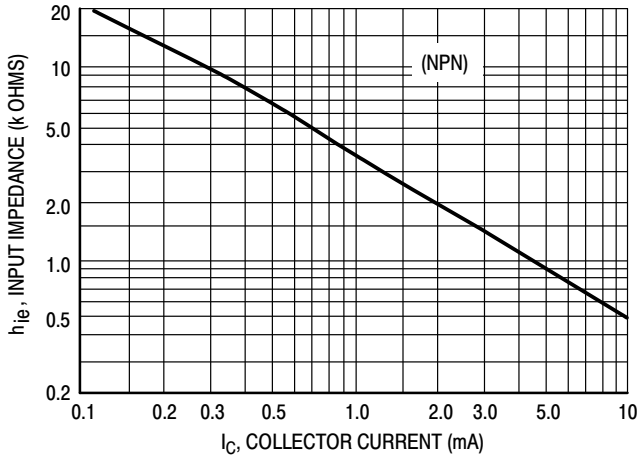


Figure 13. Input Impedance

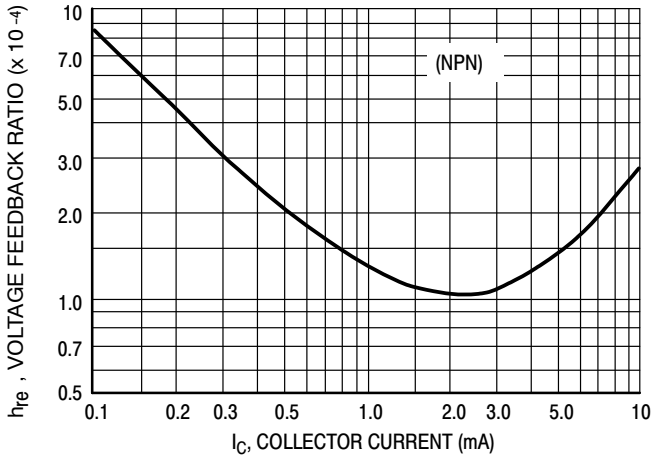


Figure 14. Voltage Feedback Ratio

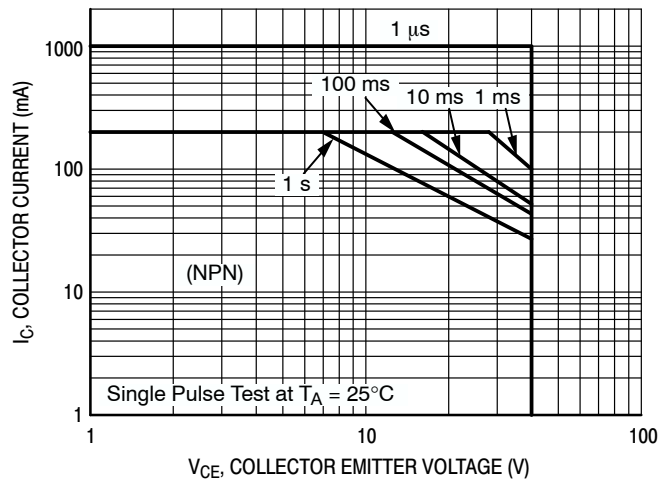


Figure 15. Safe Operating Area

MBT3946DW1T1G, SMBT3946DW1T1G

(NPN) TYPICAL STATIC CHARACTERISTICS



Figure 16. DC Current Gain

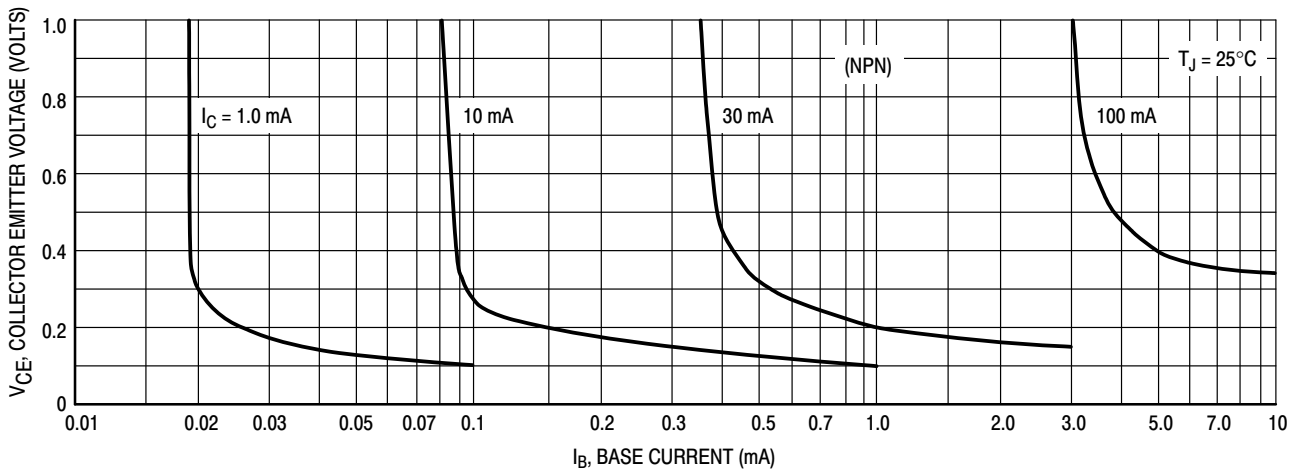


Figure 17. Collector Saturation Region

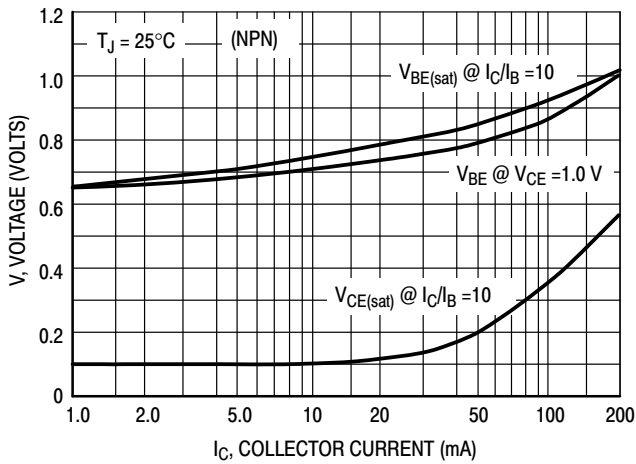


Figure 18. "ON" Voltages

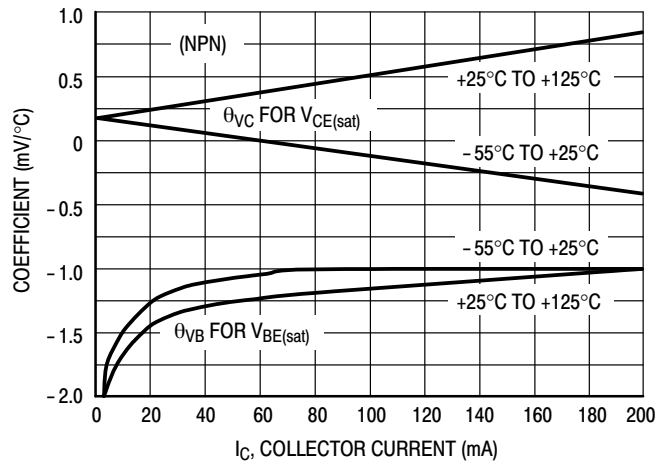


Figure 19. Temperature Coefficients

MBT3946DW1T1G, SMBT3946DW1T1G

(PNP)

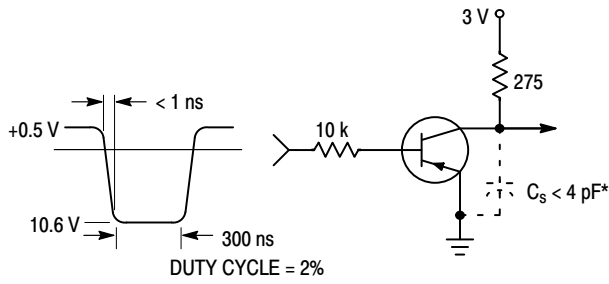


Figure 20. Delay and Rise Time Equivalent Test Circuit

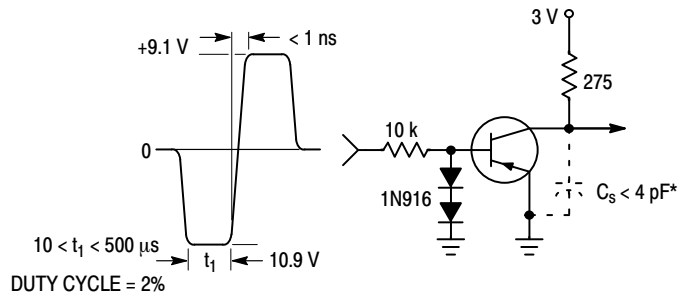


Figure 21. Storage and Fall Time Equivalent Test Circuit

* Total shunt capacitance of test jig and connectors

TYPICAL TRANSIENT CHARACTERISTICS

— $T_J = 25^\circ\text{C}$
 - - - $T_J = 125^\circ\text{C}$

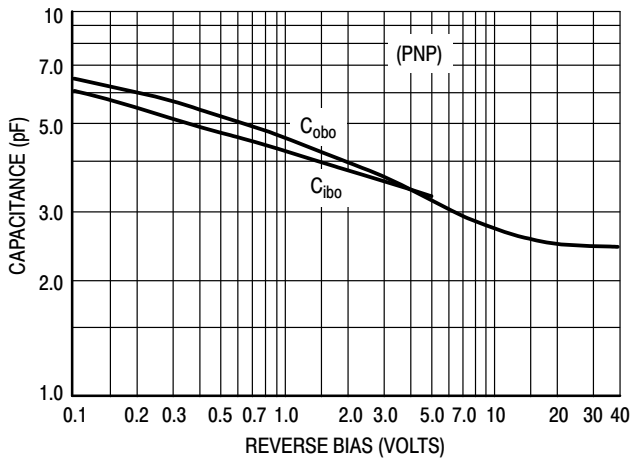


Figure 22. Capacitance

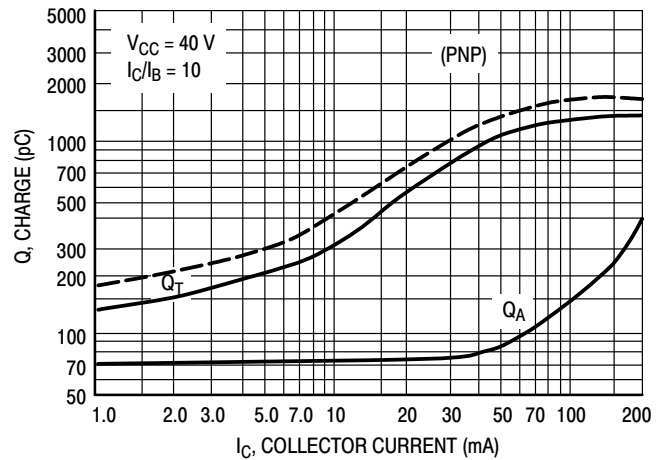


Figure 23. Charge Data

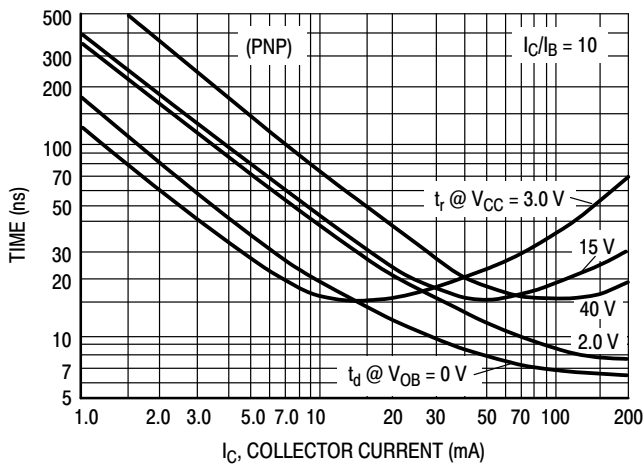


Figure 24. Turn-On Time

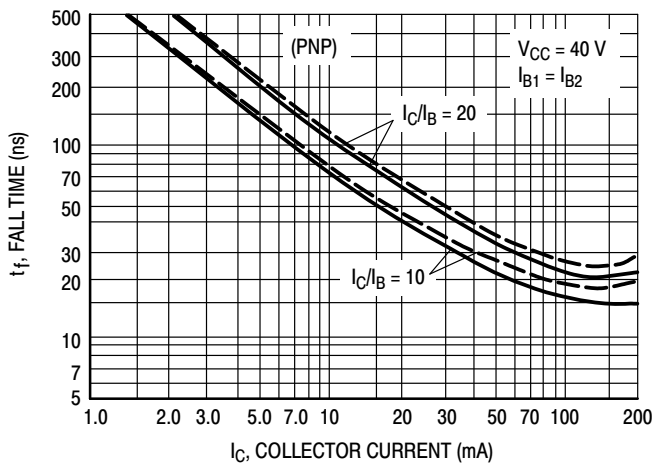


Figure 25. Fall Time

MBT3946DW1T1G, SMBT3946DW1T1G

(PNP)

TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

($V_{CE} = -5.0$ Vdc, $T_A = 25^\circ\text{C}$, Bandwidth = 1.0 Hz)



Figure 26.

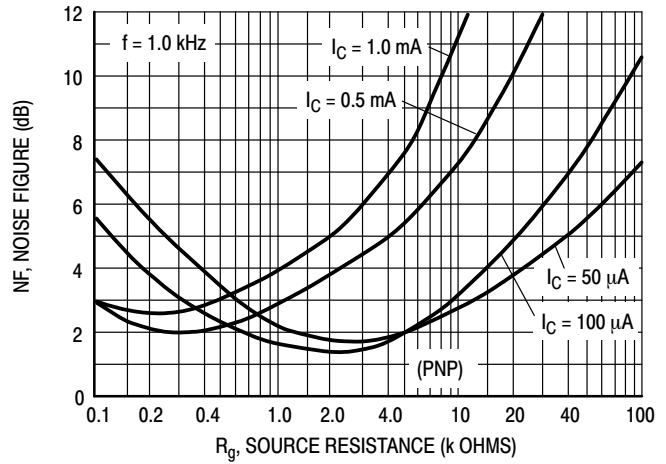


Figure 27.

h PARAMETERS

($V_{CE} = -10$ Vdc, $f = 1.0$ kHz, $T_A = 25^\circ\text{C}$)

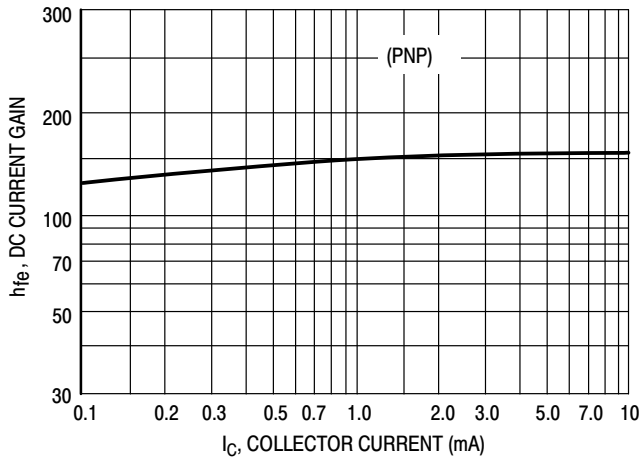


Figure 28. Current Gain

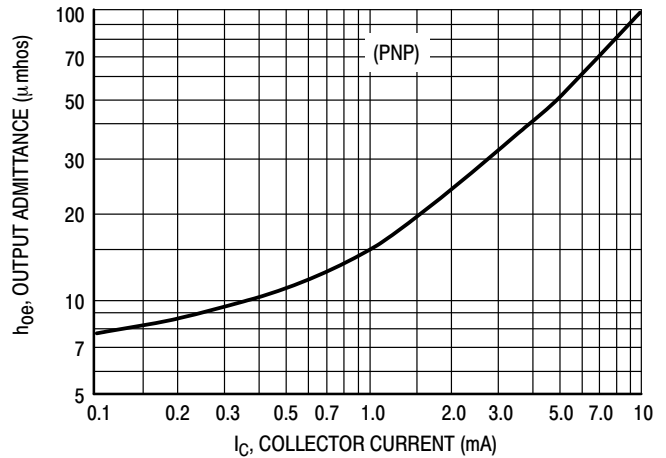


Figure 29. Output Admittance

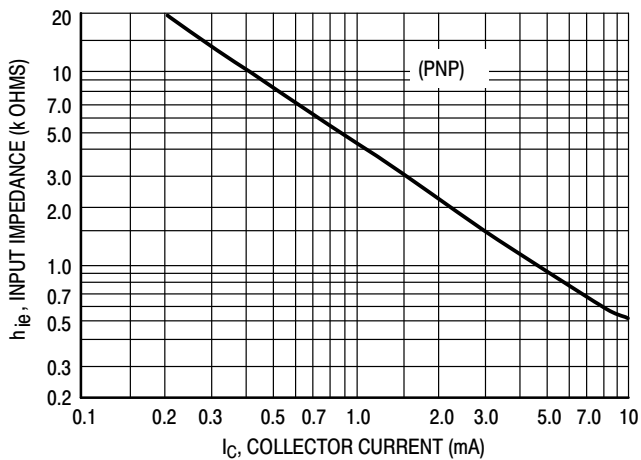


Figure 30. Input Impedance

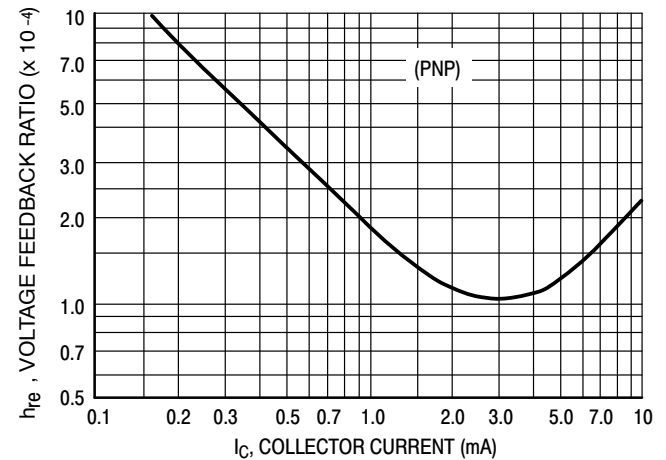


Figure 31. Voltage Feedback Ratio

MBT3946DW1T1G, SMT3946DW1T1G

(PNP)

TYPICAL STATIC CHARACTERISTICS

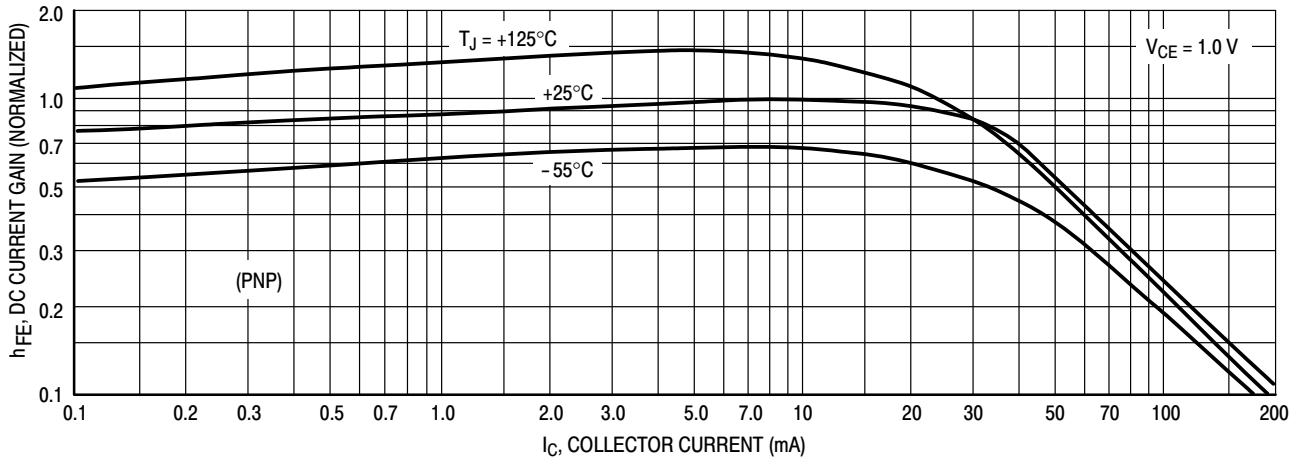


Figure 32. DC Current Gain

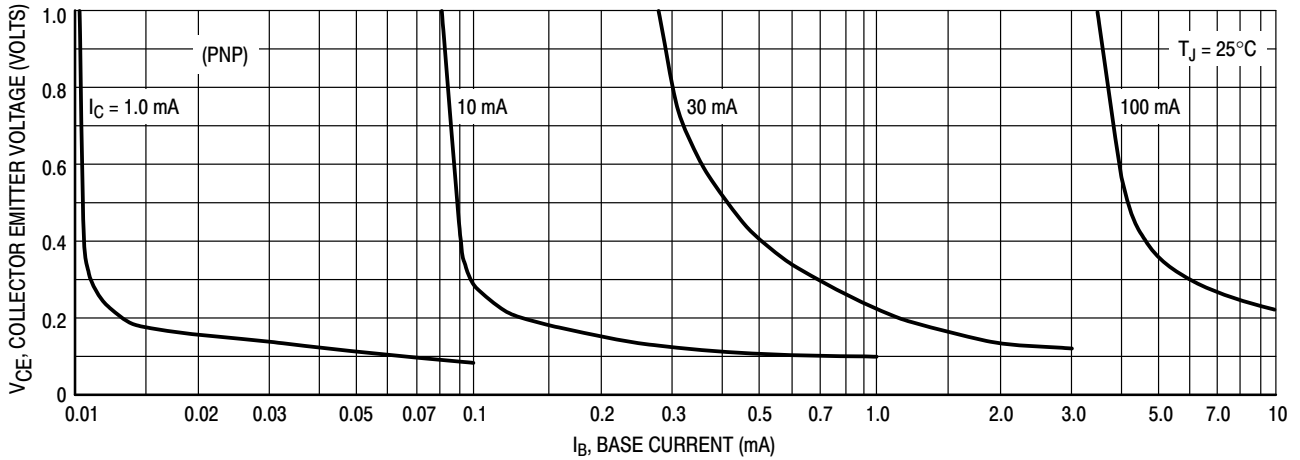


Figure 33. Collector Saturation Region

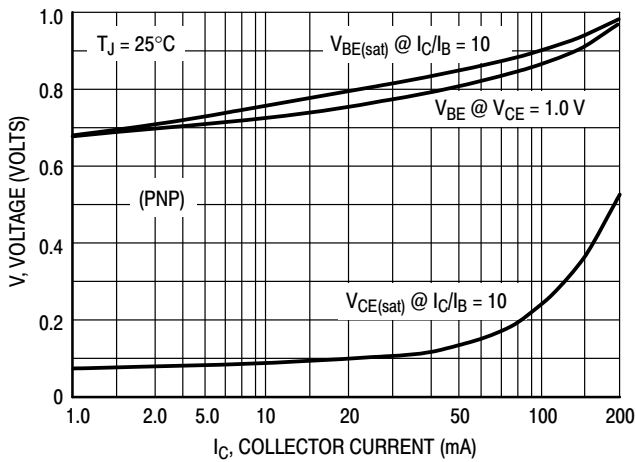


Figure 34. "ON" Voltages

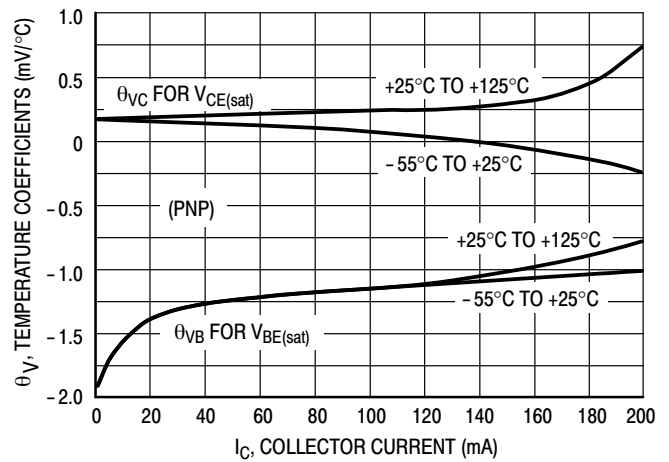


Figure 35. Temperature Coefficients

MBT3946DW1T1G, SMBT3946DW1T1G

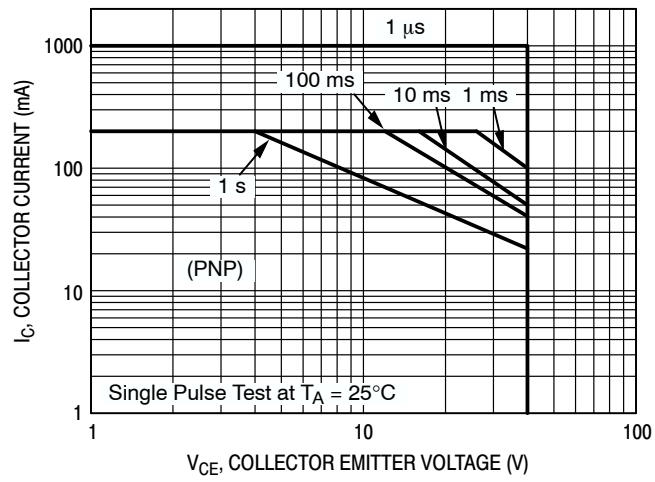


Figure 36. Safe Operating Area

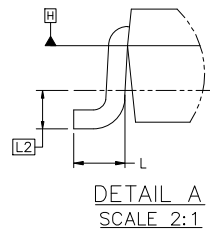
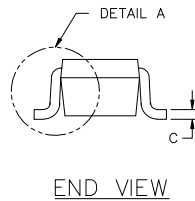
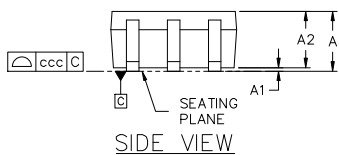
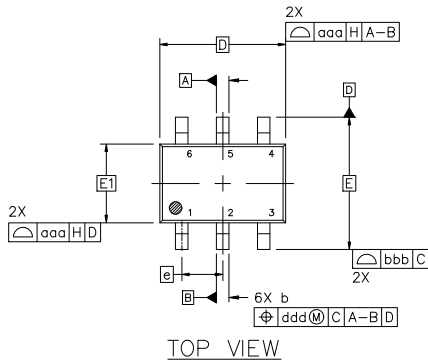


SC-88 2.00x1.25x0.90, 0.65P
CASE 419B-02
ISSUE Z

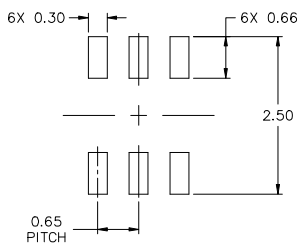
DATE 18 APR 2024

NOTES:

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
2. ALL DIMENSION ARE IN MILLIMETERS.
3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
4. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AND DATUM H.
5. DATUMS A AND B ARE DETERMINED AT DATUM H.
6. DIMENSIONS b AND c APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
7. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION b AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.

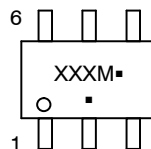


| DIM | MILLIMETERS | | |
|-----|-------------|------|------|
| | MIN. | NOM. | MAX. |
| A | --- | --- | 1.10 |
| A1 | 0.00 | --- | 0.10 |
| A2 | 0.70 | 0.90 | 1.00 |
| b | 0.15 | 0.20 | 0.25 |
| c | 0.08 | 0.15 | 0.22 |
| D | 2.00 BSC | | |
| E | 2.10 BSC | | |
| E1 | 1.25 BSC | | |
| e | 0.65 BSC | | |
| L | 0.26 | 0.36 | 0.46 |
| L2 | 0.15 BSC | | |
| aaa | 0.15 | | |
| bbb | 0.30 | | |
| ccc | 0.10 | | |
| ddd | 0.10 | | |



* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ONSEMI SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code
M = Date Code*
▪ = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or position may vary depending upon manufacturing location.

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

STYLES ON PAGE 2

| | | |
|------------------|-----------------------------|--|
| DOCUMENT NUMBER: | 98ASB42985B | Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| DESCRIPTION: | SC-88 2.00x1.25x0.90, 0.65P | PAGE 1 OF 2 |

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

SC-88 2.00x1.25x0.90, 0.65P
CASE 419B-02
ISSUE Z

DATE 18 APR 2024

| | | | | | |
|---|---|--|--|--|--|
| STYLE 1: PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2 | STYLE 2: CANCELLED | STYLE 3: CANCELLED | STYLE 4: PIN 1. CATHODE 2. CATHODE 3. COLLECTOR 4. EMITTER 5. BASE 6. ANODE | STYLE 5: PIN 1. ANODE 2. ANODE 3. COLLECTOR 4. EMITTER 5. BASE 6. CATHODE | STYLE 6: PIN 1. ANODE 2 2. N/C 3. CATHODE 1 4. ANODE 1 5. N/C 6. CATHODE 2 |
| STYLE 7: PIN 1. SOURCE 2 2. DRAIN 2 3. GATE 1 4. SOURCE 1 5. DRAIN 1 6. GATE 2 | STYLE 8: CANCELLED | STYLE 9: PIN 1. EMITTER 2 2. EMITTER 1 3. COLLECTOR 1 4. BASE 1 5. BASE 2 6. COLLECTOR 2 | STYLE 10: PIN 1. SOURCE 2 2. SOURCE 1 3. GATE 1 4. DRAIN 1 5. DRAIN 2 6. GATE 2 | STYLE 11: PIN 1. CATHODE 2 2. CATHODE 2 3. ANODE 1 4. CATHODE 1 5. CATHODE 1 6. ANODE 2 | STYLE 12: PIN 1. ANODE 2 2. ANODE 2 3. CATHODE 1 4. ANODE 1 5. ANODE 1 6. CATHODE 2 |
| STYLE 13: PIN 1. ANODE 2. N/C 3. COLLECTOR 4. EMITTER 5. BASE 6. CATHODE | STYLE 14: PIN 1. VREF 2. GND 3. GND 4. IOUT 5. VEN 6. VCC | STYLE 15: PIN 1. ANODE 1 2. ANODE 2 3. ANODE 3 4. CATHODE 3 5. CATHODE 2 6. CATHODE 1 | STYLE 16: PIN 1. BASE 1 2. EMITTER 2 3. COLLECTOR 2 4. BASE 2 5. EMITTER 1 6. COLLECTOR 1 | STYLE 17: PIN 1. BASE 1 2. EMITTER 1 3. COLLECTOR 2 4. BASE 2 5. EMITTER 2 6. COLLECTOR 1 | STYLE 18: PIN 1. VIN1 2. VCC 3. VOUT2 4. VIN2 5. GND 6. VOUT1 |
| STYLE 19: PIN 1. IOUT 2. GND 3. GND 4. V CC 5. V EN 6. V REF | STYLE 20: PIN 1. COLLECTOR 2. COLLECTOR 3. BASE 4. EMITTER 5. COLLECTOR 6. COLLECTOR | STYLE 21: PIN 1. ANODE 1 2. N/C 3. ANODE 2 4. CATHODE 2 5. N/C 6. CATHODE 1 | STYLE 22: PIN 1. D1 (i) 2. GND 3. D2 (j) 4. D2 (c) 5. VBUS 6. D1 (c) | STYLE 23: PIN 1. Vn 2. CH1 3. Vp 4. N/C 5. CH2 6. N/C | STYLE 24: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. CATHODE 6. CATHODE |
| STYLE 25: PIN 1. BASE 1 2. CATHODE 3. COLLECTOR 2 4. BASE 2 5. EMITTER 6. COLLECTOR 1 | STYLE 26: PIN 1. SOURCE 1 2. GATE 1 3. DRAIN 2 4. SOURCE 2 5. GATE 2 6. DRAIN 1 | STYLE 27: PIN 1. BASE 2 2. BASE 1 3. COLLECTOR 1 4. EMITTER 1 5. EMITTER 2 6. COLLECTOR 2 | STYLE 28: PIN 1. DRAIN 2. DRAIN 3. GATE 4. SOURCE 5. DRAIN 6. DRAIN | STYLE 29: PIN 1. ANODE 2. ANODE 3. COLLECTOR 4. EMITTER 5. BASE/ANODE 6. CATHODE | STYLE 30: PIN 1. SOURCE 1 2. DRAIN 2 3. DRAIN 2 4. SOURCE 2 5. GATE 1 6. DRAIN 1 |

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

| | | |
|-------------------------|------------------------------------|---|
| DOCUMENT NUMBER: | 98ASB42985B | Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| DESCRIPTION: | SC-88 2.00x1.25x0.90, 0.65P | PAGE 2 OF 2 |

onsemi and **ONSEMI** are trademarks of Semiconductor Components Industries, LLC dba **onsemi** or its subsidiaries in the United States and/or other countries. **onsemi** reserves the right to make changes without further notice to any products herein. **onsemi** makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. **onsemi** does not convey any license under its patent rights nor the rights of others.

onsemi, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation
onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at www.onsemi.com/support/sales