

Ultralow V_F Ultrafast Rectifier, 8 A FRED Pt[®]





8 A

600 V

0.81 V

60 ns

175 °C

DPAK (TO-252AA)

Single

DPAK (TO-252AA)

 $I_{F(\underline{A}\underline{V})}$

 V_R

V_F at I_F

t_{rr} (typ.)

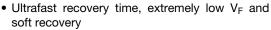
T_J max.

Package

Circuit configuration

PRIMARY CHARACTERISTICS

FEATURES





• 175 °C maximum operating junction temperature

• For PFC DCM operation

· Low leakage current

RoHS HALOGEN FREE

 Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

 Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

APPLICATIONS

AC/DC SMPS 70 W to 400 W e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

PARAMETER PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V _{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 158 °C	8	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	140	Α
Peak repetitive forward current	I _{FM}	T _C = 158 °C, f = 20 kHz, d = 50 %	16	
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-	.,	
E	V _F	I _F = 8 A	- 0.96	0.96	1.05	V	
Forward voltage	VF	I _F = 8 A, T _J = 150 °C	-	0.81	0.86		
Reverse leakage current I		$V_R = V_R$ rated	-	-	5		
		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	-	100	μΑ	
Junction capacitance	C _T	V _R = 600 V	-	8	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	1	8	-	nH	



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time		$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		1	87	-			
		$I_F = 1 A, dI_F/dt = 10$	$00 \text{ A/}\mu\text{s}, \text{ V}_{\text{R}} = 30 \text{ V}$	ı	60	100	ns A		
	t _{rr}	T _J = 25 °C	I _F = 8 A dI _F /dt = 200 A/μs V _B = 390 V	-	170	-			
		T _J = 125 °C		-	250	-			
Dook receivery ourrent	I _{RRM}	T _J = 25 °C		-	15	-			
Peak recovery current		T _J = 125 °C		-	20	-			
Reverse recovery charge	0	T _J = 25 °C	••	-	1.3	-	uC		
	Q _{rr}	T _J = 125 °C		-	2.6	-	uC		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}		-	1.8	2.2	°C/W	
Approximate weight			0.3		g		
Approximate weight			0.01			oz.	
Marking device		Case style DPAK (TO-252AA)	8EWL06FN				

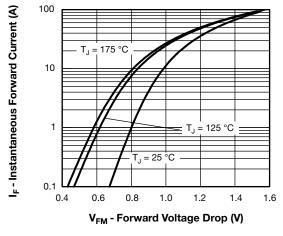


Fig. 1 - Typical Forward Voltage Drop Characteristics

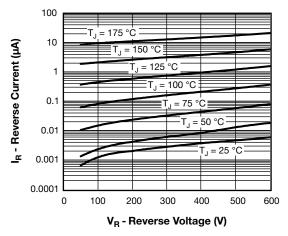


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

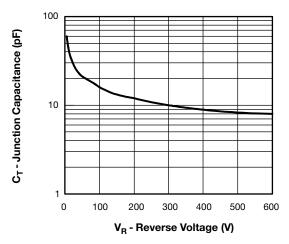


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

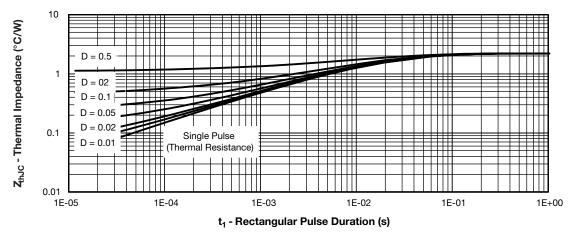


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

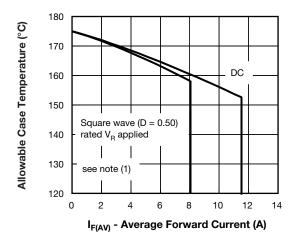


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

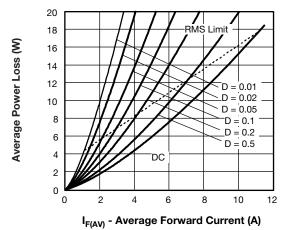


Fig. 6 - Forward Power Loss Characteristics

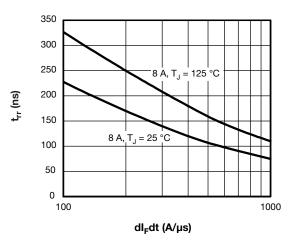


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

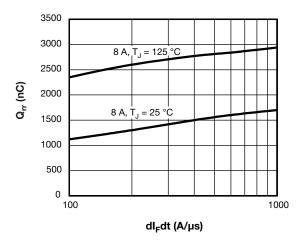
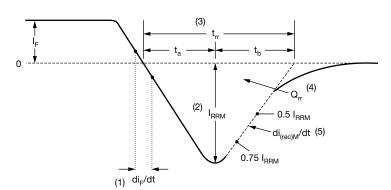


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

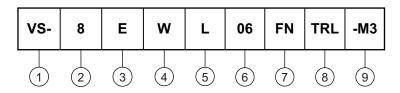
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE





1 - Vishay Semiconductors product

2 - Current rating (8 = 8 A)

3 - Circuit configuration:

E = Single diode

Package identifier:

W = D-PAK

5 - L = low V_F, fast recovery

6 - Voltage rating (06 = 600 V)

7 - FN = TO-252AA

8 - • None = tube

• TR = tape and reel

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

9 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION					
VS-8EWL06FN-M3	75	Antistatic plastic tube					
VS-8EWL06FNTR-M3	2000	13" diameter reel					
VS-8EWL06FNTRL-M3	3000	13" diameter reel					
VS-8EWL06FNTRR-M3	3000	13" diameter reel					

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95627					
Part marking information	www.vishay.com/doc?95176					
Packaging information	www.vishay.com/doc?95033					
SPICE model	www.vishay.com/doc?95373					



D-PAK (TO-252AA) "M"

DIMENSIONS in millimeters and inches



SYMBOL MILLIMETERS		IETERS	INC	NOTES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	-	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	5.21	-	0.205	1	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STINIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29	BSC	0.090	BSC	
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74 BSC		0.108 REF.		
L2	0.51	BSC	0.020 BSC		
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
Ø	0°	10°	0°	10°	
Ø1	0°	15°	0°	15°	
Ø2	25°	35°	25°	35°	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- (5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (6) Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- (8) Outline conforms to JEDEC® outline TO-252AA



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