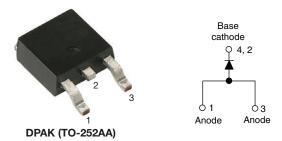
High Performance Schottky Rectifier, 10 A



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PRIMARY CHARACTERISTICS							
I _{F(AV)} 10 A							
V _R	45 V						
V _F at I _F	0.57 V						
I _{RM}	15 mA at 125 °C						
T _J max.	175 °C						
E _{AS}	20 mJ						
Package	DPAK (TO-252AA)						
Circuit configuration	Single						

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
 Decider DDA(c authors)
 Halogen
- Popular DPAK outline
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^\circ\mathrm{C}$
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-STPS1045B-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES							
I _{F(AV)}	Rectangular waveform	10	А						
V _{RRM}		45	V						
I _{FSM}	$t_p = 5 \ \mu s \ sine$	390	А						
V _F	10 A _{pk} , T _J = 125 °C	0.57	V						
TJ	Range	-40 to +175	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-STPS1045B-M3	UNITS						
Maximum DC reverse voltage	V _R	45	V						
Maximum working peak reverse voltage	V _{RWM}	40	v						

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at $T_C = 151$ °C	10						
Maximum peak one cycle non-repetitive surge current	Isou	I _{FSM} 5 μs sine or 3 μs rect. pulse 10 ms sine or 6 ms rect. pulse Following any rated load condition and with rated V _{RRM} applied		390	А				
See fig. 7	IFSM			75					
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 3.0 \ A, \ L = 4.4$	20	mJ					
Repetitive avalanche current	I _{AR}	Current decaying linearly to ze Frequency limited by T _J maxin	3.0	А					

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FREE



ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS						
		10 A	T.I = 25 °C	0.63	V				
Maximum forward voltage drop	V _{FM} ⁽¹⁾	20 A	1j=25 0	0.84					
See fig. 1	VFM (')	10 A	T 105 %C	0.57					
		20 A	T _J = 125 °C	0.72					
Maximum reverse leakage current	I _{BM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B} = Rated V_{\rm B}$	0.2	mA				
See fig. 2	IRM (")	T _J = 125 °C	$v_{\rm R}$ = Raied $v_{\rm R}$	15					
Typical junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range	760	pF					
Typical series inductance	L _S	Measured lead to lead 5 mm	5.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs					

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER SYMBOL TEST CONDITIONS VALUES									
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to +175	°C					
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W					
Maximum thermal resistance, junction to ambient	R _{thJA}		50	C/W					
Approximate weight			0.3	g					
Approximate weight			0.01	oz.					
Marking device		Case style DPAK (TO-252AA)	STPS10	STPS1045B					

Note

⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink





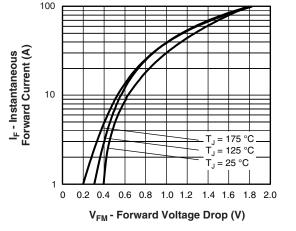


Fig. 1 - Maximum Forward Voltage Drop Characteristics

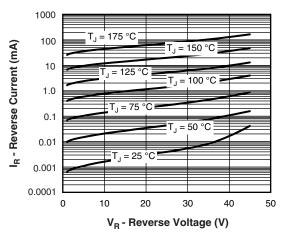


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

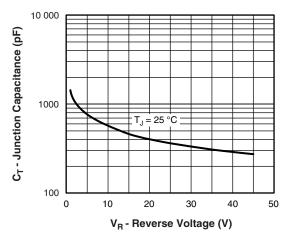
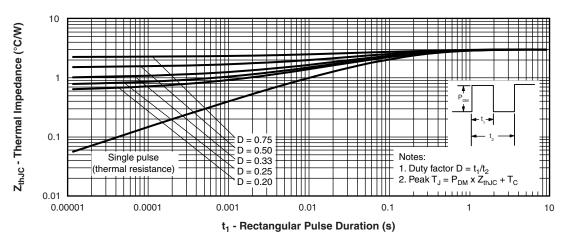


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage





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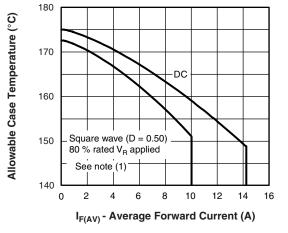
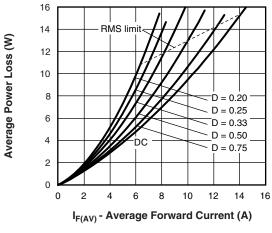


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current





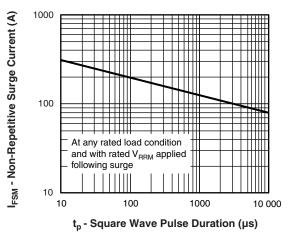


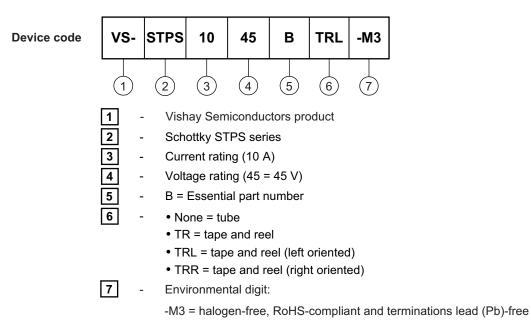
Fig. 7 - Maximum Non-Repetitive Surge Current

Note

 $^{(1)}$ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D); I_R$ at $V_{R1} = 80 \%$ rated V_R



ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-STPS1045B-M3	75	3000	Antistatic plastic tube						
VS-STPS1045BTR-M3	2000	2000	13" diameter reel						
VS-STPS1045BTRL-M3	3000	3000	13" diameter reel						
VS-STPS1045BTRR-M3	3000	3000	13" diameter reel						

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95627					
Part marking information	www.vishay.com/doc?95176					
Packaging information	www.vishay.com/doc?95033					





D-PAK (TO-252AA) "M"

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIDUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC	
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410	
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070	
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.	
b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC	
С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040	
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁶⁾ Dimension b1 and c1 applied to base metal only

⁽⁷⁾ Datum A and B to be determined at datum plane H

⁽⁸⁾ Outline conforms to JEDEC[®] outline TO-252AA



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