VS-E4PU3006LHN3

Vishay Semiconductors



Ultrafast Soft Recovery Diode, 30 A FRED Pt[®] Gen 4



PRODUCT SUMMARY								
Package	TO-247AD 2L							
I _{F(AV)}	30 A							
V _R	600 V							
V _F at I _F	1.19 V							
t _{rr} typ.	see Recovery table							
T _J max.	175 °C							
Diode variation	Single die							

FEATURES

- Gen 4 FRED Pt[®] technology
- Low I_{RRM} and reverse recovery charge
- Very low forward voltage drop
- Polyimide passivated chip for high reliability standard
- 175 °C operating junction temperature
- AEC-Q101 qualified, meets JESD 201 class 1
 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

Gen 4 Fred technology, state of the art, ultralow V_F, soft switching optimized for Discontinuous (Critical) Mode (DCM) and IGBT F/W diode.

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS						
Peak repetitive reverse voltage	V _{RRM}		600	V						
Average rectified forward current	I _{F(AV)}	T _C = 131 °C	30	^						
Non-repetitive peak surge current	I _{FSM}	T_C = 25 °C, t_p = 8.3 ms half sine wave	240	A						
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	°C						

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-					
		I _F = 30 A	-	1.36	1.6	v				
		I _F = 60 A	-	1.6	-					
Forward voltage	VF	I _F = 30 A, T _J = 125 °C	-	1.23	-					
i orward voltage	۷F	I _F = 60 A, T _J = 125 °C	-	1.5	-					
		I _F = 30 A, T _J = 150 °C	-	1.19	1.35					
		I _F = 60 A, T _J = 150 °C	-	1.48	-					
Reverse leakage current	1_	$V_{R} = V_{R}$ rated	-	-	50					
neverse leakage current	I _R	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	μA				
Junction capacitance	CT	V _R = 600 V	-	18.3	-	pF				

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RoHS

COMPLIANT

HALOGEN

FREE



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST C	MIN.	TYP.	MAX.	UNITS				
Reverse recovery time	+	T _J = 25 °C		-	65	-	ns			
	t _{rr}	T _J = 125 °C	I _F = 30 A dI _F /dt = 1000 A/μs V _R = 400 V	-	90	-				
Dealeman	I _{RRM}	T _J = 25 °C		-	18	-	A			
Peak recovery current		T _J = 125 °C		-	32	-				
Reverse recovery charge	Q _{rr}	T _J = 25 °C	vR - 400 v	-	850	-	nC			
		T _J = 125 °C		-	1850	-				

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Thermal resistance, junction to case	R _{thJC}		-	-	1	°C/W				
Thermal resistance, case to heatsink	R _{thCS}		-	0.4	-					
Weight			-	6.0	-	g				
Weight			-	0.21	-	oz.				
Mounting torque			6.0	_	12	kgf · cm				
			(5)	-	(10)	(lbf · in)				
Marking device		Case style TO-247AD 2L	E4PU3006LH							

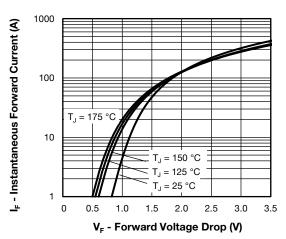


Fig. 1 - Typical Forward Voltage Drop Characteristics

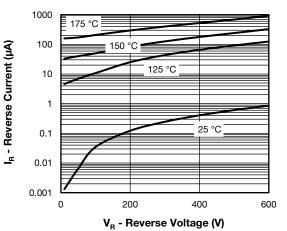


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

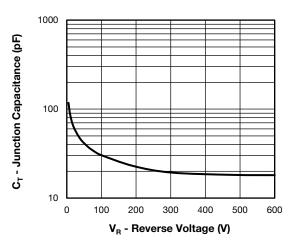


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

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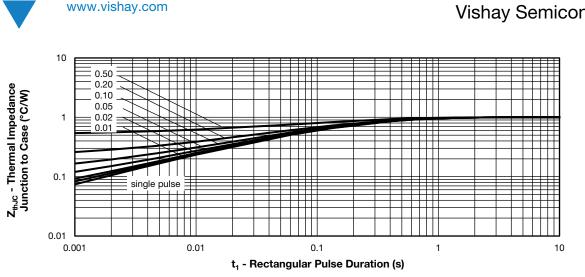


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics

140

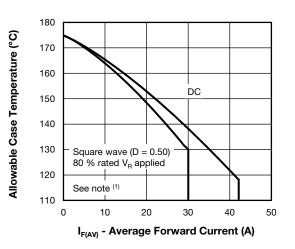


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

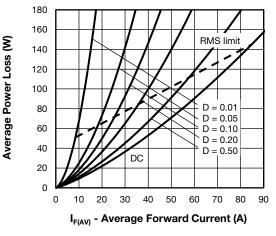


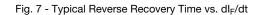
Fig. 6 - Forward Power Loss Characteristics



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⁽¹⁾ Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see Fig.5) P_{dREV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_R = rated V_R

130 120 110 125 °C 100 t_{rr} (ns) 90 25 °C 80 70 60 50 1000 400 500 600 700 800 900 dl_F/dt (A/µs)



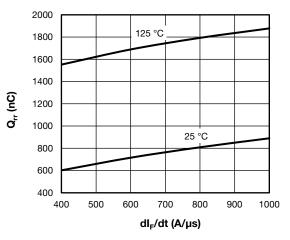


Fig. 8 - Typical Stored Charge vs. dl_F/dt

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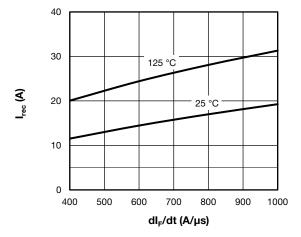


Fig. 9 - Typical Reverse Current vs. dl_F/dt

ORDERING INFORMATION TABLE

Device code	VS-	Е	4	Р	U	30	06	L	н	N3	
	1	2	3	4	5	6	7	8	9	10	
	 Vishay Semiconductors product Circuit configuration: 										
	E = single diode										
	3 - FRED Pt Gen 4										
	4 -	P =	TO-247	packag	e						
	5 -	Pro	cess typ	be:							
		U =	ultrafas	t recove	ery						
	6 -	Cur	rent rati	ng (30 =	= 30 A)						
	7 -	Volt	age rati	ng (06 =	= 600 V)						
	8 - Package: L = long lead										
	9 - H = AEC-Q 101 qualified										
	10 -	Env	ironmer	tal digit	:						
		N3 :	= haloge	en-free,	RoHS-c	ompliar	nt, and t	otally le	ad (Pb)	-free	

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-E4PU3006LHN3	25	500	Antistatic plastic tube					

LINKS TO RELATED DOCUMENTS						
Dimensions	TO-247AD 2L	www.vishay.com/doc?95536				
Part marking information	TO-247AD 2L	www.vishay.com/doc?95648				

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TO-247AD 2L

DIMENSIONS in millimeters and inches



Section C - C, D - D

(b, b2)

(4)

View	<u>/ B</u>

SYMBOL	MILLIN	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		STMDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			E	15.29	15.87	0.602	0.625	3
A1	2.21	2.59	0.087	0.102			E1	13.46	-	0.53	-	
A2	1.50	2.49	0.059	0.098			е	5.46	BSC	0.215	5 BSC	
b	0.99	1.40	0.039	0.055			ØК	0.2	254	0.0	010	
b1	0.99	1.35	0.039	0.053			L	19.81	20.32	0.780	0.800	
b2	1.65	2.39	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b3	1.65	2.34	0.065	0.092			ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	
D2	0.51	1.35	0.020	0.053				•		•		•

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

(5) Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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