Onsemi

This device employs the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity protection diodes in surface mount applications where compact size and weight are critical to the system. Features

- Low Profile Package for Space Constrained Applications
- Rectangular Package for Automated Handling
- Highly Stable Oxide Passivated Junction
- 150°C Operating Junction Temperature
- Guard-Ring for Stress Protection
- NRV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These are Pb–Free and Halide–Free Devices

Mechanical Charactersistics

- Case: Epoxy, Molded, Epoxy Meets UL 94, V-0
- Weight: 95 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Cathode Polarity Band
- Device Meets MSL 1 Requirements
- ESD Ratings: Machine Model = C Human Body Model = 3B

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	40	V
Average Rectified Forward Current (At Rated V_R , $T_C = 100^{\circ}C$)	Ι _Ο	1.5	A
Peak Repetitive Forward Current (At Rated V _R , Square Wave, 100 kHz, T _C = 130°C)	I _{FRM}	3.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	40	A
Storage/Operating Case Temperature	T _{stg} , T _C	-55 to +150	°C
Operating Junction Temperature	TJ	-55 to +150	°C
Voltage Rate of Change (Rated V_R , $T_J = 25^{\circ}C$)	dv/dt	10,000	V/μs

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

DATA SHEET

SCHOTTKY BARRIER RECTIFIER 1.5 AMPERE 40 VOLTS



SMA-FL CASE 403AA **STYLE 6**

MARKING DIAGRAM



RAE	= Specific Device Code
А	= Assembly Location
Y	= Year
WW	= Work Week

= Work Week

= Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping [†]
NRVBAF1540T3G	SMA-FL (Pb-Free)	5000 / Tape & Reel

DISCONTINUED (Note 1)

MBRAF1540T3G	SMA-FL (Pb-Free)	5000 / Tape & Reel
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†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

1. DISCONTINUED: This device is not recommended for new design. Please contact your onsemi representative for information. The most current information on this device may be available on www.onsemi.com.

MBRAF1540T3G, NRVBAF1540T3G

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance,			°C/W
Junction-to-Lead (Note 2)	$R_{\theta JL}$	25	
Thermal Resistance,			
Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	90	

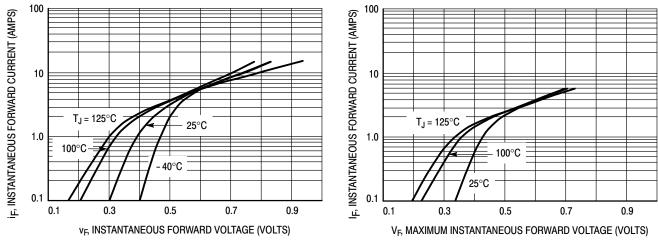
2. 1 inch square pad size (1 x 0.5 inch for each lead) on FR4 board.

ELECTRICAL CHARACTERISTICS

Characteristic		Symbol	Va	lue	Unit
Maximum Instantaneous Forward Voltage (Note 3)		٧ _F	T _J = 25°C	T _J = 125°C	V
see Figure 2 $(i_F = 1.5 \text{ A})$			0.46 0.54	0.39 0.54	
Maximum Instantaneous Reverse Current (Note 3)		I _R	T _J = 25°C	T _J = 100°C	mA
see Figure 4	(V _R = 40 V) (V _R = 20 V)		0.8 0.1	5.7 1.6	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 3. Pulse Test: Pulse Width \leq 250 µs, Duty Cycle \leq 2.0%.

MBRAF1540T3G, NRVBAF1540T3G







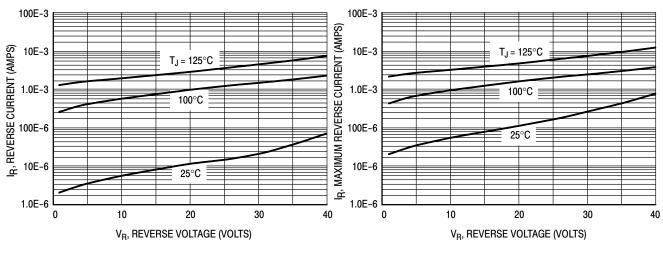
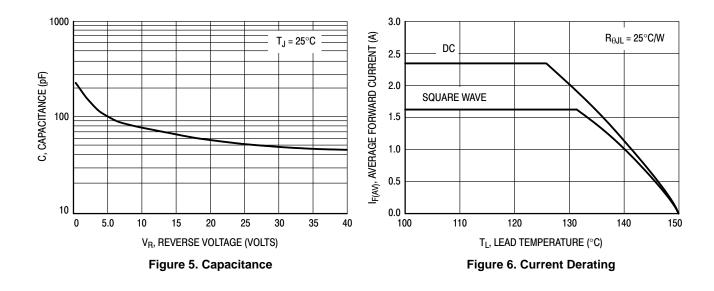


Figure 3. Typical Reverse Current

Figure 4. Maximum Reverse Current



MBRAF1540T3G, NRVBAF1540T3G

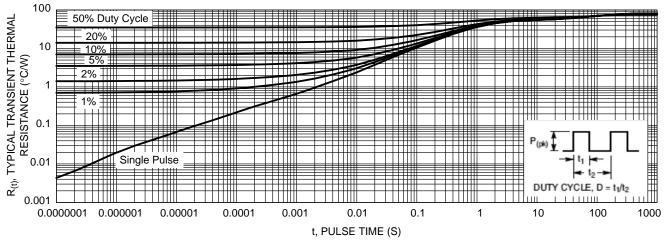
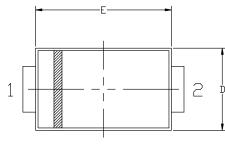
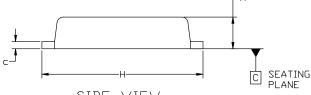


Figure 7. Typical Transient Thermal Response, Junction-to-Ambient

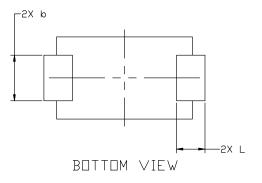












GENERIC MARKING DIAGRAM*



- XXXX = Specific Device Code A = Assembly Location
- Y = Year

ww

- = Work Week
- = Pb-Free Package
- *This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

SMA 2.60x4.30x1.00 CASE 403AA ISSUE A

NDTES:

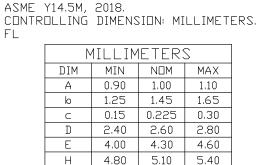
1.

2. CD 3. FL

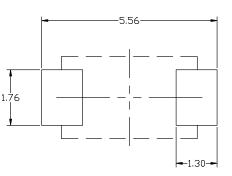
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DATE 18 JAN 2024



DIMENSIONING AND TOLERANCING AS PER



0.70

0.90

1.10

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RECOMMENDED MOUNTING FOOTPRINT

*For additional information on our Pb-Free strategy and soldering details, please download th e IN Semiconductor Soldering and Mounting Techniques Reference manual, SILDERRM/D.

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