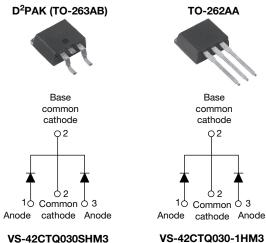


## VS-42CTQ030SHM3, VS-42CTQ030-1HM3

**Vishay Semiconductors** 

# High Performance Schottky Rectifier, 2 x 20 A



VS-42CTQ030-1HM3

PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	2 x 20 A							
V <sub>R</sub>	30 V							
V <sub>F</sub> at I <sub>F</sub>	0.38 V							
I <sub>RM</sub>	183 mA at 125 °C							
T <sub>J</sub> max.	150 °C							
E <sub>AS</sub>	13 mJ							
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA							
Circuit configuration	Common cathode							

### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Center tap configuration
- · Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability



- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified meets JESD 201 class 1A whisker test
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### DESCRIPTION

This center tap Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	IBOL CHARACTERISTICS VALUES								
I <sub>F(AV)</sub>	Rectangular waveform	40	А						
V <sub>RRM</sub>		30	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1100	А						
V <sub>F</sub>	20 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.38	V						
TJ	Range	-55 to 150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-42CTQ030SHM3 VS-42CTQ030-1HM3	UNITS					
Maximum DC reverse voltage	V <sub>R</sub>	30	V					
Maximum working peak reverse voltage	V <sub>RWM</sub>	50	V					



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ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST COND	TEST CONDITIONS		UNITS			
Maximum average	per leg			20					
forward current See fig. 5	per device	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 121 °C	40	А				
Maximum peak one cycle no	Maximum peak one cycle non-repetitive		5 µs sine or 3 µs rect. pulse Following any rated load		1100	A			
surge current per leg See fig. 7		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse Condition and with rated V <sub>RRM</sub> applied		360				
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 3 A, L = 2.90 mH		13	mJ			
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical		3	А			

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS			
		20 A	T <sub>1</sub> = 25 °C	0.48	V			
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	40 A	1j=25 0	0.57				
See fig. 1	V FM \	20 A	T <sub>.1</sub> = 125 °C	0.38				
		40 A	1j = 125 C	0.51				
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		3	mA			
See fig. 2	IRM \''	T <sub>J</sub> = 125 °C	$V_{R} = Rated V_{R}$	183				
Threshold Voltage	V <sub>F(TO)</sub>	T T movimum		0.22	V			
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		6.76	mΩ			
Maximum junction capacitance per leg	CT	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal rang	2840	pF				
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mi	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C			
Maximum thermal resistance, junction to case per leg Maximum thermal resistance, junction to case per package		P	DC operation	2.0				
		R <sub>thJC</sub>		1.0	°C/W			
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50				
Approximate weight				2	g			
Approximate weight				0.07	oz.			
Mounting torque	minimum			6 (5)	kgf ⋅ cm			
Mounting torque —	maximum			12 (10)	(lbf · in)			
Marking davias			Case style D <sup>2</sup> PAK (TO-263AB)	42CTQ	030SH			
Marking device			Case style TO-262AA	42CTQ	030-1H			

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# VS-42CTQ030SHM3, VS-42CTQ030-1HM3

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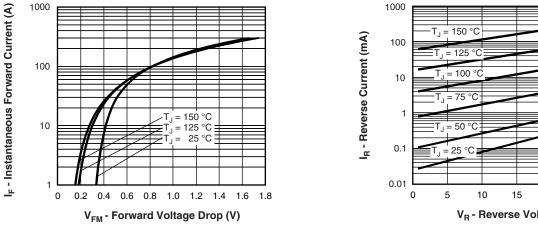


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

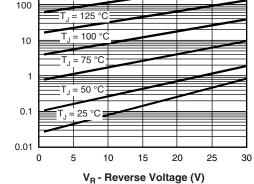


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

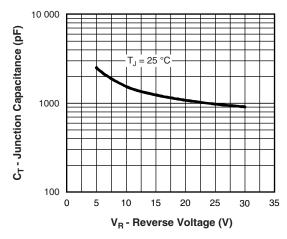
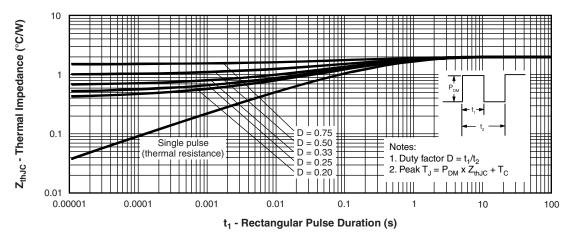


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)



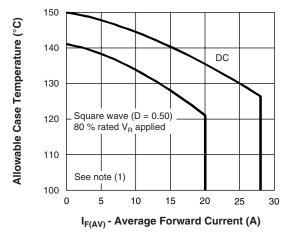


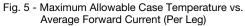
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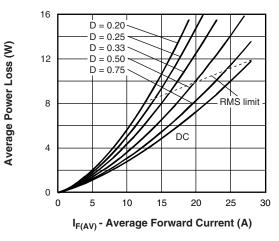


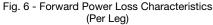
## VS-42CTQ030SHM3, VS-42CTQ030-1HM3

**Vishay Semiconductors** 









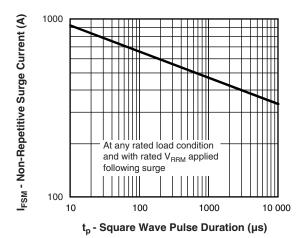


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

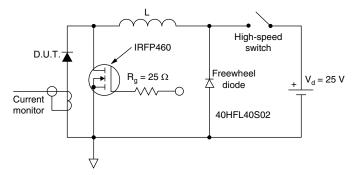


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ x \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ x \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{10} \ \mathsf{V} \end{array}$ 

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Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

Device code	VS-	42	С	т	Q	030	S	TRL	н	М3	
		2	3	4	5	6	7	8	9	10	
	1 - Vishay Semiconductors product										
	2	- Cur	rent rati	ng (40 A	A)						
	3	3 - Circuit configuration: C = Common cathode									
	4	- T = TO-220									
	5	- Sch	ottky "G	)" series							
	6	- Volt	tage rati	ing (030	= 30 V)						
	7	- •S	= D <sup>2</sup> PA	К (ТО-2	263AB)						
		• -1	= TO-2	62AA							
	8	- • N	one = T	ube							
		• TI	<ul> <li>TRL = Tape and reel (left oriented - for D<sup>2</sup>PAK only)</li> </ul>								
		• TI	<ul> <li>TRR = Tape and reel (right oriented - for D<sup>2</sup>PAK only)</li> </ul>								
	9	- H=	AEC-Q	101 qua	alified						
	10	- M3	= Halog	gen-free	, RoHS-	complia	ant and	termina	ition lea	d (Pb)-fr	

ORDERING INFORMATION									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-42CTQ030SHM3	50	1000	Antistatic plastic tubes						
VS-42CTQ030STRRHM3	800	800	13" diameter reel						
VS-42CTQ030STRLHM3	800	800	13" diameter reel						
VS-42CTQ030-1HM3	50	1000	Antistatic plastic tubes						

LINKS TO RELATED DOCUMENTS								
Dimensions -	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95046						
Dimensions	TO-262AA	www.vishay.com/doc?95419						
Port marking information	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95444						
Part marking information	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?95032						

# **Outline Dimensions**



D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches

www.vishay.com

SHA



SYMBOL	MILLIMETERS		INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

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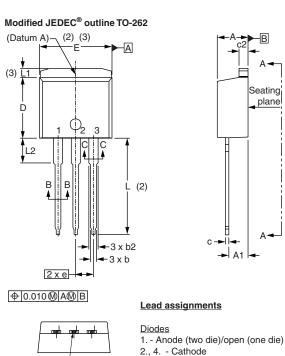
## **Outline Dimensions**



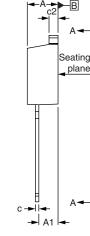
**Vishay Semiconductors** 

**TO-262** 

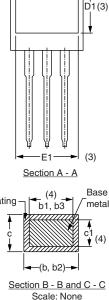
#### **DIMENSIONS** in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 \_ 1.65 0.065 3 \_ 3.36 0.132 0.146 L2 3.71

3. - Anode

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

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