

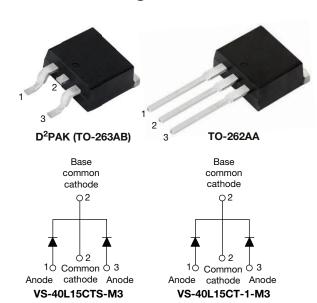
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Vishay Semiconductors

COMPLIANT HALOGEN

**FREE** 

## High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	2 x 20 A					
$V_{R}$	15 V					
V <sub>F</sub> at I <sub>F</sub>	0.33 V					
I <sub>RM</sub> max.	600 mA at 100 °C					
T <sub>J</sub> max.	125 °C					
E <sub>AS</sub>	10 mJ					
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA					
Circuit configuration	Common cathode					

#### **FEATURES**

- 125 °C T<sub>J</sub> operation (V<sub>R</sub> < 5 V)</li>
- Center tap module
- · Optimized for OR-ing applications
- Ultralow forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The center tap Schottky rectifier module has been optimized for ultralow forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	VALUES	UNITS						
I <sub>F(AV)</sub>	Rectangular waveform	40	Α					
V <sub>RRM</sub>		15	V					
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	700	Α					
V <sub>F</sub>	19 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg, typical)	0.25	V					
T <sub>J</sub>		-55 to +125	°C					

VOLTAGE RATINGS						
PARAMETER SYMBOL TEST CONDITIONS VS-40L15CTS-M3 VS-40L15CT-1-M3 UNITS						
Maximum DC reverse voltage	$V_R$	T.ı = 100 °C	15	V		
Maximum working peak reverse voltage	$V_{RWM}$	1) = 100 0	15	V		



# VS-40L15CTS-M3, VS-40L15CT-1-M3

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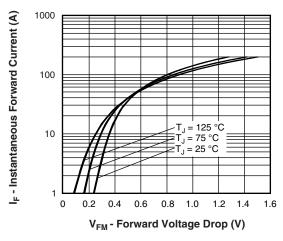
ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS						
Maximum average forwardper leg	1	50 % duty avalo at T <sub>-</sub> = 85 °C	rootangular wayoform	20					
current, see fig. 5 per device	I <sub>F(AV)</sub>	50 % duty cycle at $T_C$ = 85 °C, rectangular waveform		40					
Maximum peak one cycle non-repetitive	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load	700	A				
surge current per leg, see fig. 7		10 ms sine or 6 ms rect. pulse	condition and with rated V <sub>RRM</sub> applied	330					
Non-repetitive avalanche energy per leg E <sub>AS</sub>		$T_J = 25 ^{\circ}\text{C}$ , $I_{AS} = 2  \text{A}$ , $L = 6  \text{mH}$		10	mJ				
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		2	Α				

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
Maximum forward voltage drop per leg See fig. 1		19 A	T <sub>.1</sub> = 25 °C	-	0.41			
	V <sub>FM</sub> <sup>(1)</sup>	40 A	1J=25 C	-	0.52	V		
	VFM ('')	19 A	T.ı = 125 °C	0.25	0.33			
		40 A	1) = 125 G	0.37	0.50			
Reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	-	10	mA		
See fig. 2		T <sub>J</sub> = 100 °C	v <sub>R</sub> = nateu v <sub>R</sub>	-	600	IIIA		
Threshold voltage	V <sub>F(TO)</sub> 0.182		82	V				
Forward slope resistance	r <sub>t</sub>	ij = ijillaxilliulli	$T_J = T_J$ maximum		.6	mΩ		
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range	-	2000	pF			
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 n	8	-	nΗ			
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10	000	V/µs		

### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECH	THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	YMBOL TEST CONDITIONS		UNITS				
Maximum junction tempera	ature range	TJ		-55 to +125	°C				
Maximum storage tempera	ture range	T <sub>Stg</sub>		-55 to +150	C				
Maximum thermal resistangular junction to case per leg	ce,	R <sub>thJC</sub>	DC operation See fig. 4	1.5					
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased 0		°C/W				
Maximum thermal resistant junction to ambient	Maximum thermal resistance, junction to ambient		DC operation	40					
Approximate weight				2	g				
Approximate weight				0.07	OZ.				
Marinting taxoria	minimum		Non-lubricated threads	6 (5)	kgf ⋅ cm				
Mounting torque	maximum		Non-lubricated trireads	12 (10)	(lbf · in)				
Marking daving	Marking device		Case style D <sup>2</sup> PAK (TO-263AB)	40L15	5CTS				
warking device			irking device		Case style TO-262AA	40L15	40L15CT-1		



1000 T<sub>J</sub> = 100 °C T<sub>J</sub> = 75 °C T<sub>J</sub> = 50 °C T<sub>J</sub> = 25 °C T<sub>J</sub> = 25 °C T<sub>J</sub> = 75 °C T<sub>J</sub> = 25 °C T<sub>J</sub> = 15 V<sub>R</sub> - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

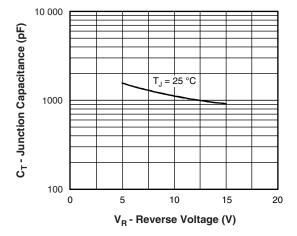


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

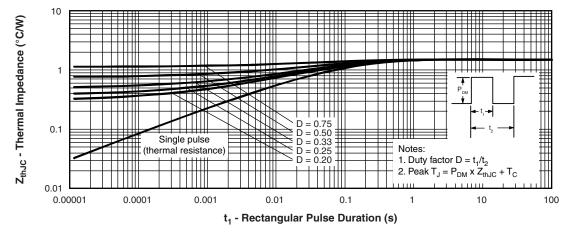


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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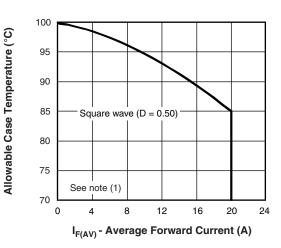


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

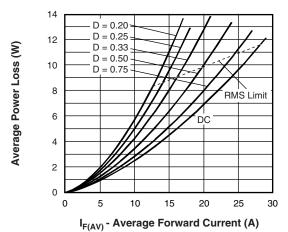


Fig. 6 - Forward Power Loss Characteristics

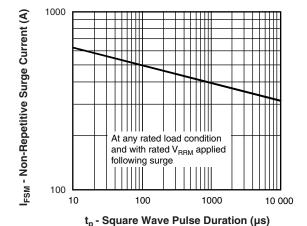


Fig. 7 - Maximum Non-Repetitive Surge Current

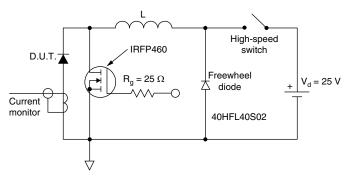


Fig. 8 - Unclamped Inductive Test Circuit

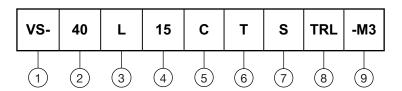
#### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>



### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating (40 A)

3 - L = Schottky "L" series

4 - Voltage rating (15 V)

**5** - C = common cathode

**6** - T = TO-220

7 - • S = D<sup>2</sup>PAK (TO-263AB)

• -1 = TO-262AA

**8** - • None = tube

• TRL = tape and reel (left oriented - for D<sup>2</sup>PAK (TO-263AB) only)

• TRR = tape and reel (right oriented - for D<sup>2</sup>PAK (TO-263AB) only)

9 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N BASE QUANTITY PACKAGING DESCRIPTION							
VS-40L15CTS-M3	50	Antistatic plastic tubes					
VS-40L15CTSTRL-M3	800	13" diameter plastic tape and reel					
VS-40L15CTSTRR-M3	800	13" diameter plastic tape and reel					
VS-40L15CT-1-M3	50	Antistatic plastic tubes					

LINKS TO RELATED DOCUMENTS						
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164				
Differisions	TO-262AA	www.vishay.com/doc?96165				
Part marking information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444				
	TO-262AA	www.vishay.com/doc?95443				
Packaging information		www.vishay.com/doc?96424				
SPICE model		www.vishay.com/doc?97118				



## D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	ES SYMBOL	MILLIM	ETERS	INC	HES	NOTES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

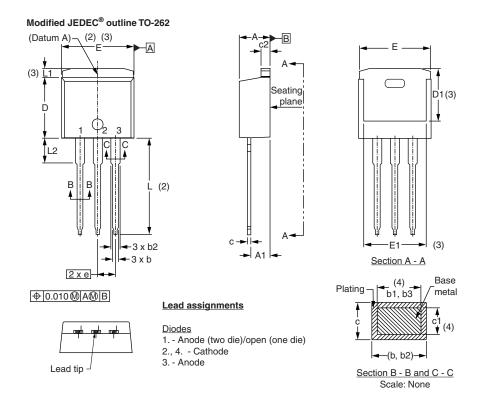
#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



### **TO-262**

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	INCHES			
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		
Α	4.06	4.83	0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2	1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54	BSC	0.10	D BSC			
L	13.46	14.10	0.530	0.555			
L1	-	1.65	-	0.065	3		
L2	3.36	3.71	0.132	0.146			

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

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