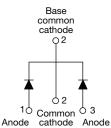
ISHA www.vishay.com

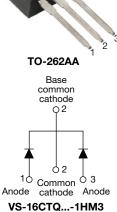
Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 8 A



D²PAK (TO-263AB)





VS-16CTQ...SHM3

PRIMARY CHARACTERISTICS							
I _{F(AV)} 2 x 8 A							
V _R	60 V to 100 V						
V _F at I _F	0.58 V						
I _{RM}	7.0 mA at 125 °C						
T _J max.	175 °C						
E _{AS}	7.5 mJ						
Package	D ² PAK (TO-263AB), TO-262AA						
Circuit configuration	Common cathode						

FEATURES

- 175 °C T_J operation
- · Center tap configuration
- Low forward voltage drop
- High purity. high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 1 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	YMBOL CHARACTERISTICS VALUE								
I _{F(AV)}	Rectangular waveform	16	A						
V _{RRM}		60 to 100	V						
I _{FSM}	t _p = 5 μs sine	850	A						
V _F	8 A _{pk} , T _J = 125 °C (per leg)	0.58	V						
TJ	Range	-55 to +175	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-16CTQ060SHM3 VS-16CTQ060-1HM3	VS-16CTQ080SHM3 VS-16CTQ080-1HM3	VS-16CTQ100SHM3 VS-16CTQ100-1HM3	UNITS			
Maximum DC reverse voltage	V _R	60	80	100	V			
Maximum working peak reverse voltage	V _{RWM}	00	00	100	v			

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS					
Maximum average forward current per leg	1	50 % duty cycle at T _C = 148 °C	8	А					
See fig. 5 per device	I _{F(AV)}	50% duty cycle at $T_C = 140\%$	16	A					
Maximum peak one cycle non-repetitive surge current per leg	lease a	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	850	А				
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	275	~				
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 0.50 \text{ A}, L = 60$	7.50	mJ					
Repetitive avalanche current per leg	I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical						

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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS					
		8 A	T.I = 25 °C	0.72	V				
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	16 A	1j=23 0	0.88					
	VFM ()	8 A	T _J = 125 °C	0.58					
		16 A	1j = 125 C	0.69					
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = rated $V_{\rm B}$	0.55	mA				
See fig. 2	IRM (1)	T _J = 125 °C	$v_{\rm R} = rateu v_{\rm R}$	7.0					
Threshold voltage	V _{F(TO)}	T T mayimum		0.415	V				
Forward slope resistance	r _t	$T_J = T_J maximum$		11.07	mΩ				
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range	500	pF					
Typical series inductance per leg	L _S	Measured lead to lead 5 n	8.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs				

Note

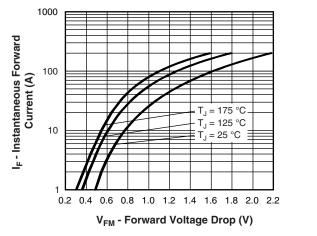
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

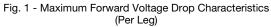
THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS				
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C				
Maximum thermal resistance, junction to case per leg Maximum thermal resistance, junction to case per package		Б	DC operation	3.25					
		R _{thJC}	DC operation	1.63	°C/W				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50					
Approvimete weight				2	g				
Approximate weight				0.07	oz.				
Mounting torque	minimum			6 (5)	kgf · cm				
Mounting torque	maximum			12 (10)	(lbf · in)				
Maultine device			Case style D ² PAK (TO-263AB)	16CTC)SH				
Marking device			Case style TO-262AA	16CTQ	1H				



VS-16CTQ...SHM3, VS-16CTQ...-1HM3 Series

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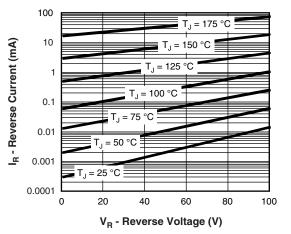


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

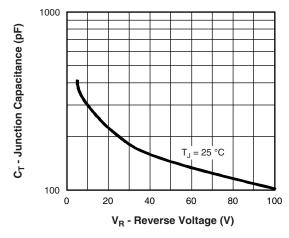


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

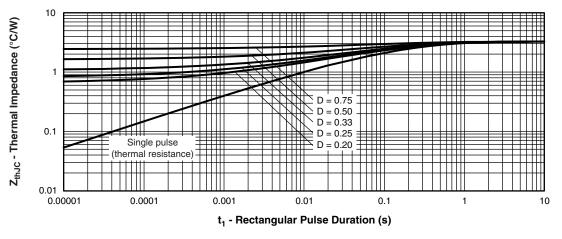


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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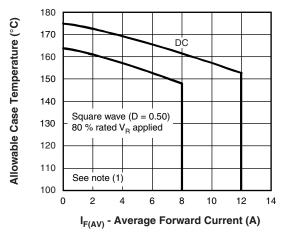
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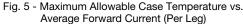
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VS-16CTQ...SHM3, VS-16CTQ...-1HM3 Series

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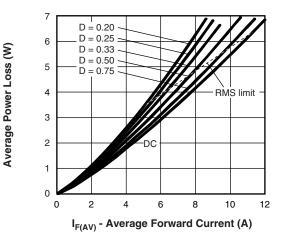


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

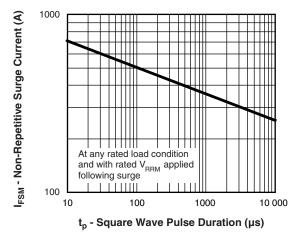


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

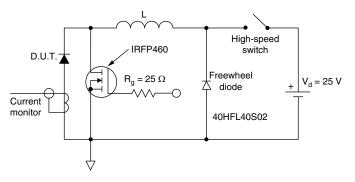


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

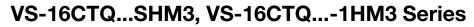
 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ at \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (1 - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ at \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \ \mathsf{applied} \end{array}$

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Vishay Semiconductors

ORDERING INFORMATION TABLE

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VISHAY

Device code	VS-	16	С	т	Q	100	s	TRL	Н	М3	
		2	3	4	5	6	7	8	9	10	
	 Vishay Semiconductors product Ourset action (40.4) 										
	 2 - Current rating (16 A) 3 - Circuit configuration: C = Common cathode 										
		- T = TO-220									
	5 - 6 -		ottky "C age rati)" series			060 = 60 080 = 80				
	7		= D ² PA	•			00 = 10				
		• -1	= TO-2	62							
	8 - • None = tube										
			 TRL = tape and reel (left oriented - for D²PAK only) 								
		• TI	 TRR = tape and reel (right oriented - for D²PAK only) 								
	9 -	• H=	AEC-Q	101 qua	alified						
	10 -	- M3	= halog	en-free,	RoHS-	compli	ant and	termina	tion lea	d (Pb)-fr	

ORDERING INFORMATION										
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-16CTQ060SHM3	50	1000	Antistatic plastic tubes							
VS-16CTQ060STRRHM3	800	800	13" diameter reel							
VS-16CTQ060STRLHM3	800	800	13" diameter reel							
VS-16CTQ060-1HM3	50	1000	Antistatic plastic tubes							
VS-16CTQ080SHM3	50	1000	Antistatic plastic tubes							
VS-16CTQ080STRRHM3	800	800	13" diameter reel							
VS-16CTQ080STRLHM3	800	800	13" diameter reel							
VS-16CTQ080-1HM3	50	1000	Antistatic plastic tubes							
VS-16CTQ100SHM3	50	1000	Antistatic plastic tubes							
VS-16CTQ100STRRHM3	800	800	13" diameter reel							
VS-16CTQ100STRLHM3	800	800	13" diameter reel							
VS-16CTQ100-1HM3	50	1000	Antistatic plastic tubes							

LINKS TO RELATED DOCUMENTS								
Dimensions	TO-263AB (D ² PAK)	www.vishay.com/doc?95046						
	TO-262AA	www.vishay.com/doc?95419						
	TO-263AB (D ² PAK)	www.vishay.com/doc?95444						
Part marking information	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?95032						
SPICE model		www.vishay.com/doc?95279						

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Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

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SHA



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

1

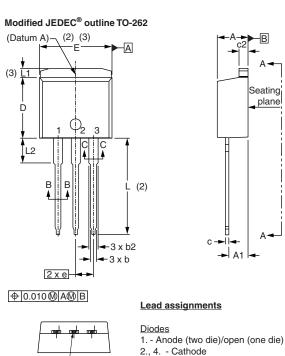
Outline Dimensions



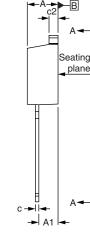
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TO-262

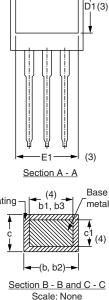
DIMENSIONS in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 _ 1.65 0.065 3 _ 3.36 0.132 0.146 L2 3.71

3. - Anode

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019

Document Number: 95419

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