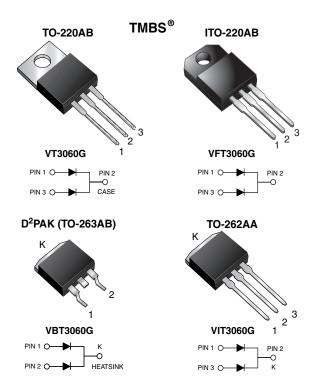
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## **Dual High Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.40 \text{ V}$  at  $I_F = 5 \text{ A}$ 



### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	2 x 15 A					
V <sub>RRM</sub>	60 V					
I <sub>FSM</sub>	150 A					
V <sub>F</sub> at I <sub>F</sub> = 15 A	0.61 V					
T <sub>J</sub> max.	150 °C					
Package	TO-220AB, ITO-220AB, D <sup>2</sup> PAK (TO-263AB), TO-262AA					
Circuit configuration	Common cathode					

### **FEATURES**

Trench MOS Schottky technology



· Low forward voltage drop, low power losses

• High efficiency operation

(e3)

 Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C (for D<sup>2</sup>PAK (TO-263AB) package)

ROHS

- Not recommended for PCB bottom side wave mounting
- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for TO-220AB, ITO-220AB and TO-262AA package)
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

### TYPICAL APPLICATIONS

For use in high frequency inverters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters and reverse battery protection.

### **MECHANICAL DATA**

**Case:** TO-220AB, ITO-220AB, D<sup>2</sup>PAK (TO-263AB), and TO-262AA

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant and commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs max.

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)									
PARAMETER			VT3060G	VFT3060G	VBT3060G	VIT3060G	UNIT		
Max. repetitive peak reverse voltage			60						
Max. average forward rectified current	per device	1	30						
(fig. 1)	per diode	I <sub>F(AV)</sub>	15				A		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load			150				Α		
Non-repetitive avalanche energy at $T_J = 25$ °C, $L = 60$ mH per diode			120				mJ		
Peak repetitive reverse current at $t_p$ = 2 $\mu$ s, 1 kHz, $T_J$ = 38 °C $\pm$ 2 °C per diode			1.0			Α			
Isolation voltage (ITO-220AB only) from terminal to heatsink t = 1 min			1500			V			
Operating junction and storage temperature range			-55 to +150				°C		



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT		
Breakdown voltage	I <sub>R</sub> = 1.0 mA	T <sub>A</sub> = 25 °C	$V_{BR}$	60 (min.)	=	V		
Instantaneous forward voltage per diode (1)	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	- V <sub>F</sub>	0.49	=	V		
	I <sub>F</sub> = 7.5 A			0.53	-			
	I <sub>F</sub> = 15 A			0.65	0.73			
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.40	=			
	I <sub>F</sub> = 7.5 A			0.46	-			
	I <sub>F</sub> = 15 A			0.61	0.69			
Reverse current per diode (2)	V <sub>R</sub> = 60 V	T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C	I <sub>R</sub>	- 14	850 40	μA mA		

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER		SYMBOL	VT3060G	VFT3060G	VBT3060G VIT3060G U		UNIT	
Typical thermal resistance	per diode	R <sub>θJC</sub>	3.2	6.2	3.2	3.2	°C/W	
	per device		1.9	5.0	1.9	1.9	C/VV	

ORDERING INFORMATION (EXAMPLE)									
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE				
TO-220AB	VT3060G-E3/4W	1.88	4W	50/tube	Tube				
ITO-220AB	VFT3060G-E3/4W	1.76	4W	50/tube	Tube				
D <sup>2</sup> PAK (TO-263AB)	VBT3060G-E3/4W	1.39	4W	50/tube	Tube				
D <sup>2</sup> PAK (TO-263AB)	VBT3060G-E3/8W	1.39	8W	800/reel	Tape and reel				
TO-262AA	VIT3060G-E3/4W	1.45	4W	50/tube	Tube				

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## **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

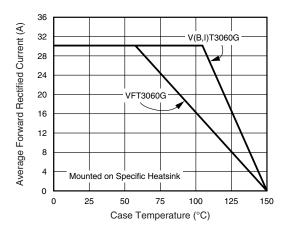


Fig. 1 - Maximum Forward Current Derating Curve

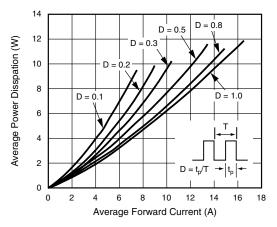


Fig. 2 - Forward Power Dissipation Characteristics Per Diode

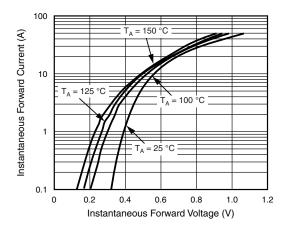


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

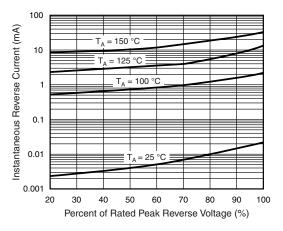


Fig. 4 - Typical Reverse Characteristics Per Diode

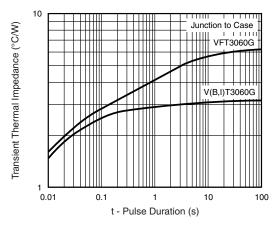


Fig. 5 - Typical Transient Thermal Impedance Per Diode

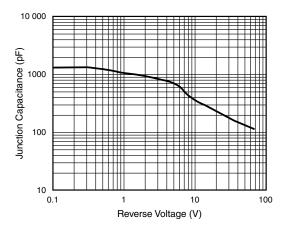
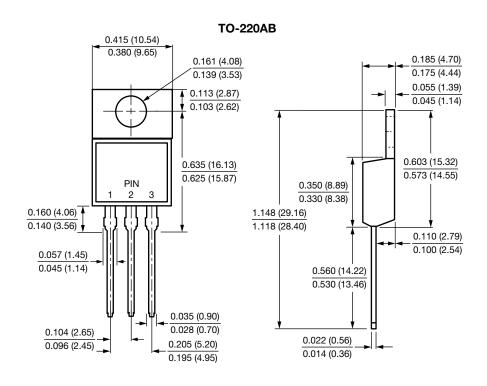


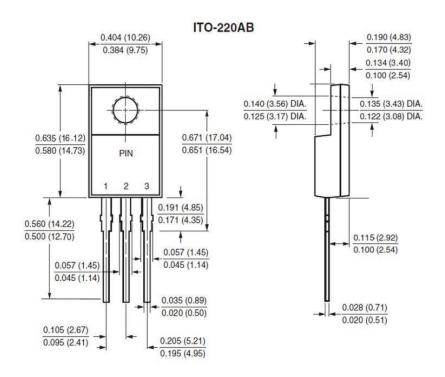
Fig. 6 - Typical Junction Capacitance Per Diode

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### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

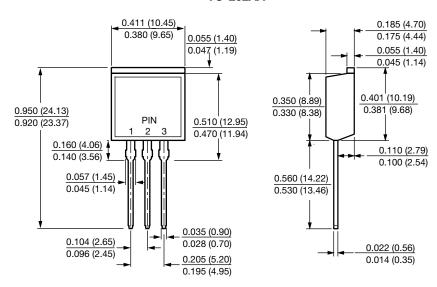




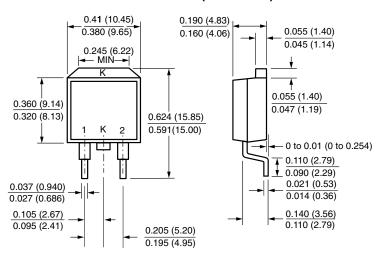
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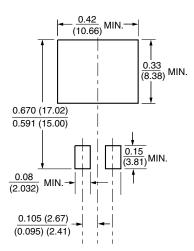
### **TO-262AA**



### D<sup>2</sup>PAK (TO-263AB)



### **Mounting Pad Layout**





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