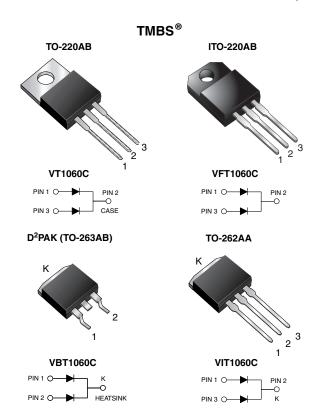


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Vishay General Semiconductor

# **Dual High Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.39 \text{ V}$  at  $I_F = 2.5 \text{ A}$ 



#### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	2 x 5 A					
V <sub>RRM</sub>	60 V					
I <sub>FSM</sub>	100 A					
V <sub>F</sub> at I <sub>F</sub> = 5.0 A	0.50 V					
T <sub>J</sub> max.	150 °C					
Package	TO-220AB, ITO-220AB, D <sup>2</sup> PAK (TO-263AB), TO-262AA					
Circuit configuration	Common cathode					

#### **FEATURES**





- · Low forward voltage drop, low power losses
- High efficiency operation

- e3)
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C (for D<sup>2</sup>PAK (TO-263AB) package)

RoHS COMPLIANT

- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for TO-220AB, ITO-220AB, and TO-262AA package)
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

#### **TYPICAL APPLICATIONS**

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters and reverse battery protection.

#### **MECHANICAL DATA**

Case: TO-220AB, ITO-220AB,  $D^2PAK$  (TO-263AB), and TO-262AA

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum



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MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)									
PARAMETER	SYMBOL	VT1060C VFT1060C VBT1060C VIT1060C	UNIT						
Maximum repetitive peak reverse voltage	$V_{RRM}$	60	V						
Maximum average forward rectified current (fig. 1)	1	10	A						
per diode	I <sub>F(AV)</sub>	5							
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode	I <sub>FSM</sub>	100							
Non-repetitive avalanche energy at T <sub>J</sub> = 25 °C, L = 60 mH	E <sub>AS</sub>	65	mJ						
Peak repetitive reverse current at $t_p$ = 2 $\mu$ s, 1 kHz, $T_J$ = 38 °C $\pm$ 2 °	C I <sub>RRM</sub>	1.0							
Isolation voltage (ITO-220AB only) from terminal to heatsink t = 1 m	n V <sub>AC</sub>	C 1500							
Operating junction and storage temperature range T <sub>J</sub> , T <sub>STG</sub> -55 to +150									

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode (1)	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub>	0.49	-	V	
	$I_F = 5.0 \text{ A}$			0.58	0.70		
	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 125 °C		0.39	-		
	I <sub>F</sub> = 5.0 A			0.50	0.60		
Reverse current per diode (2)	\/ 60 \/	T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C	I <sub>R</sub>	-	700	μΑ	
	V <sub>R</sub> = 60 V	T <sub>A</sub> = 125 °C		6.9	25	mA	

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER		SYMBOL	VT1060C	VFT1060C	VBT1060C	VIT1060C	UNIT
Typical thermal resistance	per diode	R <sub>θJC</sub>	3.5	6.5	3.5	3.5	°C/W
	per device		2.5	5.0	2.5	2.5	C/VV

ORDERING INFORMATION (Example)								
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	<b>DELIVERY MODE</b>			
TO-220AB	VT1060C-E3/4W	1.87	4W	50/tube	Tube			
ITO-220AB	VFT1060C-E3/4W	1.75	4W	50/tube	Tube			
D <sup>2</sup> PAK (TO-263AB)	VBT1060C-E3/4W	1.39	4W	50/tube	Tube			
D <sup>2</sup> PAK (TO-263AB)	VBT1060CE3/8W	1.39	8W	800/reel	Tape and reel			
TO-262AA	VIT1060C-E3/4W	1.45	4W	50/tube	Tube			

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### **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

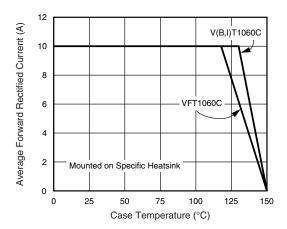


Fig. 1 - Maximum Forward Current Derating Curve

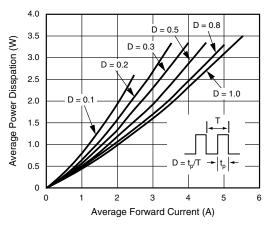


Fig. 2 - Forward Power Dissipation Characteristics Per Diode

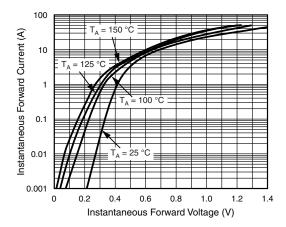


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

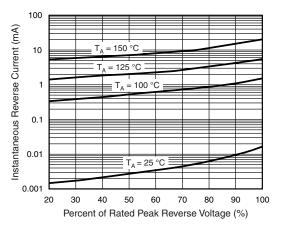


Fig. 4 - Typical Reverse Characteristics Per Diode

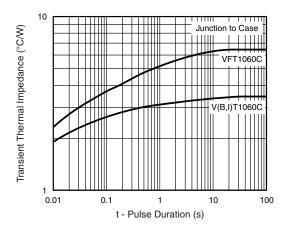


Fig. 5 - Typical Transient Thermal Impedance Per Diode

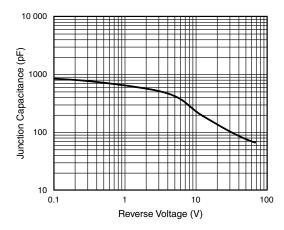
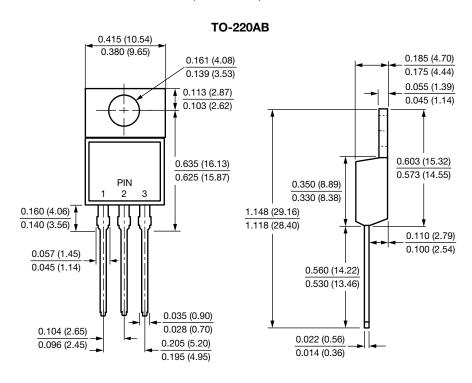
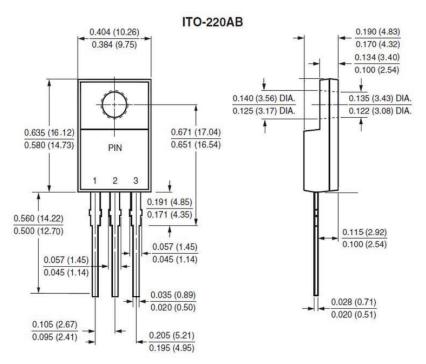


Fig. 6 - Typical Junction Capacitance Per Diode

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#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

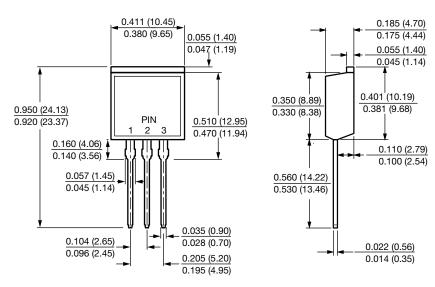




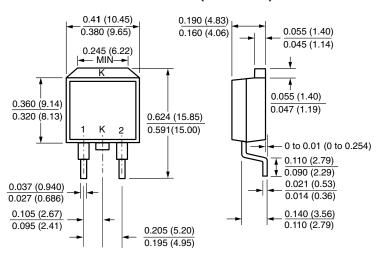
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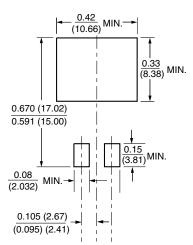
#### **TO-262AA**



#### D<sup>2</sup>PAK (TO-263AB)



#### **Mounting Pad Layout**





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