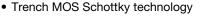
FEATURES 



- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation

Ultra Low V<sub>F</sub> = 0.5 V at I<sub>F</sub> = 2.5 A

- Meets MSL 1, per J-STD-020. level LF maximum peak of 260 °C
- AEC-Q101 qualified available: Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

#### **MECHANICAL DATA**

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test Polarity: as marked

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V10DM100C	UNIT	
Device marking code			V10DM100C		
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	100	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub> <sup>(1)</sup>	10	А	
	per diode		5	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	80	А	
Operating junction temperature range		T <sub>J</sub> <sup>(2)</sup>	-40 to +175	°C	
Storage temperature range		T <sub>STG</sub>	-55 to +175		

#### Notes

<sup>(1)</sup> Mounted on infinite heatsink

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>0,JA</sub>

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Anode 1 O-

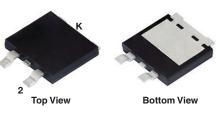
Anode 2 O Cathode

#### DESIGN SUPPORT TOOLS AVAILABLE



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 5 A			
V <sub>RRM</sub>	100 V			
I <sub>FSM</sub>	80 A			
V <sub>F</sub> at I <sub>F</sub> = 5 A (T <sub>A</sub> = 125 °C)	0.62 V			
T <sub>J</sub> max.	175 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			

# SMPD (TO-263AC)



eSMP<sup>®</sup> Series



RoHS

COMPLIANT

HALOGEN FREE

Vishay General Semiconductor



# V10DM100C



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## Vishay General Semiconductor

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.58	-	V	
	$I_F = 5 A$			0.71	0.79		
	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 125 °C		0.50	-		
	$I_F = 5 A$			0.62	0.70		
Reverse current at rated $V_R$ per diode	V <sub>R</sub> = 70 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	0.01	-	mA	
		T <sub>A</sub> = 125 °C		0.70	-		
	V <sub>B</sub> = 100 V	T <sub>A</sub> = 25 °C		-	0.1		
	v <sub>R</sub> = 100 v	T <sub>A</sub> = 125 °C		1.4	4.0		
Typical junction capacitance	4.0 V, 1 MHz		CJ	480	-	pF	

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V10DM100C	UNIT		
Typical thermal resistance per device	R <sub>0JC</sub> <sup>(1)</sup>	2.5	°C/W		
	R <sub>0JA</sub> <sup>(2)(3)</sup>	58			

#### Notes

<sup>(1)</sup> Mounted on infinite heatsink

 $\label{eq:linear} \ensuremath{^{(2)}}\xspace$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$  - junction-to-ambient

<sup>(3)</sup> Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V10DM100C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V10DM100CHM3/I (1)	0.55		2000/reel	13" diameter plastic tape and reel		

#### Note

(1) AEC-Q101 qualified



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### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

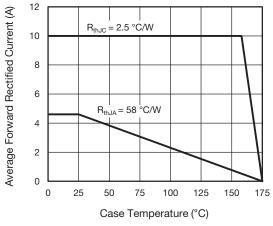


Fig. 1 - Maximum Forward Current Derating Curve

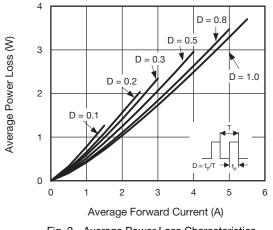
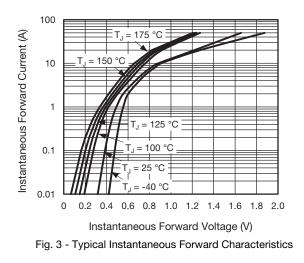


Fig. 2 - Average Power Loss Characteristics



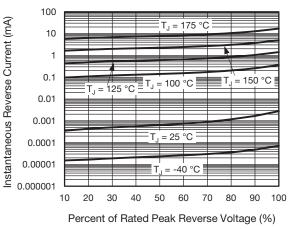
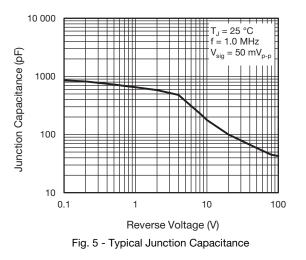
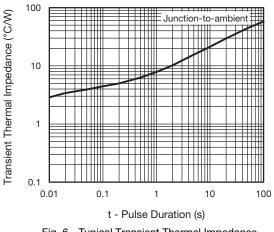
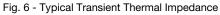


Fig. 4 - Typical Reverse Leakage Characteristics







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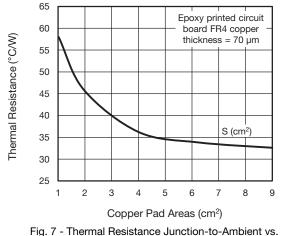
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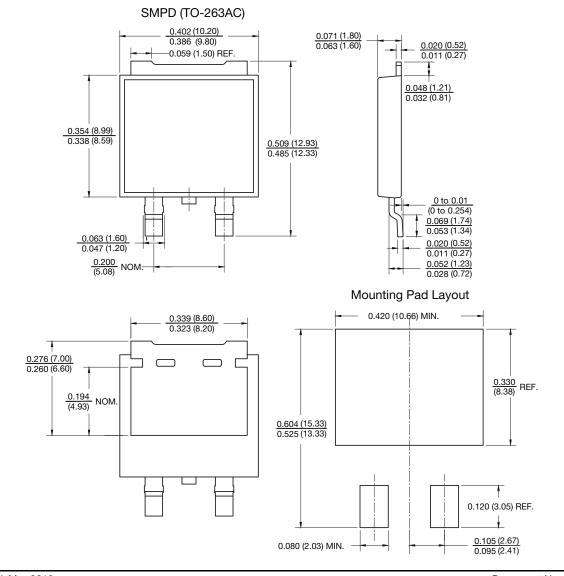


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Copper Pad Areas





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