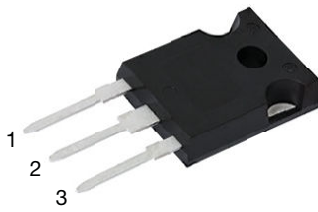
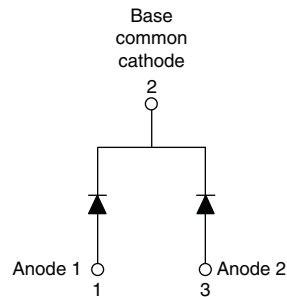


Ultrafast Rectifier, FRED Pt[®], 2 x 30 A


TO-247AC 3L


FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
 COMPLIANT
 HALOGEN
FREE

DESCRIPTION / APPLICATIONS

VS-60CPU06... series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, welding, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

PRIMARY CHARACTERISTICS

$I_{F(AV)}$	2 x 30 A
V_R	600 V
V_F at I_F	1.1 V
t_{rr} typ.	27 ns
T_J max.	175 °C
Package	TO-247AC 3L
Circuit configuration	Common cathode

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Repetitive peak reverse voltage	V_{RRM}		600	V
Average rectified forward current	$I_{F(AV)}$	Rated V_R , $T_C = 137$ °C	30	A
per leg			60	
per device				
Non-repetitive peak surge current per leg	I_{FSM}	$T_J = 25$ °C, $t_p = 10$ ms	300	
Peak repetitive forward current per leg	I_{FM}	Rated V_R , square wave, 20 kHz, $T_C = 137$ °C	60	
Operating junction and storage temperatures	T_J, T_{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25$ °C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_R	$I_R = 100$ μ A	600	-	-	V
Forward voltage	V_F	$I_F = 30$ A	-	1.31	1.65	
		$I_F = 30$ A, $T_J = 150$ °C	-	1.1	1.4	
Reverse leakage current	I_R	$V_R = V_R$ rated	-	0.02	50	μ A
		$T_J = 150$ °C, $V_R = V_R$ rated	-	30	250	
Junction capacitance	C_T	$V_R = 200$ V	-	22	-	pF
Series inductance	L_S	Measured lead to lead 5 mm from package body	-	3.5	-	nH



DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25\text{ }^\circ\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	$I_F = 1.0\text{ A}$, $di_F/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	27	35	ns
		$T_J = 25\text{ }^\circ\text{C}$	-	42	-	
		$T_J = 125\text{ }^\circ\text{C}$	-	110	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^\circ\text{C}$	-	5	-	A
		$T_J = 125\text{ }^\circ\text{C}$	-	11	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^\circ\text{C}$	-	110	-	nC
		$T_J = 125\text{ }^\circ\text{C}$	-	630	-	

THERMAL - MECHANICAL SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$ unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T_J , T_{Stg}		-65	-	175	$^\circ\text{C}$
Thermal resistance, junction to case per leg	R_{thJC}		-	0.6	0.9	$^\circ\text{C}/\text{W}$
Thermal resistance, junction to ambient per leg	R_{thJA}	Typical socket mount	-	-	70	
Thermal resistance, case to heatsink	R_{thCS}	Mounting surface, flat, smooth, and greased	-	0.4	-	
Weight			-	6.0	-	g
			-	0.22	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC 3L	60CPU06			

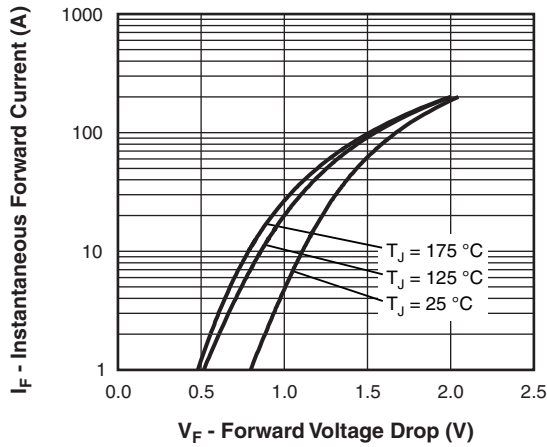


Fig. 1 - Typical Forward Voltage Drop Characteristics

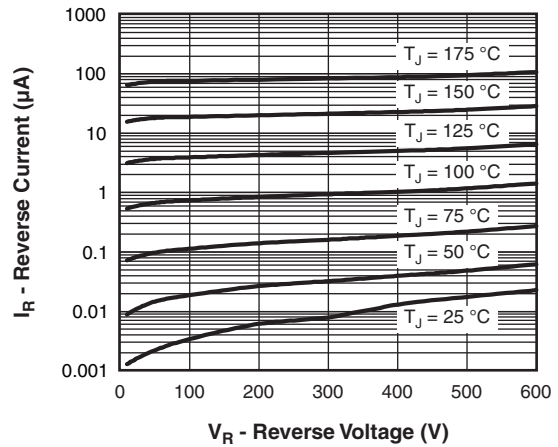


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

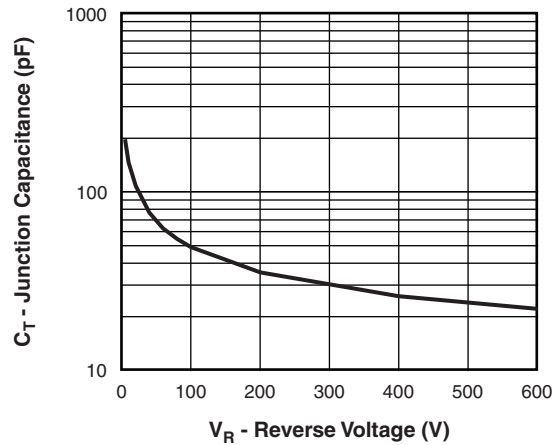


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

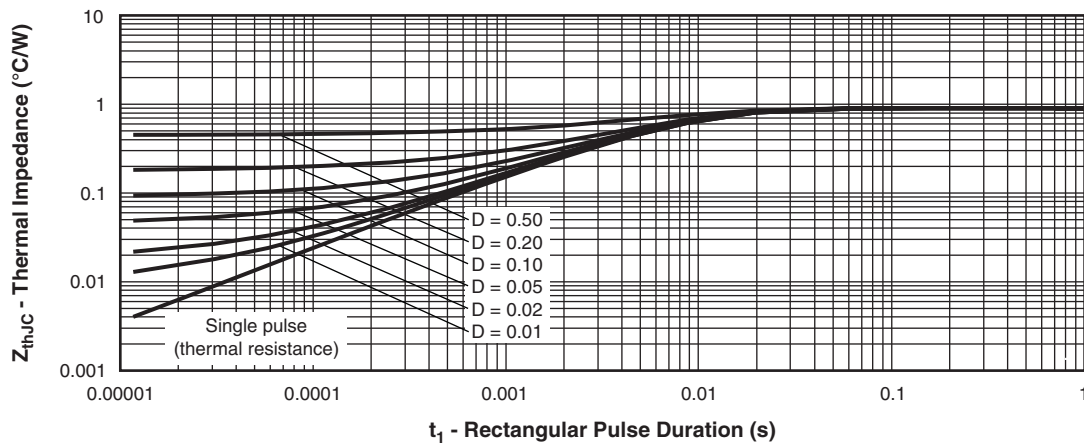


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

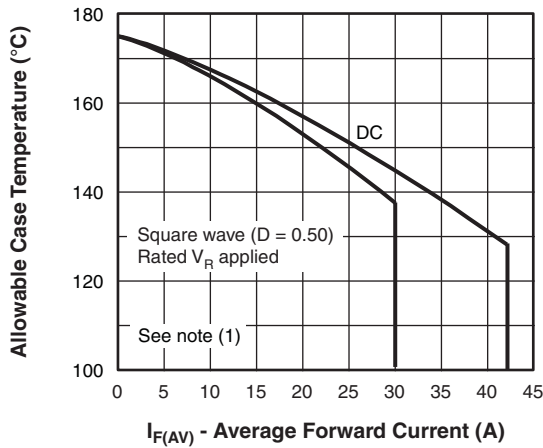


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

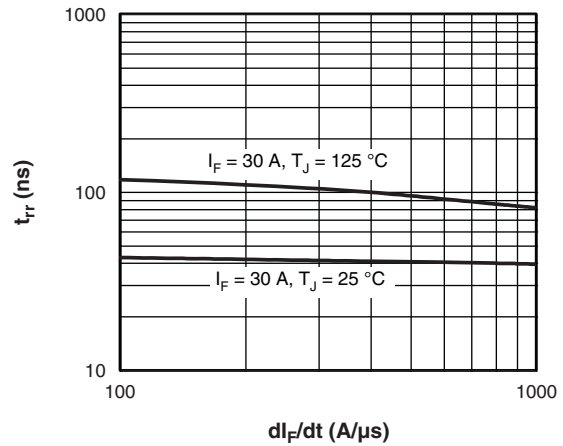


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

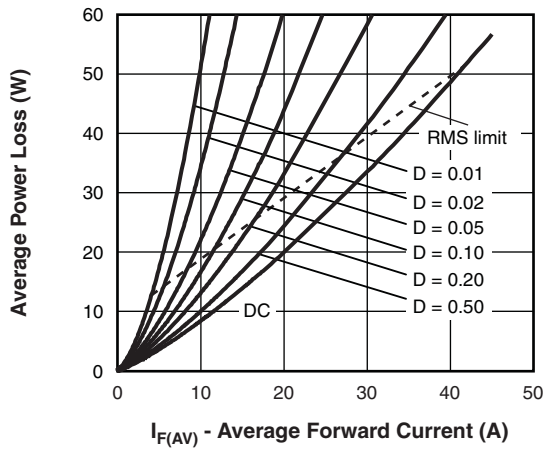


Fig. 6 - Forward Power Loss Characteristics

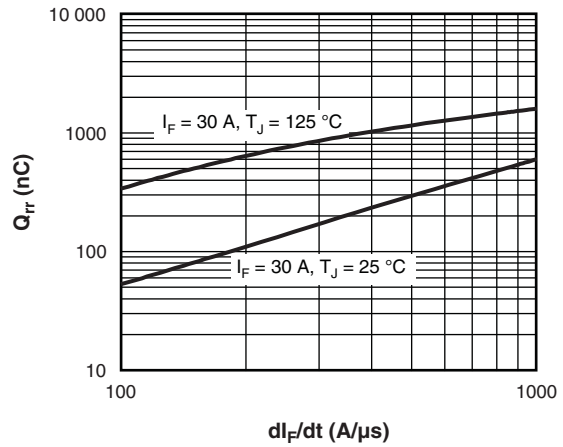
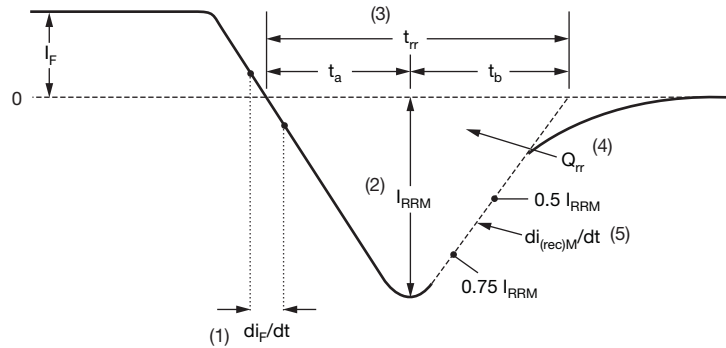


Fig. 8 - Typical Stored Charge vs. dI_F/dt

Note

- (1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
 Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = rated V_R



- (1) di_F/dt - rate of change of current through zero crossing
- (2) I_{RRM} - peak reverse recovery current
- (3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through $0.75 I_{RRM}$ and $0.50 I_{RRM}$ extrapolated to zero current.
- (4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}
- $$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$
- (5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code	VS-	60	C	P	U	06	-N3
	1	2	3	4	5	6	7

- 1** - Vishay Semiconductors product
- 2** - Current rating (60 = 60 A)
- 3** - Circuit configuration:
C = common cathode
- 4** - Package:
P = TO-247AC (modified)
- 5** - U = ultrafast rectifier
- 6** - Voltage rating (06 = 600 V)
- 7** - Environmental digit:
-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)

PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-60CPU06-N3	25	500	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS

Dimensions	www.vishay.com/doc?96138
Part marking information	www.vishay.com/doc?95007
SPICE model	www.vishay.com/doc?96084



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