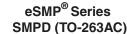


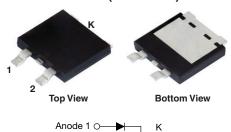
AUTOMOTIVE

ROHS

HALOGEN FREE

Ultrafast Rectifier, 2 x 8 A FRED Pt®





Cathode

LINKS TO ADDITIONAL RESOURCES

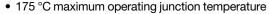
Anode 2 O-



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 8 A			
V_{R}	600 V			
V _F at I _F	0.94 V			
t _{rr}	45 ns			
T _J max.	175 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			

FEATURES

Ultrafast recovery time, reduced Q_{rr}, and soft recovery



• For PFC CRM, snubber operation

Low forward voltage drop

· Low leakage current

 Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

• AEC-Q101 qualified, meets JESD 201 class 2 whisker test

 Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop, ultrafast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, in the AC/DC section of SMPS, freewheeling and clamp diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage		V_{RRM}		600	V
Average rectified forward current	per device	- I _{F(AV)}	T _{solder pad} = 149 °C	16	
	per diode			8	Α
Non-repetitive peak surge current	per device	I _{FSM}	T _J = 25 °C, 6 ms square pulse	200	A
Non-repentive peak surge current	per diode			105	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	MBOL TEST CONDITIONS MIN. TYP. MA		MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-	
Forward voltage, per diode	V _F	I _F = 8 A	-	1.1	1.4	V
		I _F = 8 A, T _J = 150 °C	-	0.94	1.15	
Reverse leakage current, per diode	I _R	$V_R = V_R$ rated	-	-	5	
		T _J = 150 °C, V _R = V _R rated	-	20	150	μΑ
Junction capacitance, per diode	C _T	V _R = 600 V	-	8	-	pF



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A, dI_F/dt = 50 A$	/μs, V _R = 30 V	ı	45	ı	
B	+	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	-	60	no
Reverse recovery time	t _{rr}	T _J = 25 °C		-	70	-	ns
		T _J = 125 °C		-	100	-	
Dook receivery ourrent		T _J = 25 °C	I _F = 8 A, dI _F /dt = 500 A/μs,	-	12	-	Α
Peak recovery current I _{RRM}	T _J = 125 °C	$V_R = 400 \text{ V}$	-	17	-	^	
Deverse receivers shows	T _J = 25 °C		-	430	-	nC	
Reverse recovery charge	Q_{rr}	T _J = 125 °C		-	850	-	IIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	+175	°C
Thermal resistance, per diode junction to mount	R _{thJM}		-	1.8	2.5	°C/W
Approximate weight				0.55		g
Approximate weight				0.02		oz.
Marking device		Case style SMPD (TO-263AC)	16CDU06			

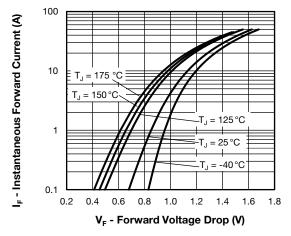


Fig. 1 - Typical Forward Voltage Drop Characteristics

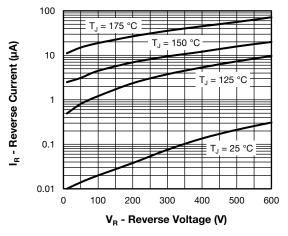


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

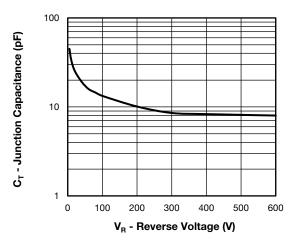


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

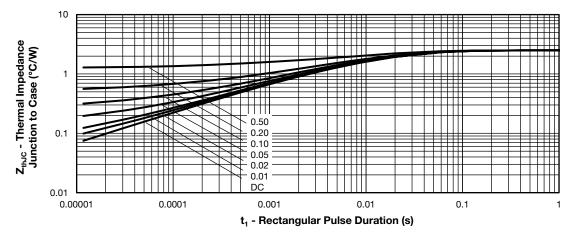


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

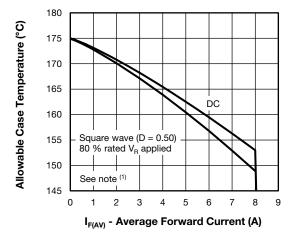


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

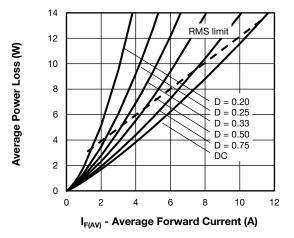


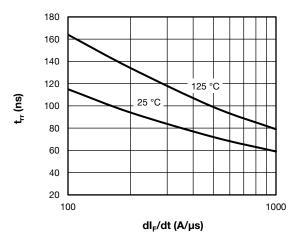
Fig. 6 - Forward Power Loss Characteristics

Note

Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 5); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R

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Vishay Semiconductors



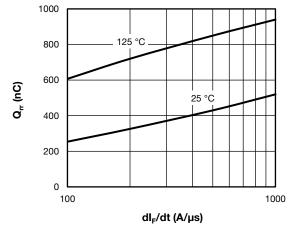
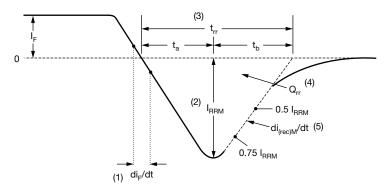


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

Fig. 8 - Typical Stored Charge vs. dl_E/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $t_{\rm rr}$ reverse recovery time measured from zero crossing point of negative going $I_{\rm F}$ to point where a line passing through 0.75 $I_{\rm RRM}$ and 0.50 $I_{\rm RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

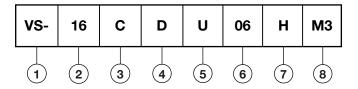
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (16 A)

Circuit configuration:

C = common cathode

4 - D = SMPD package

5 - Process type,

U = ultrafast recovery

6 - Voltage code (06 = 600 V)

7 - H = AEC-Q101 qualified

8 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

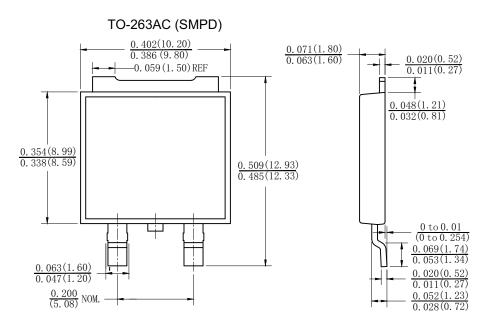
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-16CDU06HM3/I	2000	2000	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95604</u>				
Part marking information	www.vishay.com/doc?95566			
Packaging information	www.vishay.com/doc?88869			
SPICE model	www.vishay.com/doc?96575			

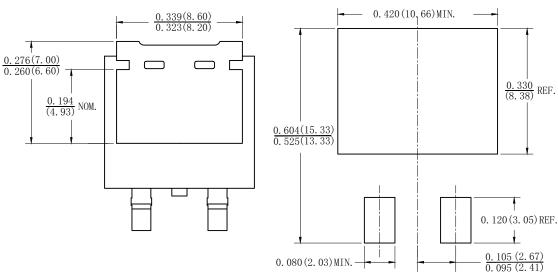


TO-263AC (SMPD)

DIMENSIONS in inches (millimeters)



Mounting Pad Layout





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Vishay

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