COMPLIANT



# 20 W Power Resistor, Thick Film Technology, TO-220



#### **DESIGN SUPPORT TOOLS**

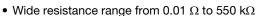
click logo to get started



The well known TO-220 package is compact and easy to mount.

#### **FEATURES**

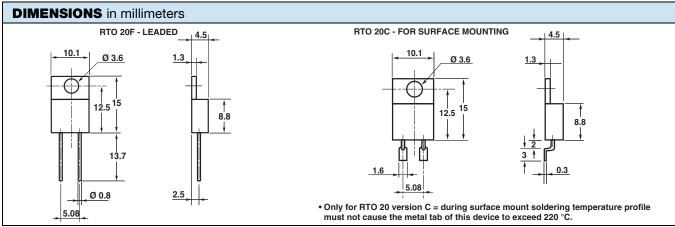
- 20 W at 25 °C heatsink mounted
- High power dissipation to size ratio



- Negligible inductance
- · Easy mounting
- TO-220 package: compact and easy to mount
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

Two versions of this thick film resistor are available:

- · A radial leaded version for PCB mounting
- A flat lead version for surface mounting



#### Note

Tolerances unless stated: ± 0.4 mm

STANDARD ELECTRICAL SPECIFICATIONS							
MODEL	SIZE	$\begin{array}{c} \textbf{RESISTANCE} \\ \textbf{RANGE} \\ \Omega \end{array}$	RATED POWER  P <sub>25 °C</sub> W	LIMITING ELEMENT VOLTAGE U <sub>L</sub> V	TOLERANCE ± %	TEMPERATURE COEFFICIENT ± ppm/°C	CRITICAL RESISTANCE $\Omega$
RTO 20	TO-220	0.010 to 550K <sup>(1)</sup>	20	500	1, 2, 5, 10	150	12.5K

#### Note

(1) E24 series

MECHANICAL SPECIFICATIONS				
Mechanical Protection	Insulated case			
Resistive Element	Thick film			
Substrate	Alumina onto base of nickel coated copper			
Connections	Tinned copper			
Weight	2.2 g max.			

ENVIRONMENTAL SPECIFICATIONS				
Temperature Range	-55 °C to 155 °C			
Climatic Category	55 / 155 / 56			
Sealing	Sealed container, solder immersion			
Flammability	IEC 60695-11-5 2 applications 30 s separated by 60 s			

Note

Revision: 16-Nov-17

Not compatible with RoHS reflow profile

TECHNICAL SPECIFICATIONS				
Dissipation and Associated	Onto a heatsink			
Thermal Resistance and Nominal Power	20 W at + 25 °C R <sub>TH (j - 0</sub> : 6.5 °C/W Free air: 2 W at +25 °C			
Dielectric Strength MIL STD 202	2000 V <sub>RMS</sub> - 1 min - 10 mA max. (between terminals and heatsink)			
Insulation Resistance	$\geq 10^6  \text{M}\Omega$			
Inductance	≤ 0.1 µH			

DIMENSIONS			
Standard Package	TO-220 insulated case		

Document Number: 50005

### Vishay Sfernice

PERFORMANCE					
TESTS	CONDITIONS	REQUIREMENTS			
Momentary Overload	EN 60115-1 2 Pr 5 s for $R < 2 \Omega$ 1.6 Pr 5 s for $R \ge 2 \Omega$ $U_S < 1.5 U_L$	± (0.25 % + 0.005 Ω)			
Rapid Temperature Change	EN 60115-1/60068-2-14 5 cycles -55 °C to +155 °C	$\pm (0.5 \% + 0.005 \Omega)$			
Load Life	EN 60115-1 1000 h Pr at +25 °C	± (1 % + 0.005 Ω)			
Humidity (Steady State)	EN 60115-1 56 days RH 95 %	$\pm (0.5 \% + 0.005 \Omega)$			
High Temperature Exposure	NF EN 140 000 1000 h - 40 % Pr at +100 °C	$\pm (0.5 \% + 0.005 \Omega)$			
Vibration	MIL STD 202, Method 204 C Test D	± (0.2 % + 0.005 Ω)			
Terminal Strength	MIL STD 202, Method 211 Test A1	± (0.2 % + 0.005 Ω)			
Shock	IEC 60115-1 IEC 60068-2-27 Saw tooth: 100 <i>g</i> /6 ms	$\pm (0.5 \% + 0.005 \Omega)$			

RESISTANCE VALUE IN RELATION TO TOLERANCE AND TCR						
Resistance Values	≥ 0.1	≥ 0.5				
Tolerances	± 1 % at ± 10 %					
Typical Temperature Coefficient Range (-55 °C to +155 °C)	± 900 ppm/°C	± 700 ppm/°C	± 250 ppm/°C	± 150 ppm/°C		

#### Note

· For very low ohmic values, TCR for information

#### **CHOICE OF THE HEATSINK**

The user must choose the board according to the working conditions of the component (power, room temperature). Maximum working temperature must not exceed 155 °C. The dissipated power is simply calculated by the following ratio:

$$P = \frac{\Delta T}{R_{TH (j-c)} + R_{TH (c-h)} + R_{TH (h-a)}}$$
(1)

P: Expressed in W

ΔT: Difference between maximum working temperature and room temperature

 $R_{TH (j-c)}$ : Thermal resistance value measured between resistive layer and outer side of the resistor. It is the thermal resistance of the component: Special Features table.

R<sub>TH (c - h)</sub>: Thermal resistance value measured between outer side of the resistor and upper side of the heatsink. This is the thermal resistance of the interface (grease, thermal pad), and the quality of the fastening device.

R<sub>th (h - a)</sub>: Thermal resistance of the heatsink.

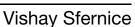
#### Example:

R<sub>TH (c - a)</sub> for RTO 20 power rating 10 W at ambient temperature +25 °C

Thermal resistance R<sub>TH (j - c)</sub>: 6.5 °C/W

Considering equation (1) we have:

$$\begin{split} &\Delta T = 155~^{\circ}C - 25~^{\circ}C = 130~^{\circ}C \\ &R_{TH~(j-c)} + R_{TH~(c-h)} + R_{TH~(h-a)} = \frac{\Delta T}{P} = \frac{130}{10} = 13~^{\circ}C/W \\ &R_{TH~(c-h)} + R_{TH~(h-a)} = 13~^{\circ}C/W - 6.5~^{\circ}C/W = 6.5~^{\circ}C/W \end{split}$$





#### **OVERLOADS**

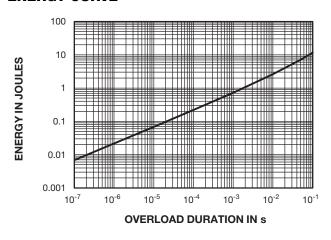
In any case the applied voltage must be lower than the maximum overload voltage of 750 V.

The values indicated on the graph below are applicable to resistors in air or mounted onto a heatsink.

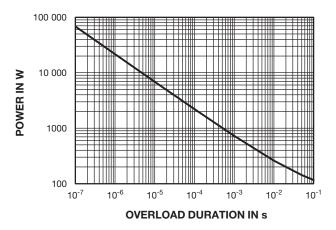
#### **MARKING**

Model, style, resistance value (in  $\Omega$ ), tolerance (in %), manufacturing date, Vishay Sfernice trademark.

#### **ENERGY CURVE**



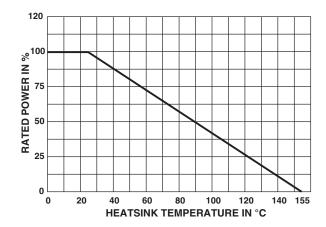
#### **POWER CURVE**



#### **POWER RATING**

The temperature of the heatsink should be maintained within the limits specified.

To improve the thermal conductivity, surfaces in contact should be coated with a silicone grease and the torque applied on the screw for tightening should be around 1 Nm. Spring clip can also be used to mount the component on an heatsink (ex: Kunze, clip KU4-498).



#### **PACKAGING**

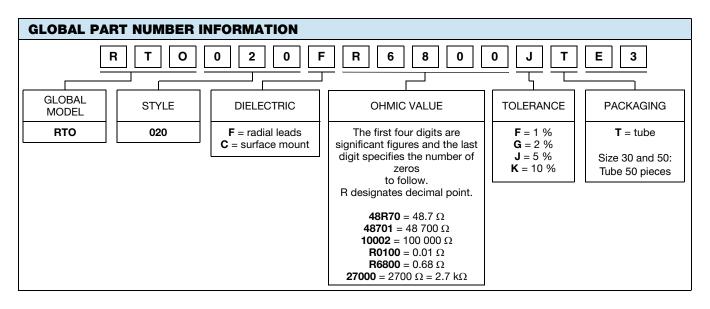
Tube of 50 units



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# Vishay Sfernice

ORDERING INFORMATION								
RTO	20	F	U68	5 %	XXX	TU50	e3	
MODEL	STYLE	CONNECTIONS	RESISTANCE VALUE	TOLERANCE	CUSTOM DESIGN	PACKAGING	LEAD (Pb)-FREE	
F: radial leads C: surface mount			± 1 % ± 2 % ± 5 % ± 10 %	Optional on request: special TCR, shape etc.				





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