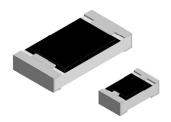


Thick Film Surface Mount Chip Resistors, Wraparound, Extremely Low Value (0.01 Ω to 0.976 Ω)



LINKS TO ADDITIONAL RESOURCES



FEATURES

- Extremely low resistance values $(0.01 \Omega \text{ to } 0.976 \Omega)$
- Sulfur resistant (per ASTM B809-95 humid vapor test)
- · Enhanced power rating due to long side terminal construction (0612, 1020 types)
- Suitable for current sensing and shunts
- Metal glaze on high quality ceramic
- Protective overglaze
- Lead (Pb)-free solder contacts on Ni barrier layer
- AEC-Q200 qualified
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





HALOGEN

FREE

GLOBAL MODEL	CASE SIZE	POWER RATING P _{70°C} W	TEMPERATURE COEFFICIENT + ppm/°C	$\begin{array}{c} \textbf{RESISTANCE} \\ \textbf{RANGE} \\ \Omega \end{array}$	TOLERANCE ± %	E-SERIES (2)	
			400	0.033 to 0.05	5.0	24	
RCWE0402 (3)(4)	0402	0.125	200	0.051 to 0.196	1.0, 5.0	24; 96	
			100	0.2 to 0.976	0.5 ⁽¹⁾ , 1.0, 5.0	24, 90	
RCWE0603 (4)			700	0.010 to 0.018	5.0	24	
	0603	0.2	400	0.02 to 0.0324	1.0, 5.0	24; 96	
HCVVEU0U3 (1)	0603		200	0.033 to 0.105	1.0, 5.0		
			100	0.11 to 0.976	0.5 ⁽¹⁾ , 1.0, 5.0		
			400	0.010 to 0.018	5.0	24	
RCWE0805 (4)	0805	0.25	300	0.02 to 0.0324	1.0, 5.0	24; 96	
RCWEU805 (4)	0805		200	0.033 to 0.05	1.0, 5.0		
			100	0.051 to 0.976	0.5 ⁽¹⁾ , 1.0, 5.0	1	
RCWE0612 (4)	0612	1.0	300	0.010 to 0.016	2.0, 5.0	0.4	
			200	0.018 to 0.2	2.0, 5.0	24	
			100	0.205 to 0.976	1.0, 5.0	24; 96	
	1206	0.5	600	0.010 to 0.018	5.0	24	
RCWE1206 (4)			300	0.02 to 0.0324	1.0, 5.0	24; 96	
			200	0.033 to 0.05	1.0, 5.0		
			100	0.051 to 0.976	0.5 ⁽¹⁾ , 1.0, 5.0		
RCWE1210 ⁽⁴⁾	1210	1.0	500	0.010 to 0.018	5.0	24	
			300	0.02 to 0.0324	1.0, 5.0	24; 96	
			200	0.033 to 0.05	1.0, 5.0		
			100	0.051 to 0.976	0.5 ⁽¹⁾ , 1.0, 5.0	1	
DOME 1000 (4)	1000	0.0	200	0.010 to 0.016	2.0, 5.0	24	
RCWE1020 (4)	1020	2.0	100	0.0162 to 0.976	1.0, 5.0	24; 96	
RCWE2010 ⁽⁴⁾			600	0.010 to 0.018	5.0	24	
	2010	1.0	300	0.02 to 0.0324	1.0, 5.0	24; 96	
			200	0.033 to 0.05	1.0, 5.0		
			100	0.051 to 0.976	0.5 ⁽¹⁾ , 1.0, 5.0	7	
RCWE2512 ⁽⁴⁾			600	0.010 to 0.018	5.0	24	
	0510		300	0.02 to 0.0324	1.0, 5.0		
	2512	2.0	200	0.033 to 0.05	1.0, 5.0	24; 96	
			100	0.051 to 0.976	0.5 ⁽¹⁾ , 1.0, 5.0		

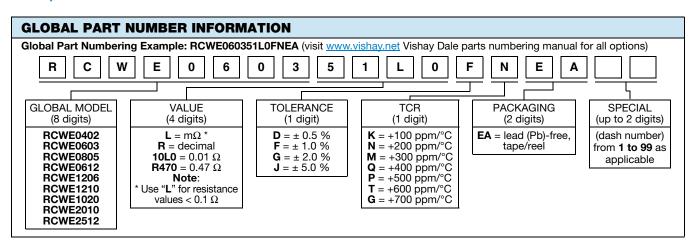
Notes

- Power rating depends on the max. temperature at the solder point, the component placement density and the substrate material Part marking: Reference "Surface Mount Resistor Marking" (www.vishay.com/doc?20020)
 Temperature range of TCR rating is 0 °C to 150 °C. TCR values are (+) range only with no (-) range values; 1/2 of previous tolerance range Tight tolerance of 0.5 % is available for resistance values above 0.300 Ω (0402 size) and above 0.200 Ω (0603 to 2512 sizes)
 Use E24 decades only for 5.0 % tolerance. E24 or E96 decades are available for 0.5 % and 1.0 % tolerance. Refer to standard decade table (www.vishay.com/dod
- Terminal strength tested per AEC-Q200-006 with the exception of 0.75 kg force is used

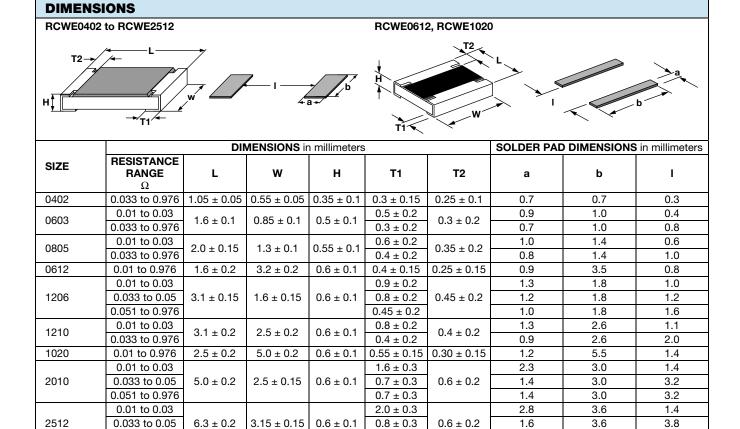
Qualified to AEC-Q200 rev. D

Revision: 24-Oct-2023





TECHNICAL SPECIFICATIONS										
PARAMETER	UNIT	0402	0603	0805	0612	1206	1210	1020	2010	2512
Operating temperature range	°C	-55 to +155								
Maximum operating voltage	V	$(P \times R)^{1/2}$								
Insulation voltage U _{ins} (1 min)	V	> 75	> 100	> 200	> 100	> 300	> 300	> 300	> 300	> 300
Insulation resistance	Ω					> 10 ⁹				
Weight/1000 pieces (typical)	g	0.7	3	5.5	11.5	10.5	17.5	27.5	26	40.5



Notes

Revision: 24-Oct-2023

• 3D models available: www.vishav.com/doc?31106

0.051 to 0.976

Surface mount solder profile recommendations: www.vishay.com/doc?31052

1.6

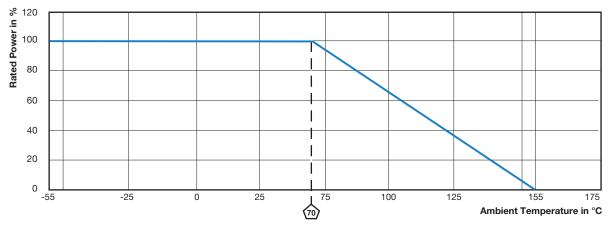
3.6

3.8

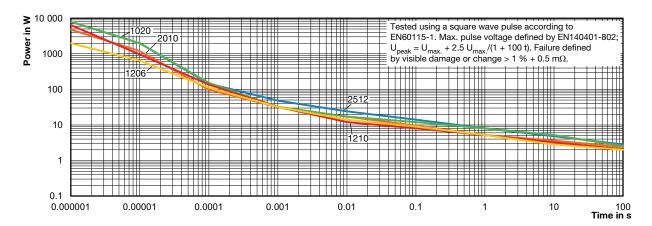
 0.8 ± 0.3

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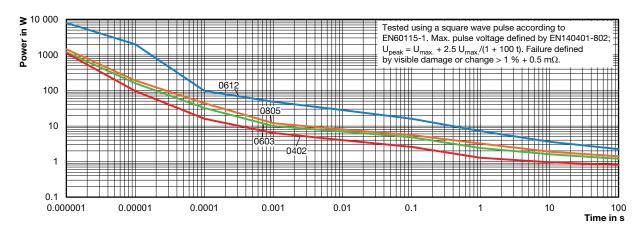
DERATING



SINGLE PULSE



SINGLE PULSE



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PERFORMANCE					
TEST	CONDITIONS OF TEST	TEST LIMITS			
Thermal shock	MIL-STD-202, method 107, -55 °C to +125 °C, 300 cycles at each extreme	$\pm 1.0 \% + 0.0005 \Omega$			
Short time overload	2 x rated power; size and duration - 0402: 0.5 s, 0603 and 0805: 1 s, 1206 and larger: 2 s	\pm 0.5 % + 0.0005 Ω			
High temperature exposure	MIL-STD-202, method 108, 1000 h at T = 125 °C, 0 % power	± 2.0 % + 0.0005 Ω			
Temperature cycling	JESD 22, method JA-104, 1000 cycles (-55 °C to +125 °C)	± 2.0 % + 0.0005 Ω			
Biased humidity	MIL-STD-202, method 103, 1000 h 85 °C / 85 % RH, 10 % x ($P \times R$) ^{1/2}	± 2.0 % + 0.0005 Ω			
Mechanical shock	MIL-STD-202, method 213, condition C, 10 g's, 6 ms (half sine), 3 directions	± 1.0 % + 0.0005 Ω			
Vibration	MIL-STD-202, method 204, 5 g's, 20 min, 12 cycles, 3 directions, 10 Hz to 2000 Hz	± 1.0 % + 0.0005 Ω			
Operational life	MIL-STD-202, method 108, 1000 h at T = 125 °C at rated power	$\pm~2.0~\%~+~0.0005~\Omega$			
Resistance to solder heat	MIL-STD-202, method 210, +260 °C solder, 10 s to 12 s dwell, 25 mm/s emergence	\pm 1.0 % + 0.0005 Ω			
Moisture resistance	MIL-STD-202, method 106, 0 % power, 7a and 7b not required	$\pm~2.0~\%~+~0.0005~\Omega$			

Note

 Contact <u>ww2bresistors@vishay.com</u> for application specific performance requirements or qualification data. Typical performance is better than stated test limits

PACKAGING								
MODEL	REEL							
	TAPE WIDTH	DIAMETER	PITCH	PIECES/REEL	CODE			
RCWE0402	8 mm / punched paper	180 mm / 7"	2 mm	10 000	EA			
RCWE0603	8 mm / punched paper	180 mm / 7"	4 mm	5000	EA			
RCWE0805	8 mm / punched paper	180 mm / 7"	4 mm	5000	EA			
RCWE0612	8 mm / punched paper	180 mm / 7"	4 mm	5000	EA			
RCWE1206	8 mm / punched paper	180 mm / 7"	4 mm	5000	EA			
RCWE1210	8 mm / punched paper	180 mm / 7"	4 mm	5000	EA			
RCWE1020	12 mm / embossed plastic	180 mm / 7"	4 mm	4000	EA			
RCWE2010	12 mm / embossed plastic	180 mm / 7"	4 mm	4000	EA			
RCWE2512	12 mm / embossed plastic	180 mm / 7"	8 mm	2000	EA			

Notes

- Embossed carrier tape per EIA-481-1A
- Additional packaging details at: www.vishay.com/doc?31543

LINKS TO RELATED DOCUMENTS					
SELECTOR GUIDE					
Overview of Automotive Grade Products	www.vishay.com/doc?49924				
TECHNICAL NOTES					
SMD Current Sense: AEC-Q200 vs. Vishay Qualification	www.vishay.com/doc?30416				
MIL-PRF vs. AEC-Q200: Do You Know What You Are Getting?	www.vishay.com/doc?11000				
WHITE PAPER					
Thermal Management for Surface-Mount Devices	www.vishay.com/doc?30380				
Temperature Coefficient of Resistance for Current Sensing	www.vishay.com/doc?30405				



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